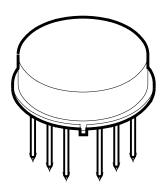
## TTM Technologies

### RAD HARD MEDIUM POWER OP-AMP

# 0041RH

#### FEATURES:

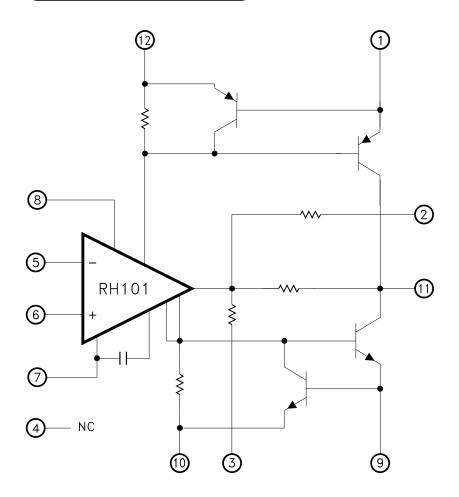
- Total Dose Hardened to 100 Krads(Si) (Method 1019.7 Condition A)
- Output Current 0.5 Amps Peak
- · Low Power Consumption-Class C Design
- Programmable Current Limit
- · Continuous Output Short Circuit Duration
- Rad Hard LH0041Replacement
- Functionally Equivalent Non Rad Hard Device MSK0041



#### DESCRIPTION:

The MSK0041RH is a general purpose Class C power operational amplifier. This amplifier offers high output currents, making it an excellent choice for motor drive circuits. It is the industry wide RAD HARD replacement for the LH0041. The amplifier and load can be protected from fault conditions through the use of internal current limit circuitry that can be user programmed with two external resistors. This device is also compensated with a single external capacitor. The MSK0041RH is available in a hermetically sealed 12 pin TO-8 package.

#### **EQUIVALENT SCHEMATIC**



#### TYPICAL APPLICATIONS

- · Servo Amplifer
- · Motor Driver
- Audio Amplifier
- Programmable Power Supply

#### PIN-OUT INFORMATION

- 1 ISC+
- 2 Compensation
- 3 GND
- 4 NC
- 5 -Input
- 6 +Input
- 7 Balance
- 8 Balance
- 9 ISC-
- 10 -VCC
- 11 Output
- 12 +VCC

CASE = ISOLATED

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#### **ABSOLUTE MAXIMUM RATINGS**

(8)

±Vcc	Supply Voltage±22V	Tst
Iout	Peak Output Current	TLD
VIN	Differential Input Voltage ±30A	
VIN	Common Mode Input Voltage ±15V	TJ
		т.

Tst	Storage Temperature Range	-65°C to +150°C
TLD	Lead Temperature Range	
	(10 Seconds)	300°C
TJ	Junction Temperature	
Tc	Case Operating Temperature Range	
	MSK0041K/HRH	-55°C to +125°C
	MSK00/1RH	-40°C to +85°C

#### **ELECTRICAL SPECIFICATIONS**

Parameter	Test Conditions (1) (9)		Group A	MSK0041K/HRH (6)			MSK0041RH 5			Units
	rest conditions (1) (9)			Min.	Тур.	Max.	Min.	Тур.	Max.	Units
STATIC										
Supply Voltage Range 3		-	±5	±15	±18	±5	±15	±18	V	
Quiescent Current	$V_{IN} = 0V$		1, 2, 3	-	±1.0	±3.5	-	±1.0	±4.0	mA
Power Consumption 3	V <sub>IN</sub> = 0V		1, 2, 3	-	75	105	-	90	120	mW
INPUT										
I + O#+ \/- + ()	\\(\tau = 0\\\)		1	-	±0.5	±3.0	-	±0.5	±6.0	mV
Input Offset Voltage ②	VIN = 0V		2, 3	-	±2.0	±5.0	-	-	-	μV/°C
	VcM = 0V		1	-	±100	±300	-	±150	±500	nA
Input Bias Current (2)	Either Input		2, 3	-	±0.4	±1.0	-	-	-	μΑ
		Post Radiation	1	-	±300	±500	-	±300	±600	nA
L	V 0V		1	-	±2.0	±100	-	±2.0	±200	nA
Input Offset Current (2)	VCM = UV	Vcm = 0V		-	-	±300	-	-	-	nA
Input Capacitance 4	F = DC		-	-	3	-	-	3	-	pF
Input Resistance (3)	F = DC		-	0.3	1.0	-	0.3	1.0	-	ΜΩ
0 4 5 6 7 7 7	ion Ratio F = 10Hz V <sub>CM</sub> = ±10V		4	70	90	-	70	90	-	dB
Common Mode Rejection Ratio			5, 6	70	90	-	-	-	-	dB
Davier Cumply Dejection Detic	VCC = ±5V to ±15V		1	80	95	-	80	95	-	dB
Power Supply Rejection Ratio			2, 3	80	-	-	-	-	-	dB
Input Noise Voltage (4)	F = 10Hz to 10KHz		-	-	5	-	-	5	-	µVкмs
OUTPUT										
0 + 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	RL = 100Ω F = 100Hz		4	±13	±14	-	±13	±14	-	V
Output Voltage Swing			5, 6	±13	±14	-	-	-	-	V
Output Short Circuit Current	Rsc = 3.3Ω Vout = M	AX	4	182	220	300	180	220	300	mA
TRANSFER CHARACTERISTICS										
0	VOUT = ±10V RL = 10	0V RL = 100Ω		1.2	3.0	-	1.0	3.0	-	V/µS
Slew Rate		Post Radiation	4	1.1	-	-	0.9	-	-	v/µS
0 1 1/1 0:	5 4011 5 4140		4	100	105	-	100	105	-	dB
Open Loop Voltage Gain	$F = 10Hz$ $RL = 1K\Omega$		5, 6	88	96	-	-	-	-	dB
	Vout = 1V Rise and F	all	4	-	0.3	1.0	-	0.3	1.5	μS
Transition Times		Post Radiation	4	-	-	1.2	-	-	1.6	μS
Overshoot	Small Signal		4	-	5	20	-	5	30	%
Thermal Resistance ③	Junction to Case @ 125 Each Output Device	°C	-	-	85	90	-	85	90	°C/W

#### NOTES:

- ① Unless otherwise specified, ±VCC = ±15V, Cc = 3000pF.
- ② Specification applies for ±5V ≤ ±VCC ≤ ±18V.
- 3 Guaranteed by design but not tested.
- 4 Typical parameters are representative of actual device performance but are for reference only.
- (5) Industrial grade devices shall be tested to subgroups 1 and 4 unless otherwise specified.
- 6 Military grade devices (H suffix) shall be 100% tested to subgroups 1, 2, 3 and 4.

Subgroup 1, 4  $TA = TC = +25^{\circ}C$ 

Subgroup 2, 5  $TA = TC = +125^{\circ}C$ 

Subgroup 3, 6 TA =  $Tc = -55^{\circ}C$ 

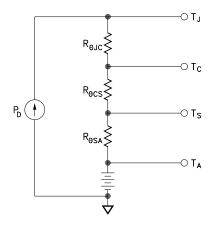
- Subgroup 5 and 6 testing available upon request.
- 8 Continuous operation at or above absolute maximum ratings may adversely effect the device performance and/or life cycle.
- Pre and post irradiation limits at +25°C, are identical up to 100Krad TID unless otherwise specified.

#### **APPLICATION NOTES**

#### HEAT SINKING

To select the correct heat sink for your application, refer to the thermal model and governing equation below.

#### Thermal Model:



#### Governing Equation:

 $T_J = P_D x (R_{\theta JC} + R_{\theta CS} + R_{\theta SA}) + T_A$ 

Where

TJ = Junction Temperature
PD = Total Power Dissipation

 $R_{\theta JC}$  = Junction to Case Thermal Resistance  $R_{\theta CS}$  = Case to Heat Sink Thermal Resistance  $R_{\theta SA}$  = Heat Sink to Ambient Thermal Resistance

Tc = Case Temperature
TA = Ambient Temperature
Ts = Sink Temperature

#### Example:

In our example the amplifier application requires the output to drive a 10 volt peak sine wave across a 100 ohm load for 0.1 amp of output current. For a worst case analysis we will treat the 0.1 amp peak output current as a D.C. output current. The power supplies are  $\pm 15$  VDC.

1.) Find Power Dissipation

PD = [(quiescent current) X (+VCC - (VCC))] + [(Vs - Vo) X IOUT] =  $(3.5 \text{ mA}) \times (30.0) + (50.0) \times (0.4A)$ 

= (3.5 mA) X (30 V) + (5 V) X (0.1 A)

= 0.1W + 0.5W

= 0.6W

- 2.) For conservative design, set T<sub>J</sub> = +150°C.
- 3.) For this example, worst case TA = +25°C.
- 4.) ReJC = 85°C/W
- 5.) Rearrange governing equation to solve for Resa:

 $R_{\theta}SA = (T_J - T_A) / P_D - (R_{\theta}J_C) - (R_{\theta}C_S)$ 

 $= (150^{\circ}\text{C} - 25^{\circ}\text{C}) / 0.6\text{W} - (85^{\circ}\text{C/W}) - (0.15^{\circ}\text{C/W})$ 

= 123°C/W

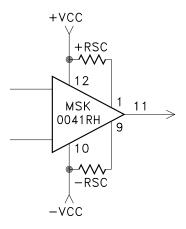
The heat sink in this example must have a thermal resistance of no more than 123°C/W to maintain a junction temperature of less than +150°C. This calculation assumes a case to sink thermal resistance of 0.15°C/W.

#### **CURRENT LIMIT**

The MSK0041RH has an on-board current limit scheme designed to limit the output drivers anytime output current exceeds a predetermined limit. The following formula may be used to determine the value of the current limit resistance necessary to establish the desired current limit.

$$Rsc = \frac{0.7}{Isc}$$

#### **Current Limit Connection**



See "Application Circuits" in this data sheet for additional information on current limit connections.

#### POWER SUPPLY BYPASSING

Both the negative and the positive power supplies must be effectively decoupled with a high and low frequency bypass circuit to avoid power supply induced oscillation. An effective decoupling scheme consists of a 0.1 microfarad ceramic capacitor in parallel with a 4.7 microfarad tantalum capacitor from each power supply pin to ground. This capacitor will eliminate any peak output voltage clipping which may occur due to poor power supply load regulation. All power supply decoupling capacitors should be placed as close to the package power supply pins as possible.

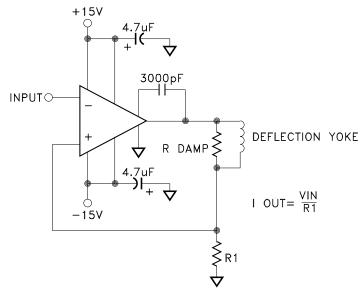
#### SAFE OPERATING AREA

The safe operating area curve is a graphical representation of the power handling capability of the amplifier under various conditions. The wire bond current carrying capability, transistor junction temperature and secondary breakdown limitations are all incorporated into the safe operating area curves. All applications should be checked against the curves to ensure high M.T.B.F.

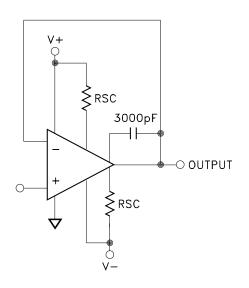
#### TOTAL DOSE RADIATION TEST PERFORMANCE

Radiation performance curves for TID testing have been generated for all radiation testing performed by TTM Technologies. These curves show performance trends throughout the TID test process and can be located in the MSK0041RH radiation test report. The complete radiation test report is available in the RAD HARD PRODUCTS section on the TTM Technologies website.

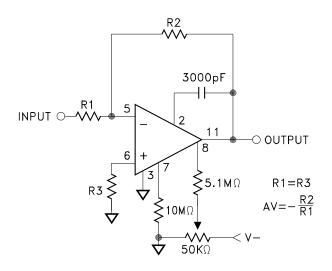
#### **APPLICATION CIRCUITS**



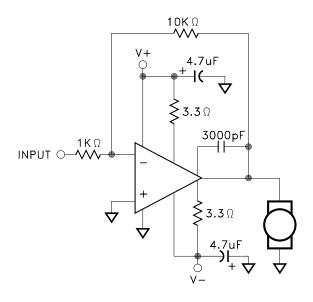
CRT DEFLECTION YOKE DRIVER



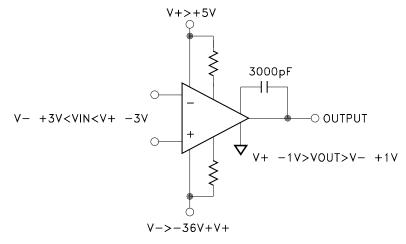
UNITY GAIN CIRCUIT WITH SHORT CIRCUIT LIMITING



OFFSET VOLTAGE NULL CIRCUIT

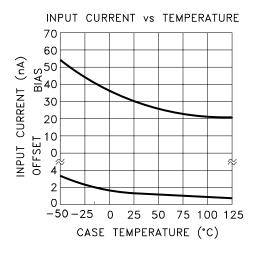


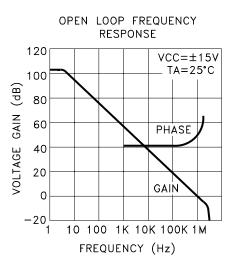
DC SERVO AMPLIFIER

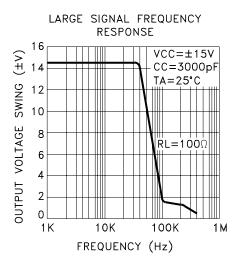


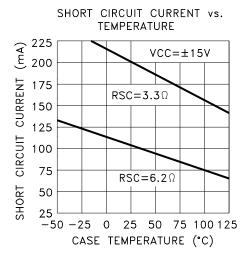
NON SYMMETRICAL SUPPLIES

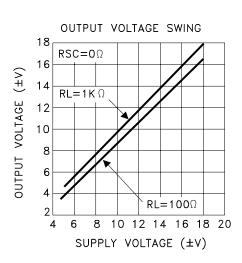
#### TYPICAL PERFORMANCE CURVES

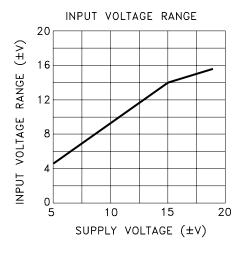


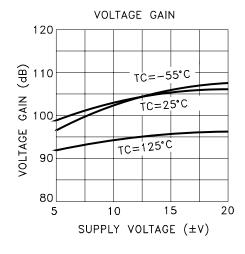


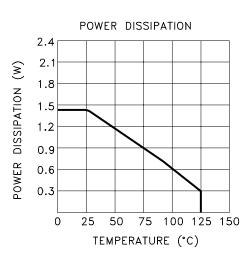


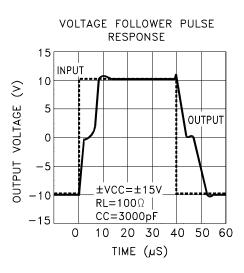


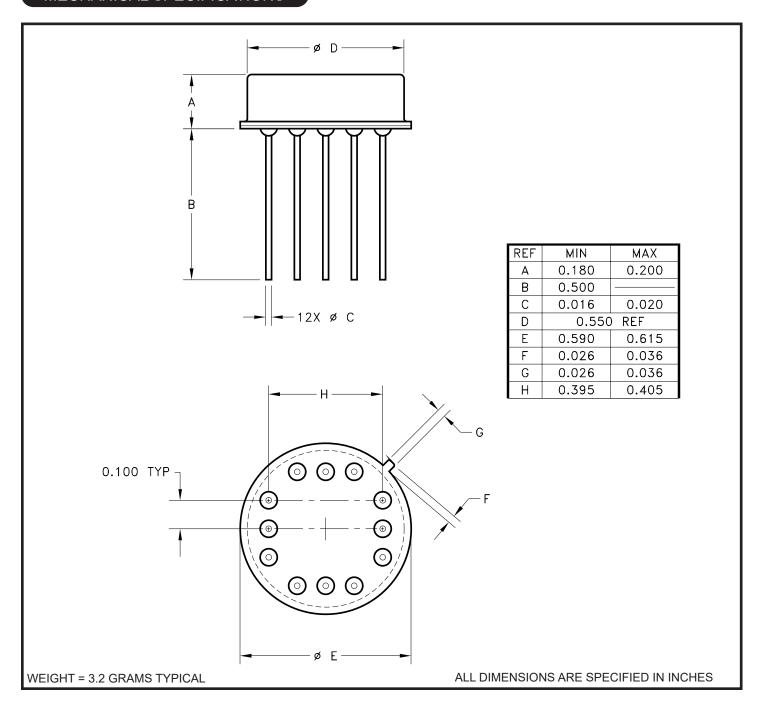












#### **ORDERING INFORMATION**

Part Number	Screening Level
MSK0041RH	Industrial
MSK0041HRH	MIL-PRF-38534 CLASS H
MSK0041KRH	MIL-PRF-38534 CLASS K

6

8548-65 Rev. H 9/21

#### **REVISION HISTORY**

REV	STATUS	DATE	DESCRIPTION
F	Released	07/15	Update format. Correct thermal calculation example.
G	Released	03/18	Update E dimension from 0.595 min - 0.605 max to 0.590 min - 0.615 max
Н	Released	09/21	Remove MIL-PRF-38535

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