

Total Dose Radiation Test Report

MSK4304RH

3 - Phase Motor Drive Hybrid

June 14, 2007 (1st test)

November 6, 2013 (2nd Test: IC Wafer Lot: DJA6ENC
Transistor Wafer Lot: BA1016MFA #16)

June 27, 2018 (3rd Test: IC Wafer Lot: G3W8EDA Wf#3
Transistor Wafer Lot: DA03104MSA Wf#5)

N. Kresse
J. Joy

Anaren, Inc - MSK Products

I. Introduction:

The Total Ionizing Dose radiation test plan for the MSK4304RH was developed to qualify the devices as RAD Hard to 300Krad(Si). The testing was performed beyond 300Krad(Si) to show trends in the device performance as a function of total dose. The test does not classify maximum radiation tolerance of the device, but simply offers designers insight to the critical parameter-shifts up to the specified total dose level.

MIL-STD-883 Method 1019.7 and ASTM F1892-06 were used as guidelines in the development and implementation of the total dose test plan for the MSK4304RH.

II. Radiation Source:

Total ionizing dose testing was performed at the University of Massachusetts, Lowell, using a cobalt 60 radiation source. The dose rate was determined to be 119.2Rad(Si)/sec. The total dose schedule can be found in Table I.

III. Test Setup:

All test samples were subjected to Group A Electrical Test in accordance with the device data sheet. In addition, all devices received a minimum of 160 hours of burn-in per MIL-STD-883 Method 1015 and were fully screened IAW MIL-PRF-38535 Class H. For test platform verification, one control device was tested at 25°C. Eight devices were then tested at 25°C, prior to irradiation, and were found to be within acceptable test limits.

The devices were vertically aligned with the radiation source and enclosed in a lead/aluminum container during irradiation. Four devices were kept under bias during irradiation. Four devices had all leads grounded during irradiation for the unbiased condition.

After each irradiation, the device leads were shorted together and the devices were transported to the electrical test platform. Testing was performed in accordance with the MSK device data sheet. Testing was performed on irradiated devices, as well as the control devices, at each total dose level. Electrical tests were completed within one hour of irradiation. Devices were subjected to subsequent radiation doses within two hours of removal from the radiation field.

IV. Data:

All performance curves are averaged from the test results of the biased and unbiased devices respectively. Post 300Krad(Si) limits have also been plotted for reference. If required, full test data can be obtained by contacting Anaren, Inc – MSK Products.

V. Summary:

Based on the test data recorded during radiation testing and statistical analysis, the MSK4304RH qualifies as a 300Krad(Si) radiation hardened device. All performance curves stayed well within specifications up to the maximum test dose, 450Krad(Si) TID.

MSK4304RH Biased/Unbiased Dose Rate Schedule

Dosimetry Equipment
Bruker Biospin # 0162

Irradiation Date
6/27/18

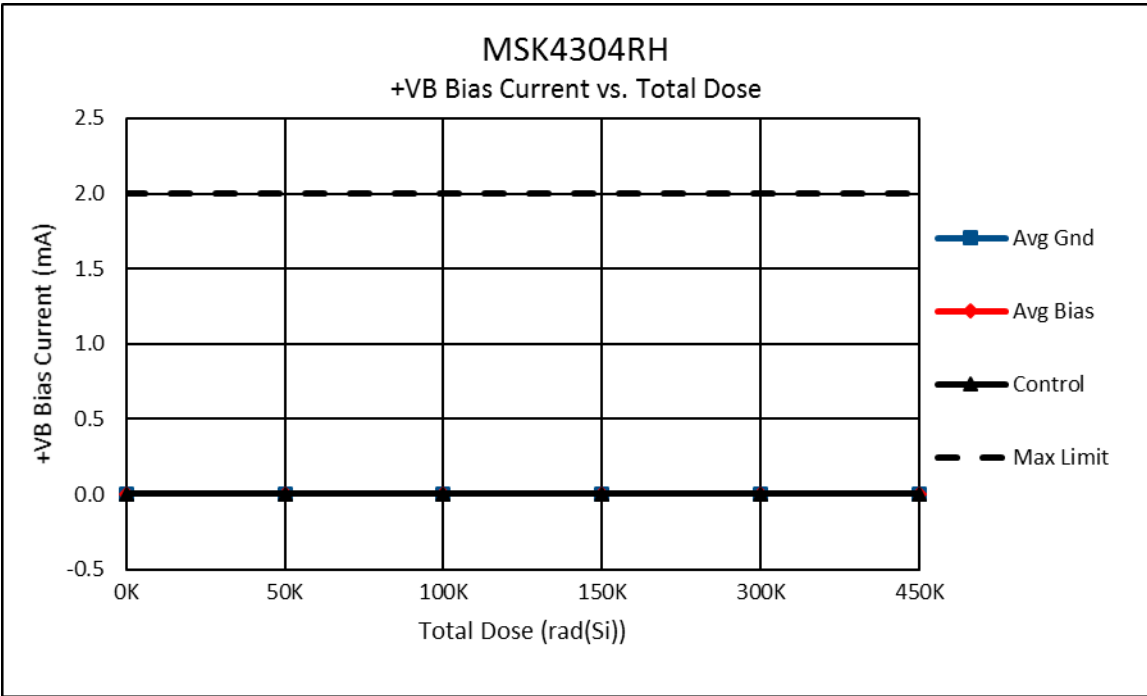
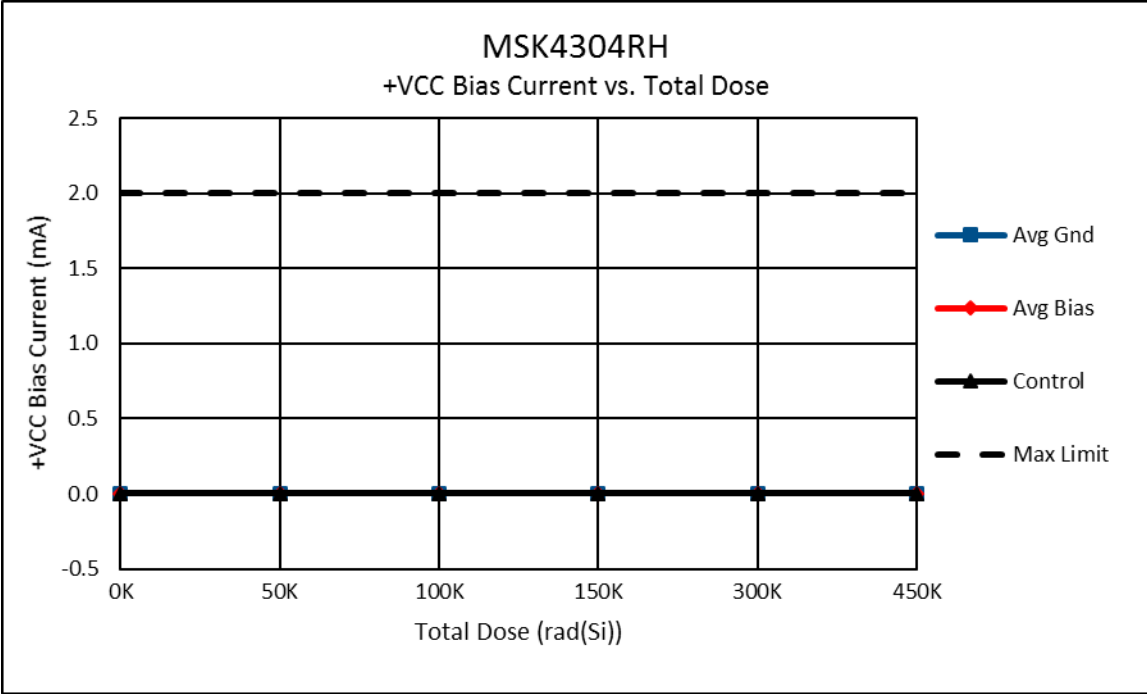
Exposure Length (min:sec)	Incremental Dose rad(Si)	Cumulative Dose rad(Si)
7:12	51,500	51,500
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7:12	51,500	154,500
21:36	154,500	309,000
21:36	154,500	463,500

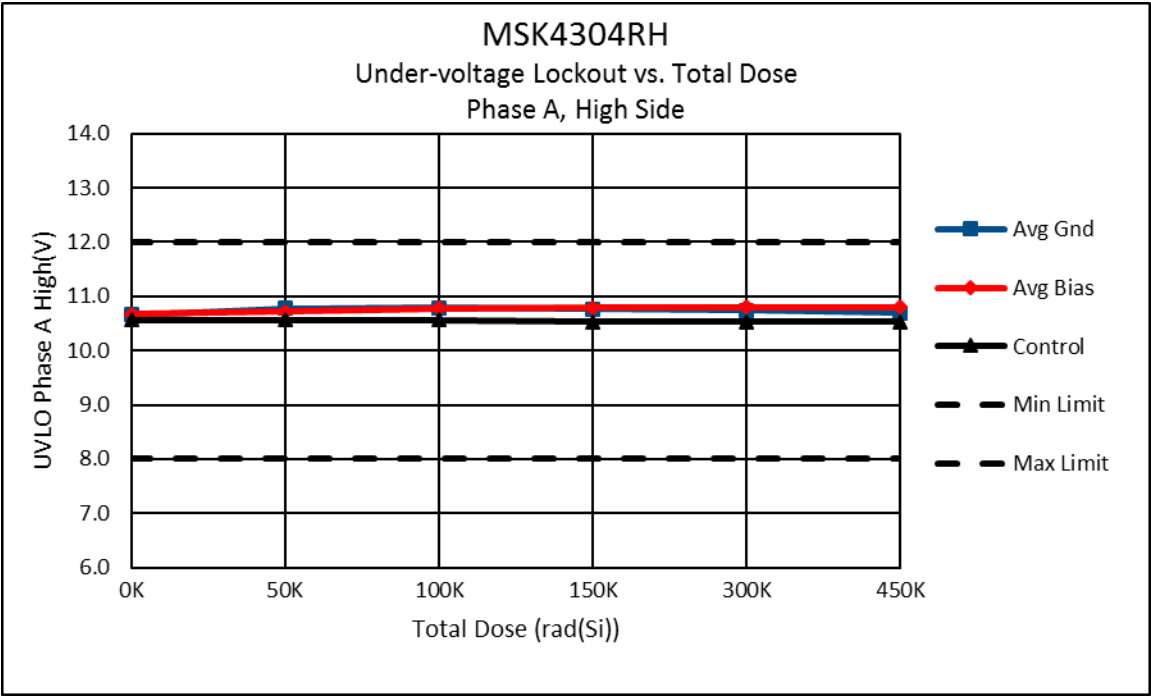
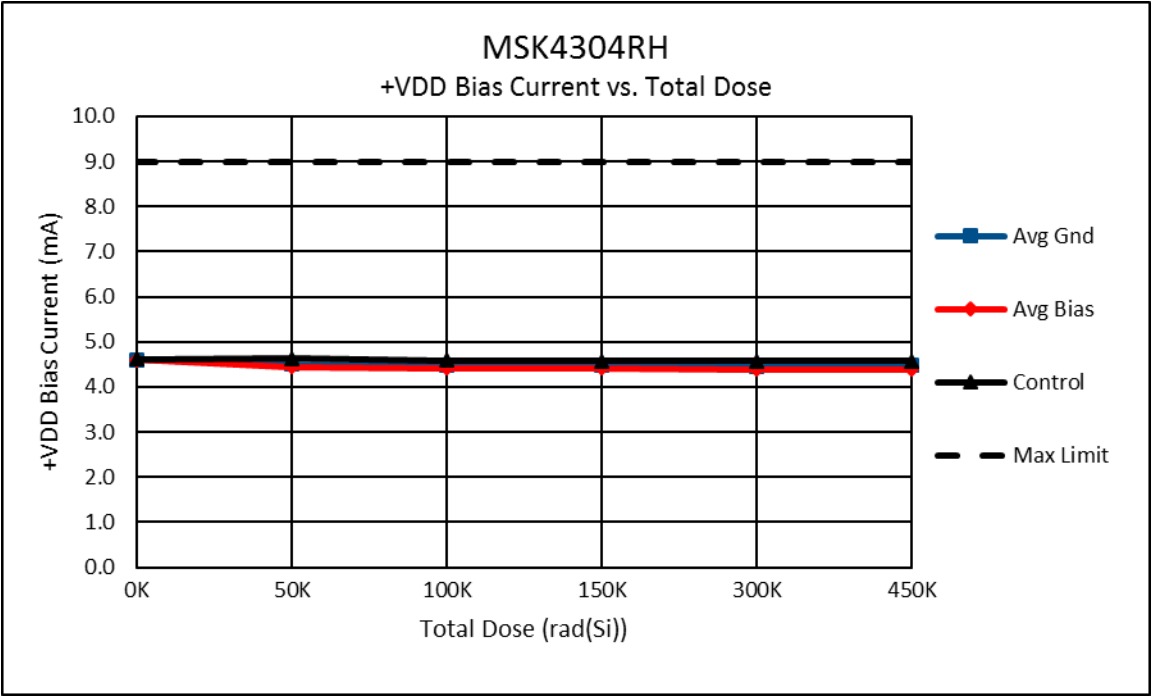
Biased S/N – 0254, 0255, 0256, 0260

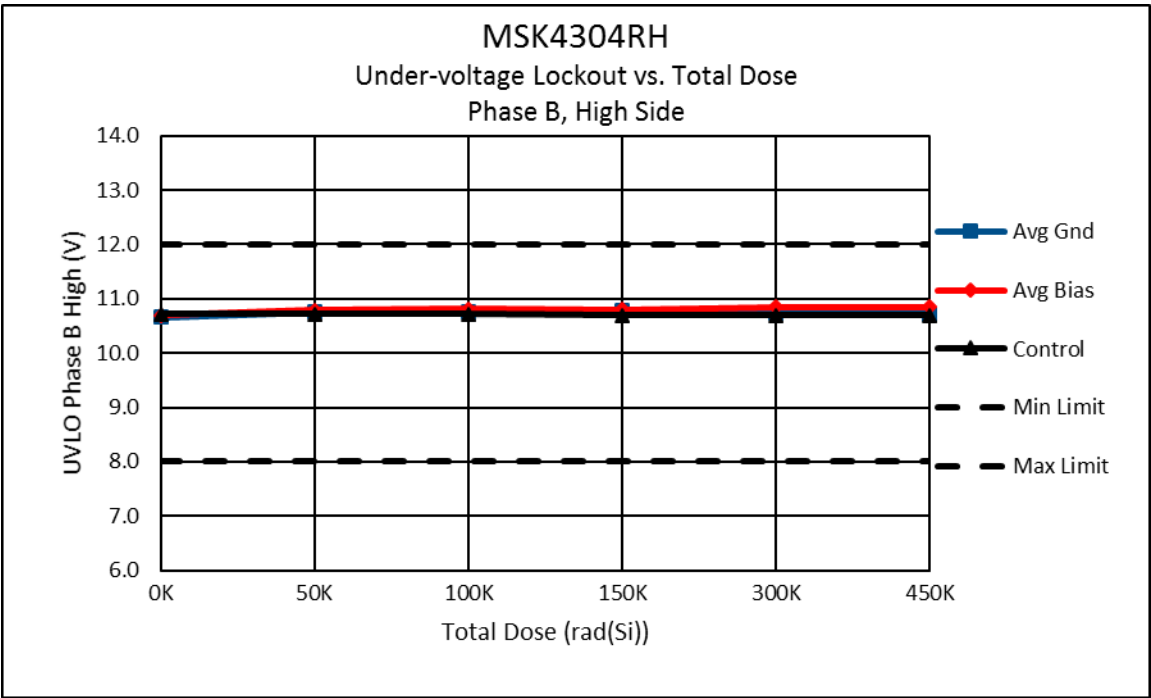
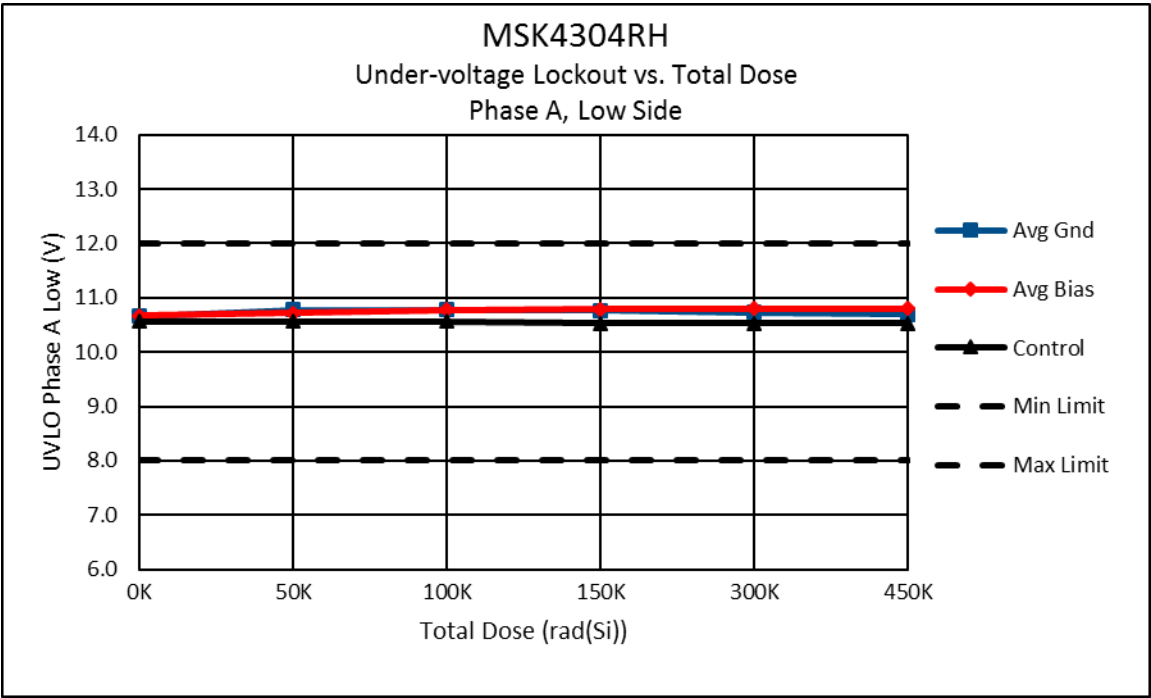
Unbiased S/N – 0261, 0262, 0263, 0264

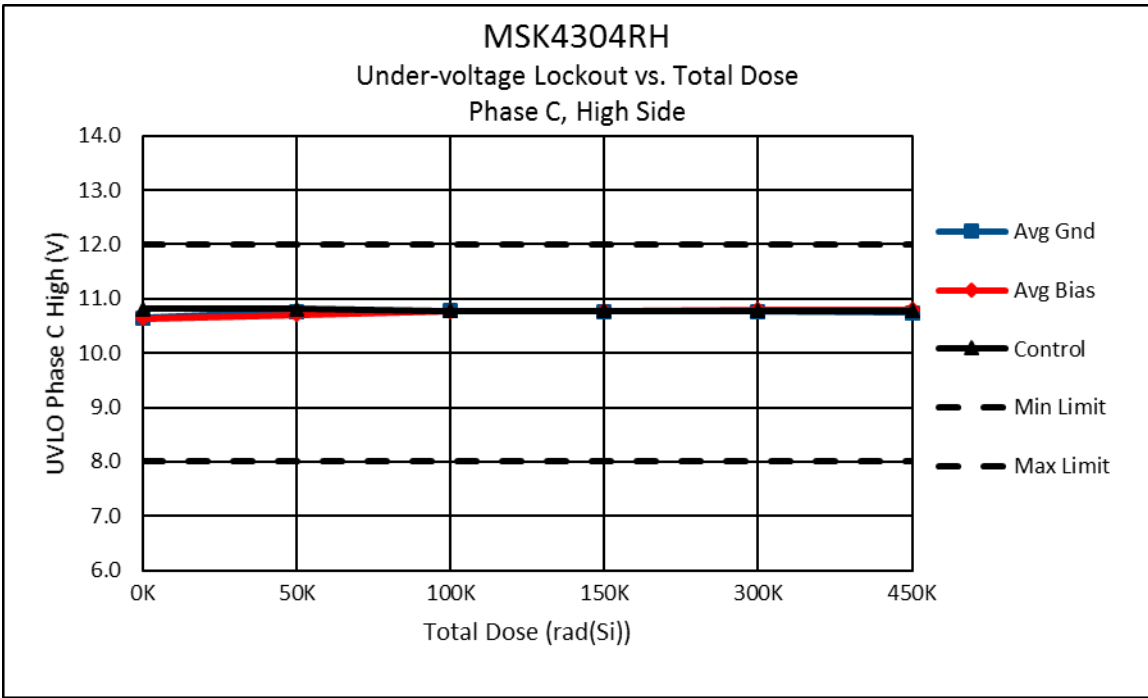
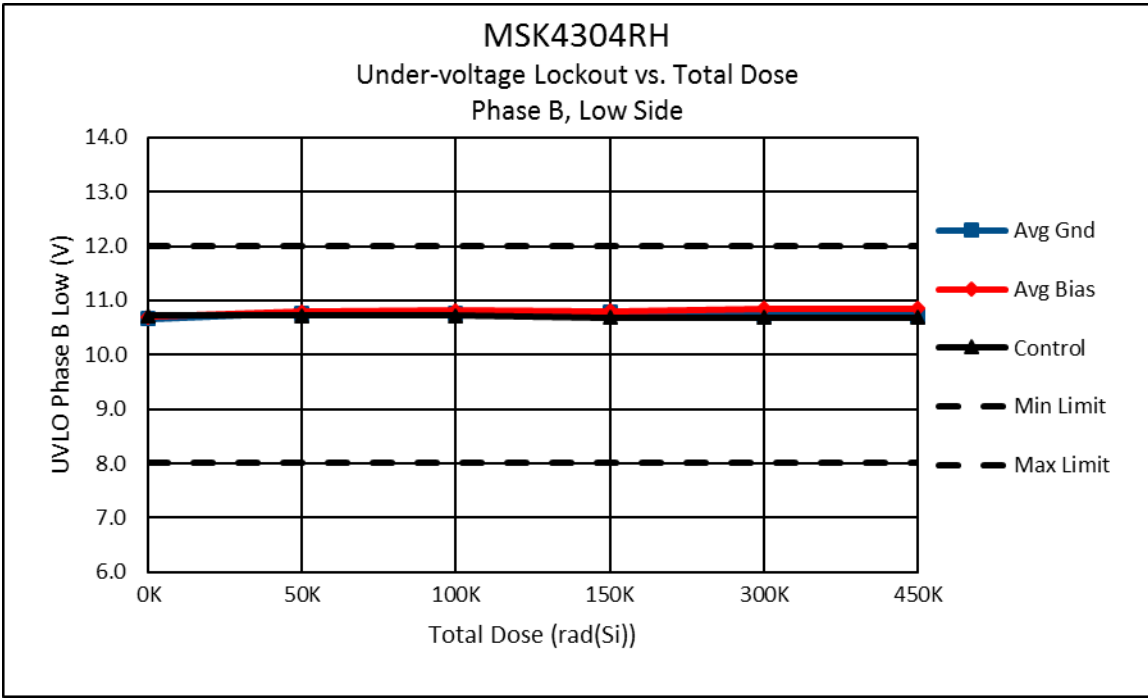
Table 1

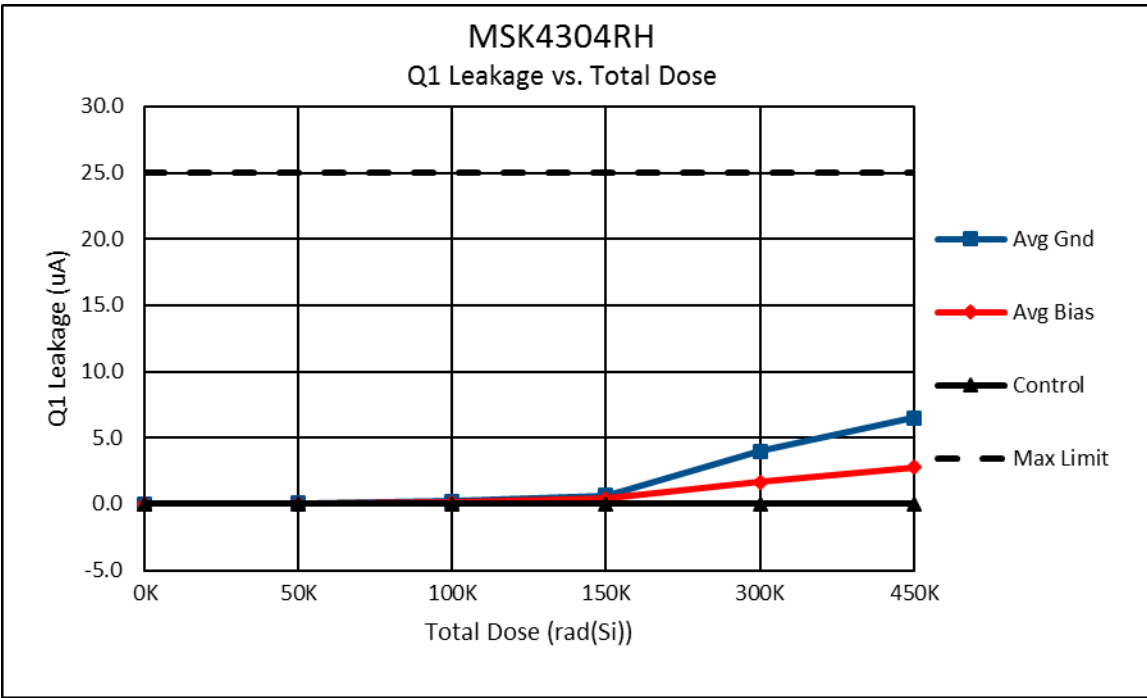
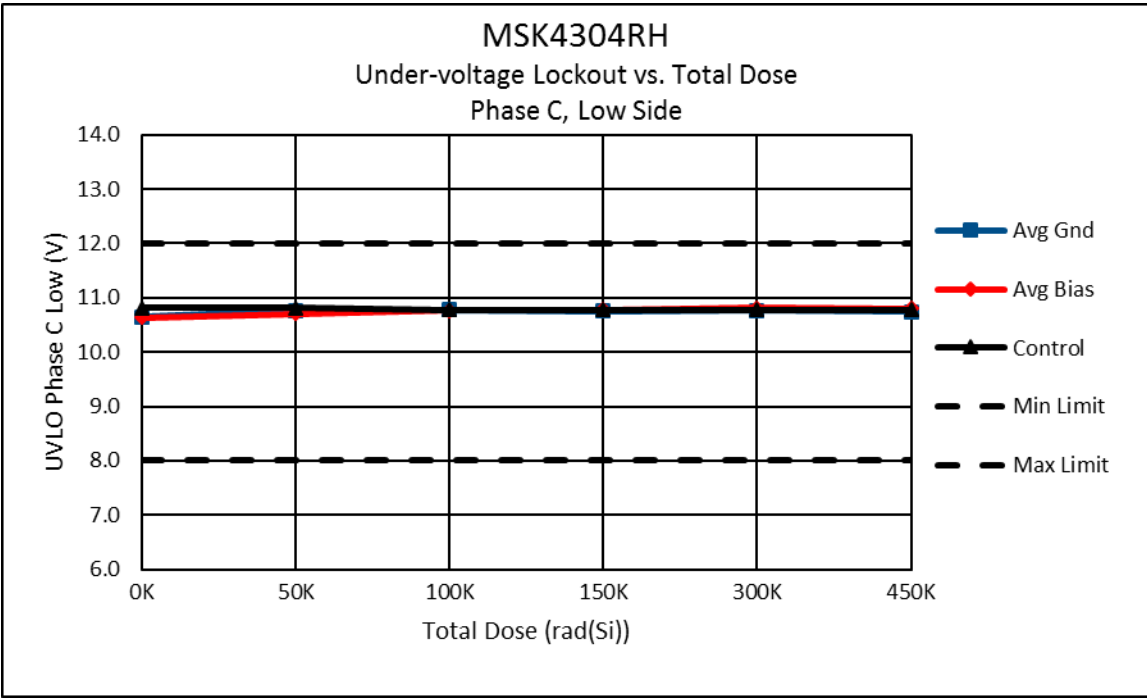
Dose Time, Incremental Dose and Total Cumulative Dose

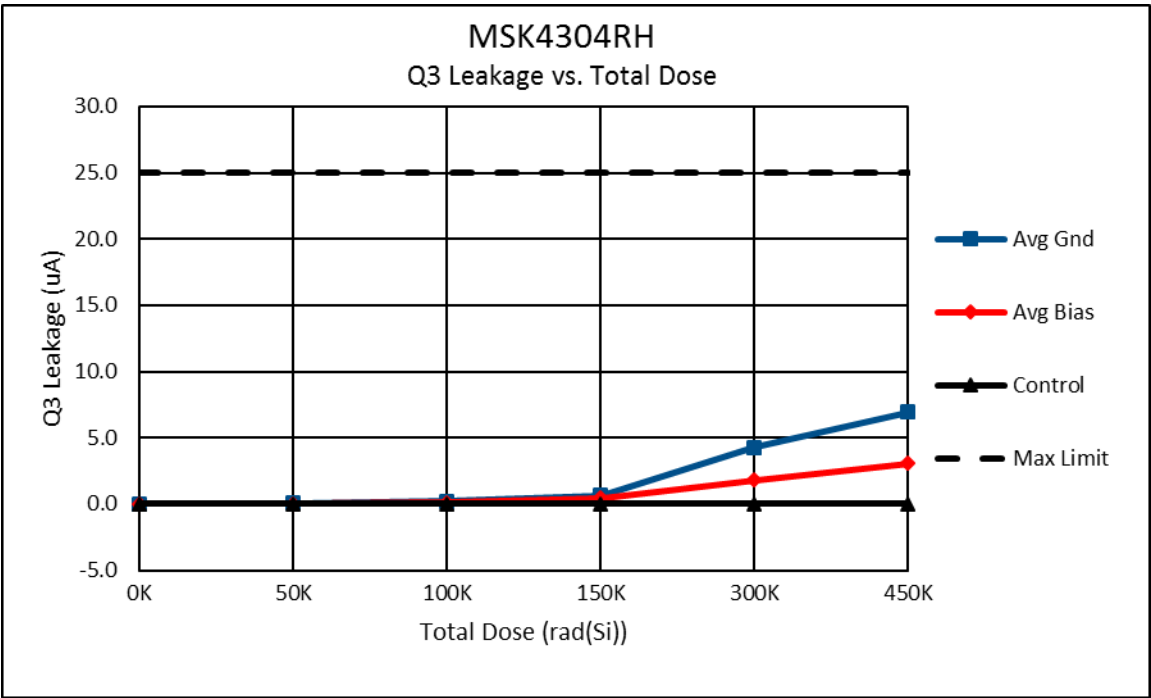
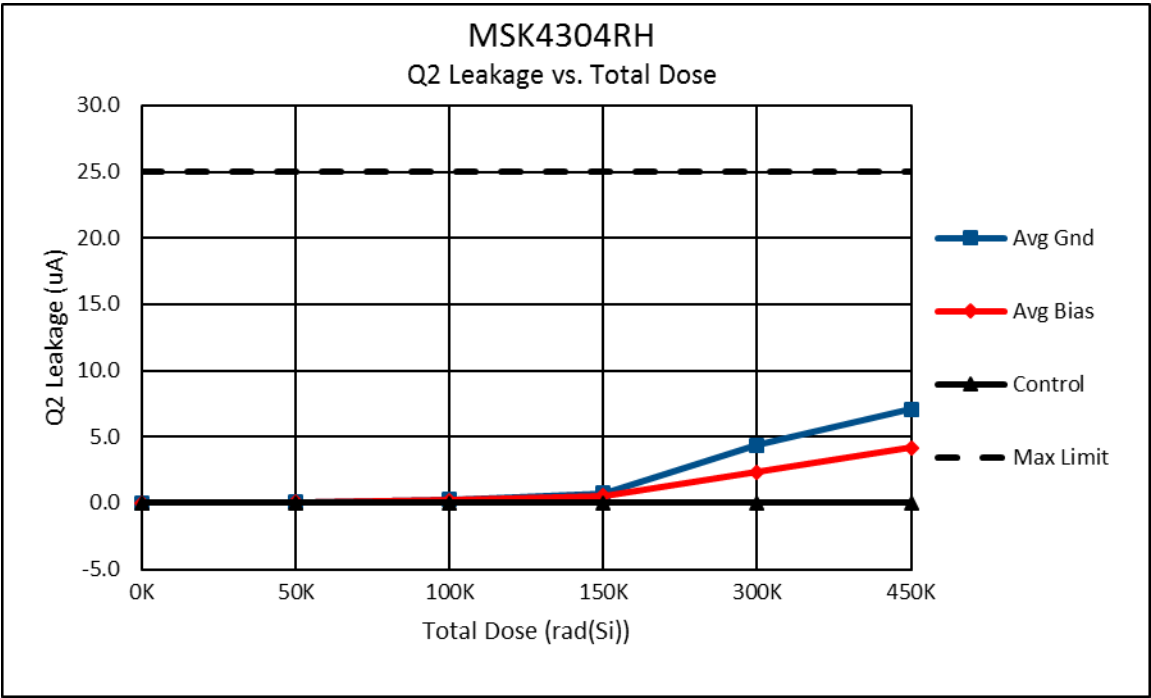


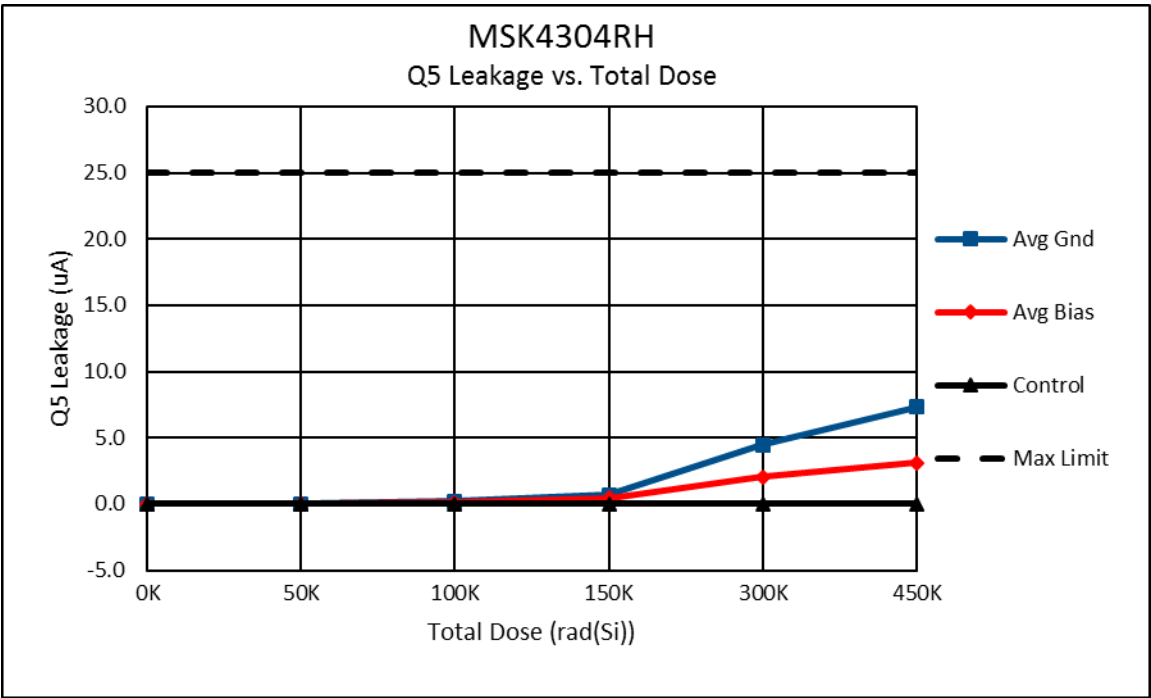
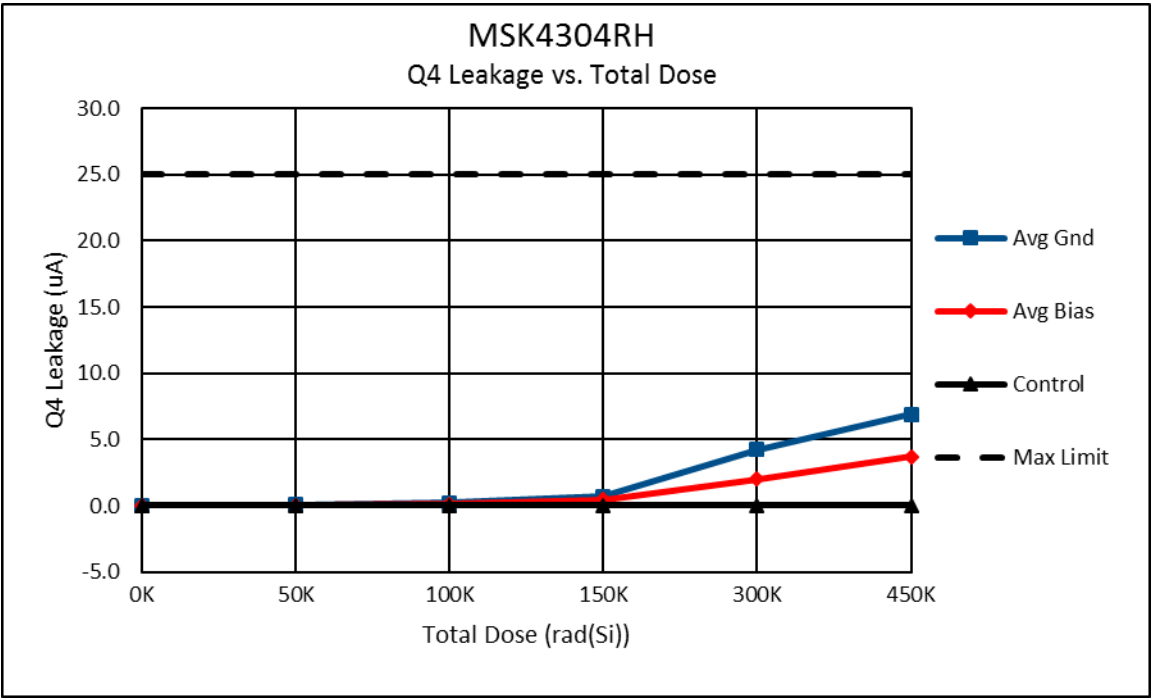


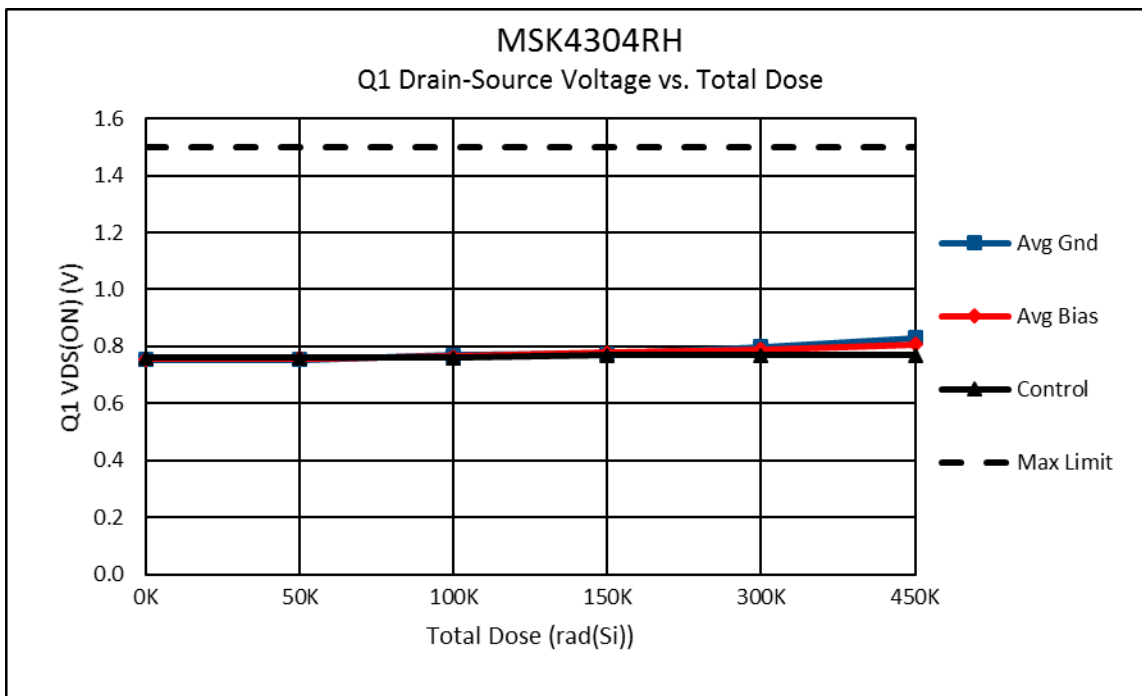
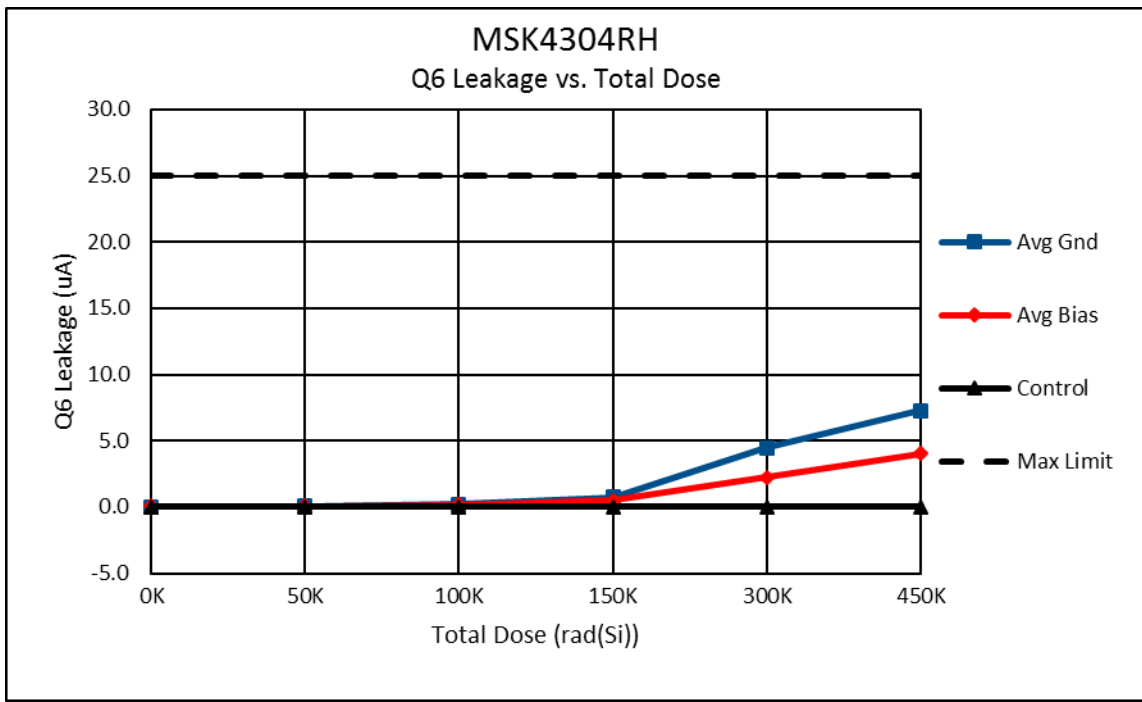


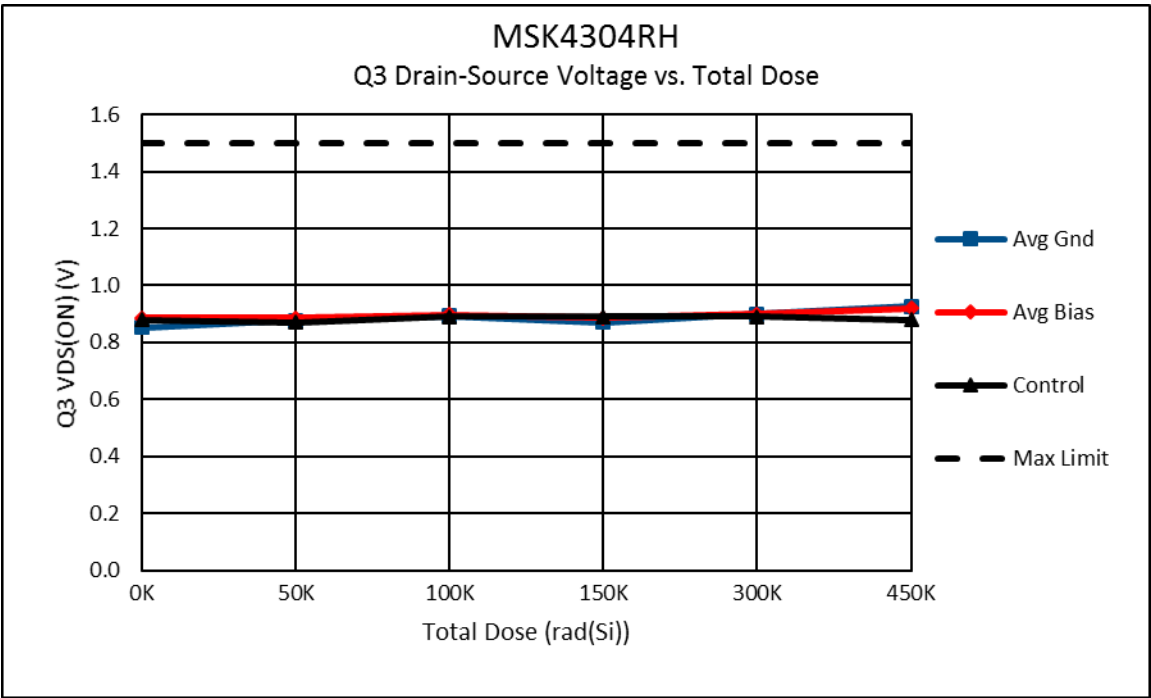
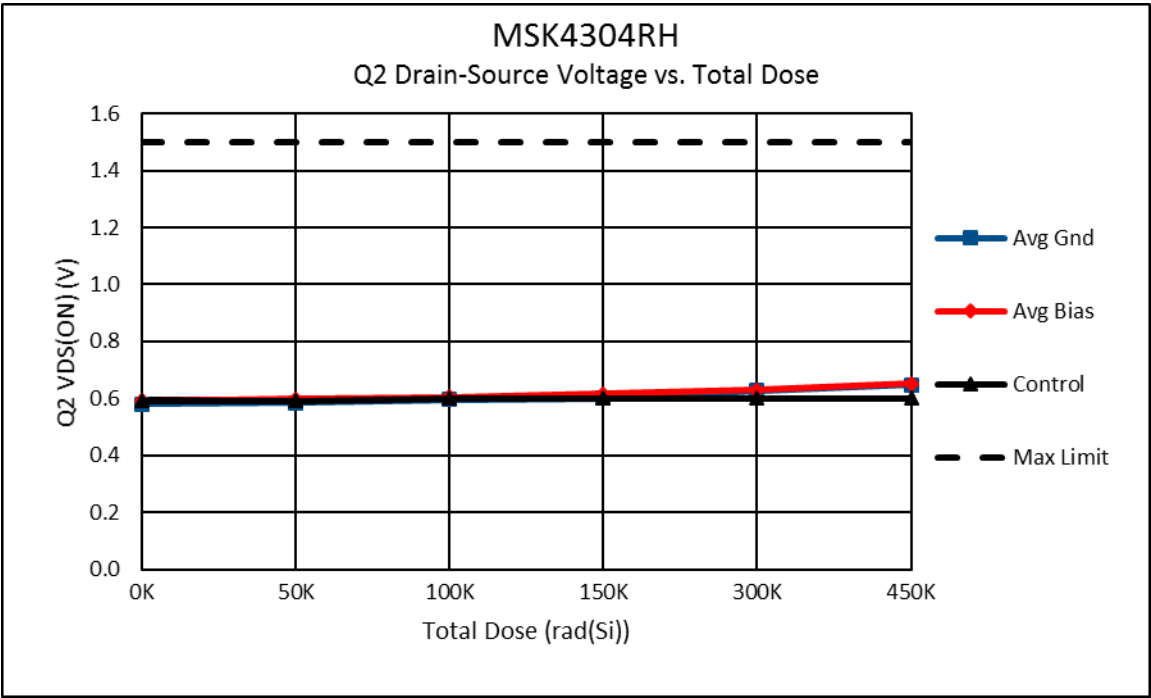


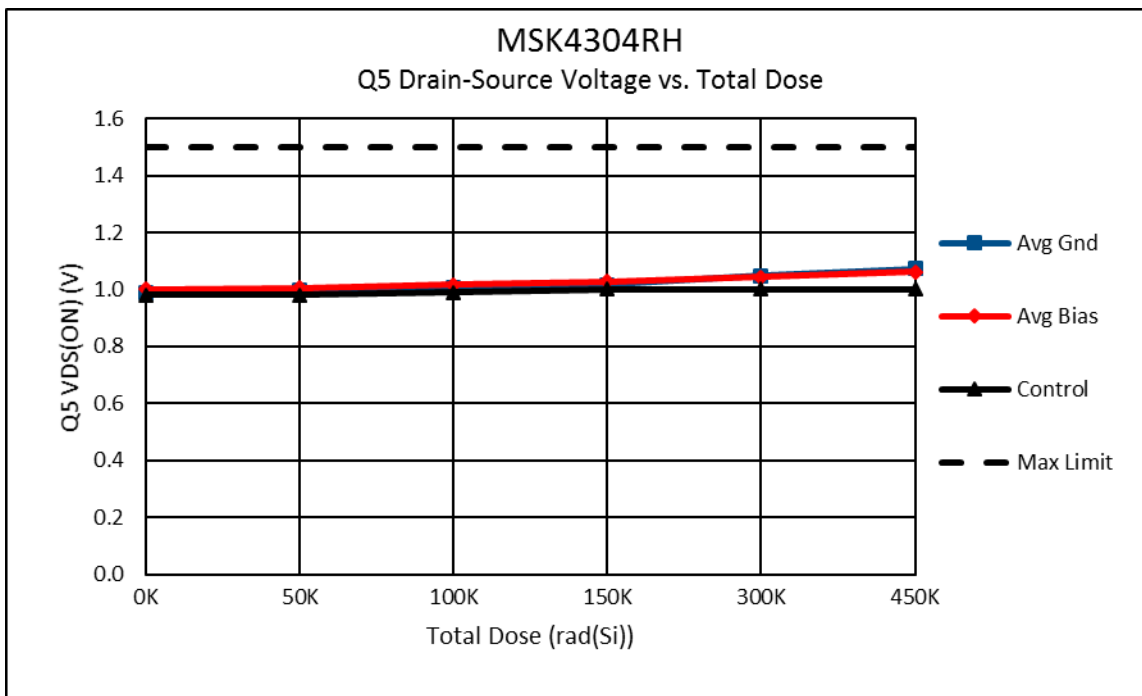
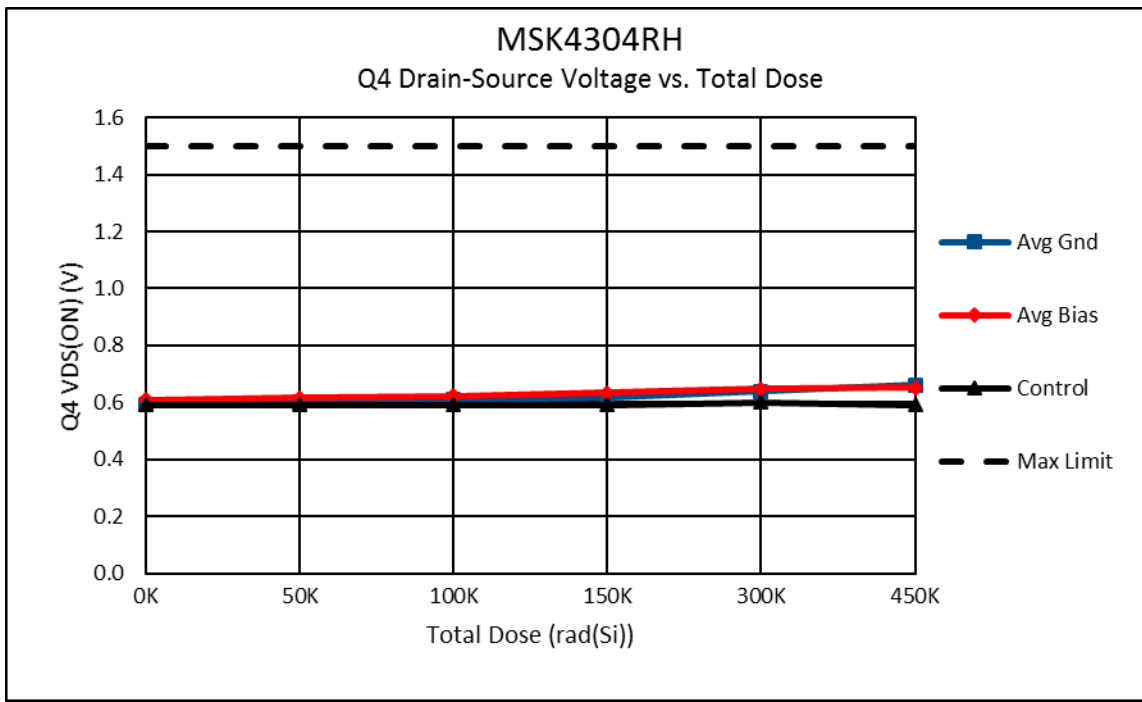


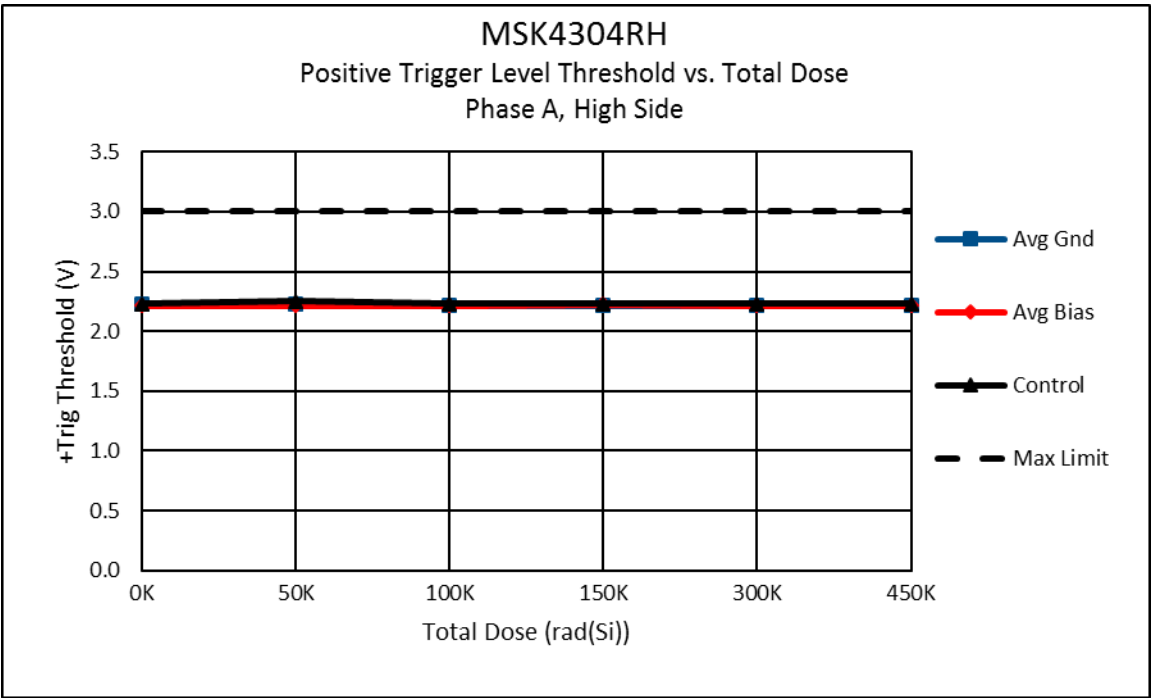
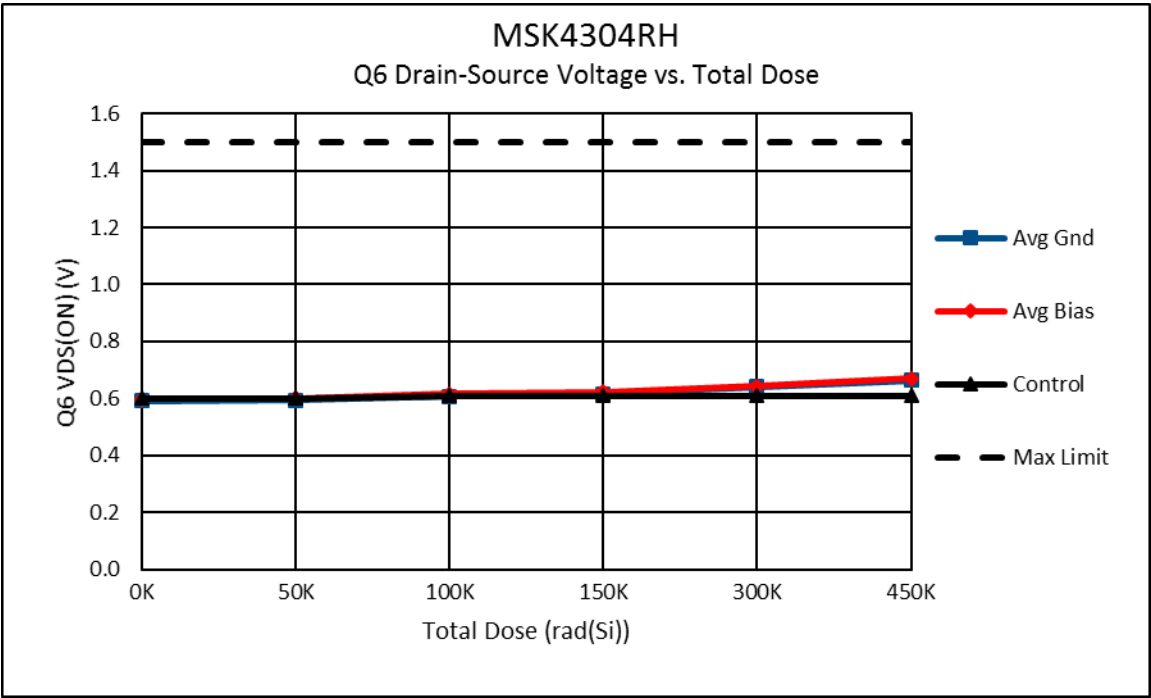


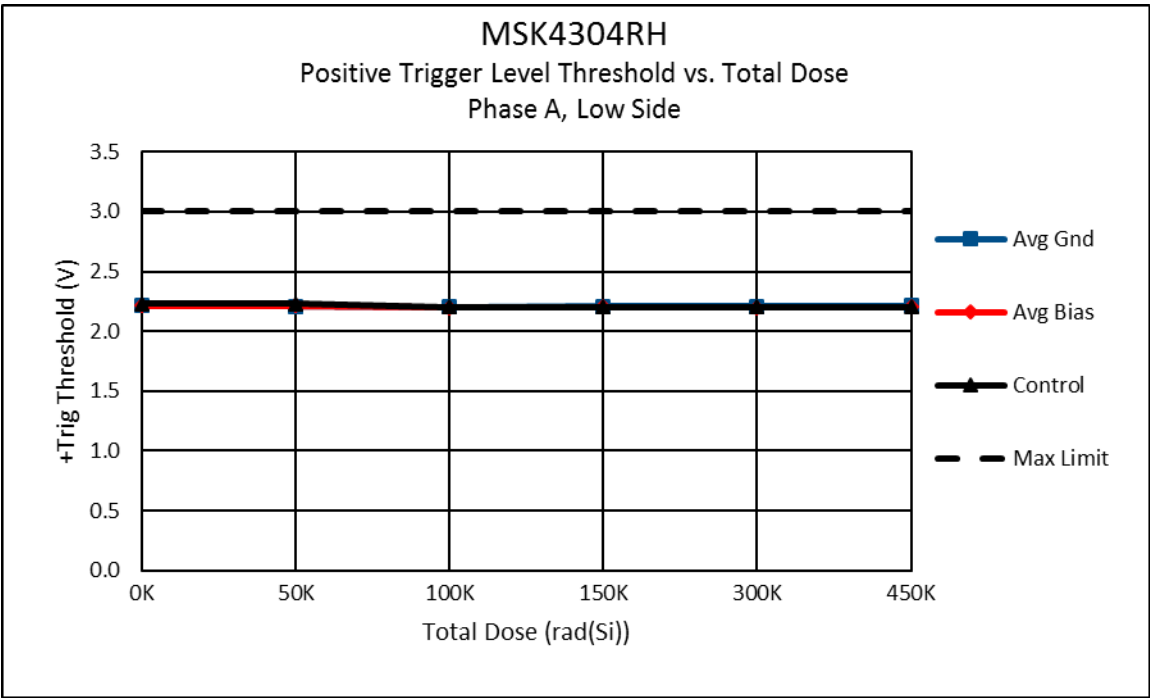
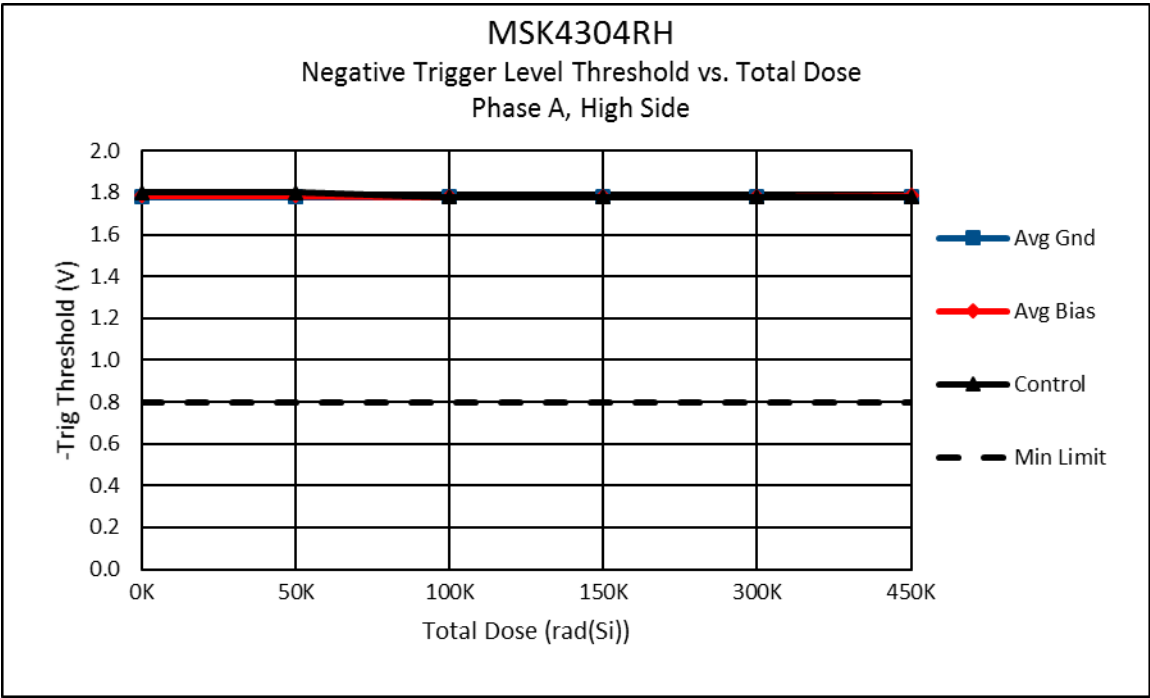


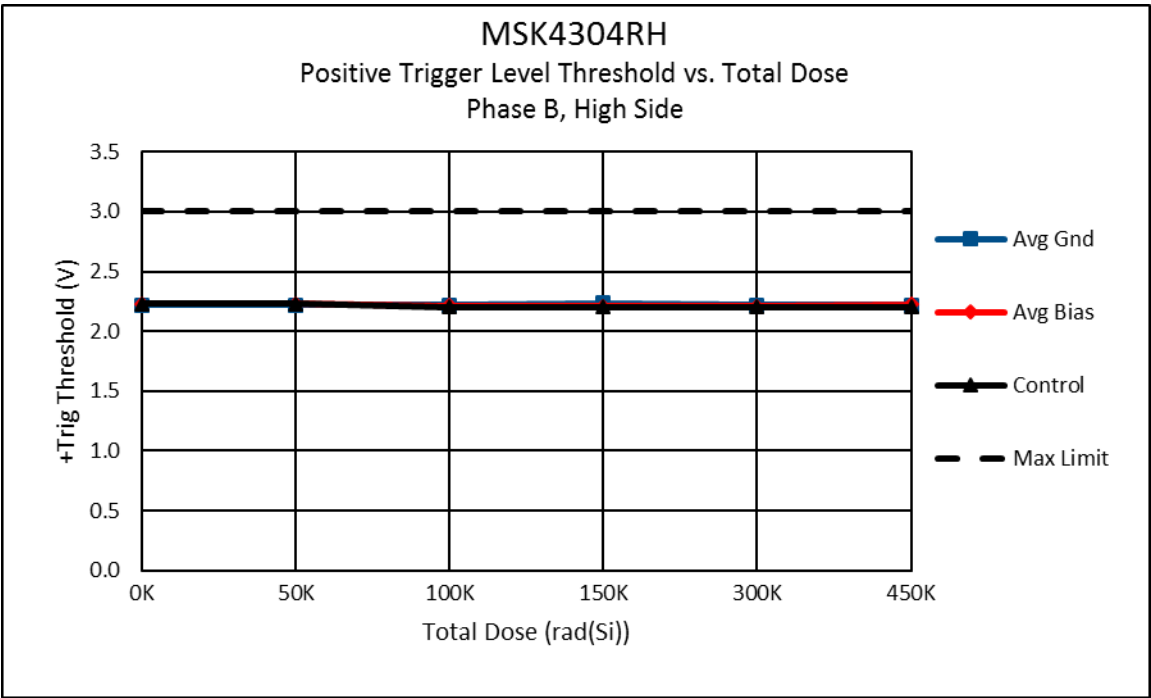
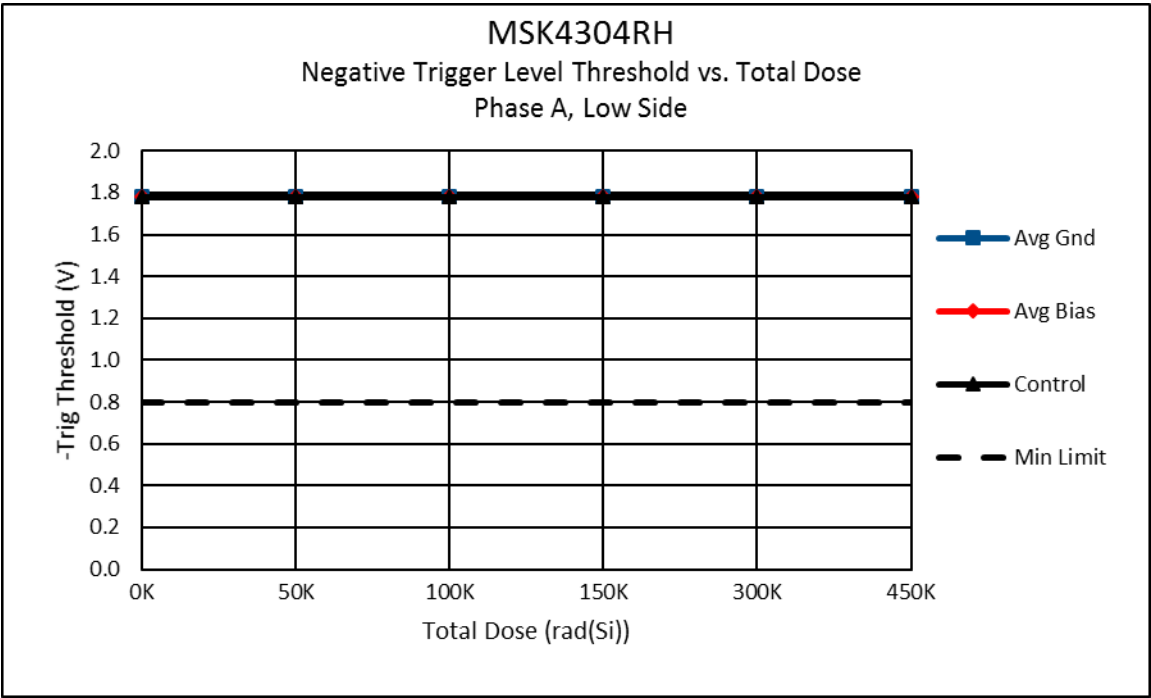


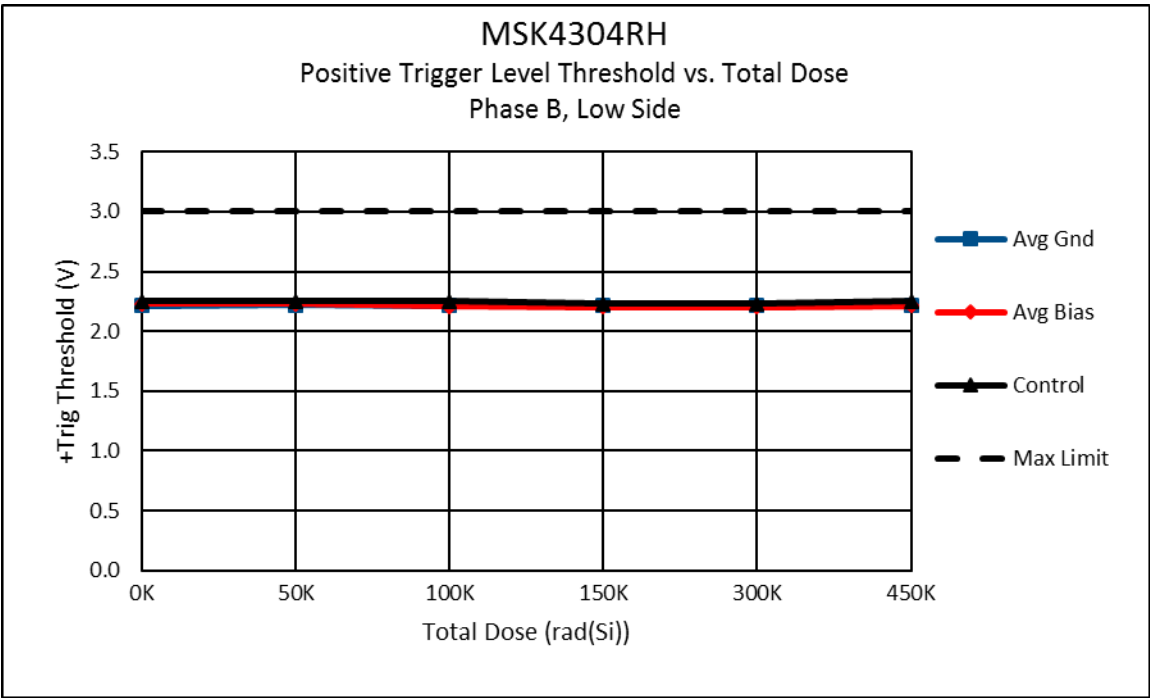
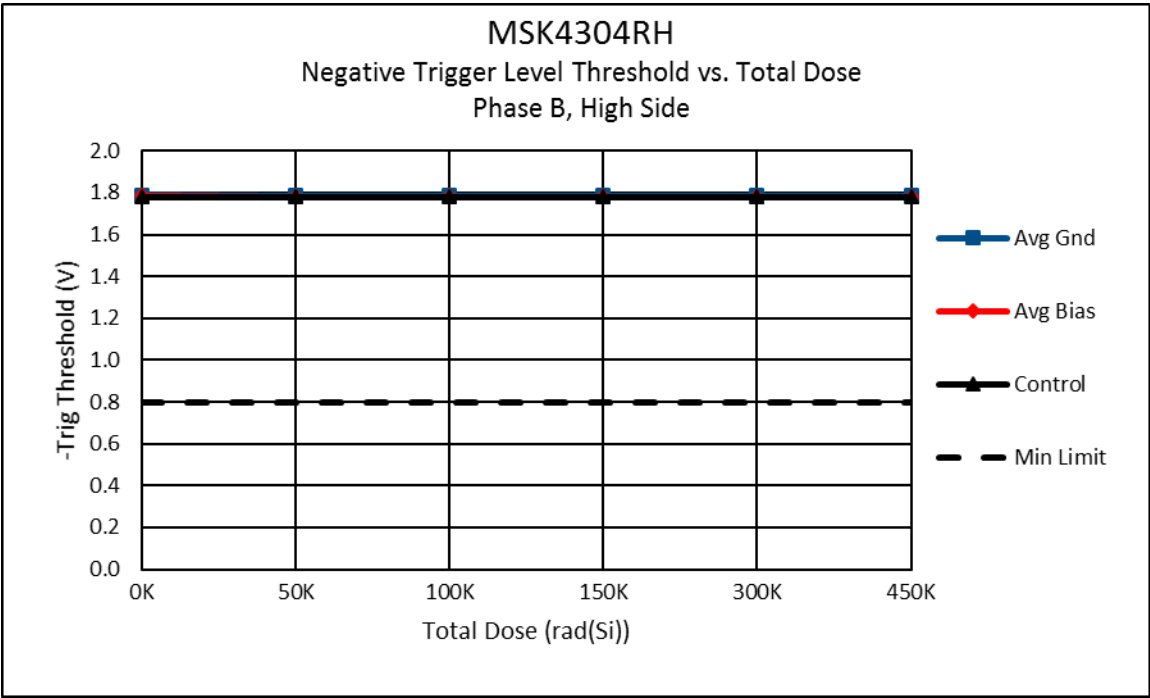


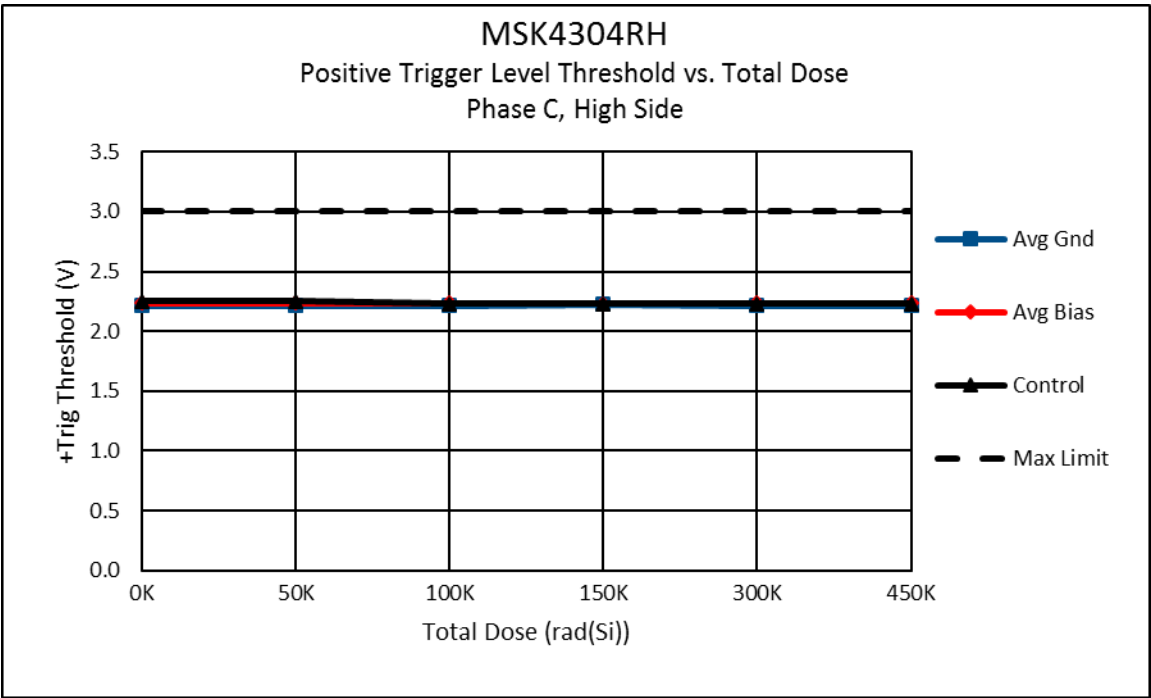
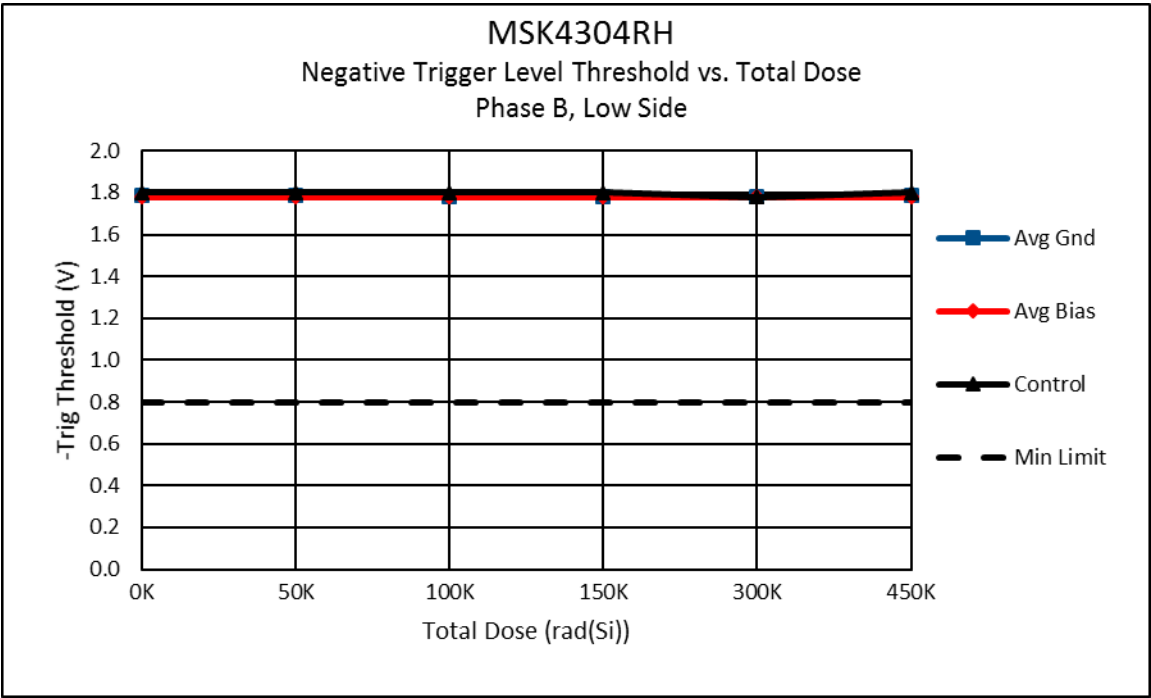


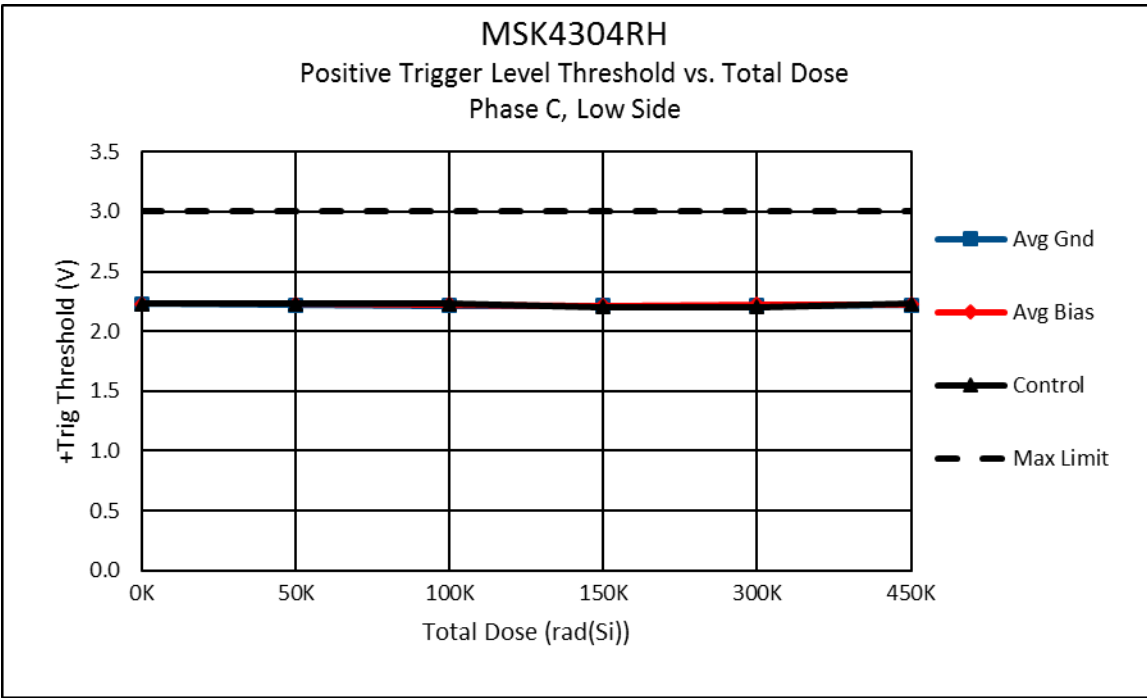
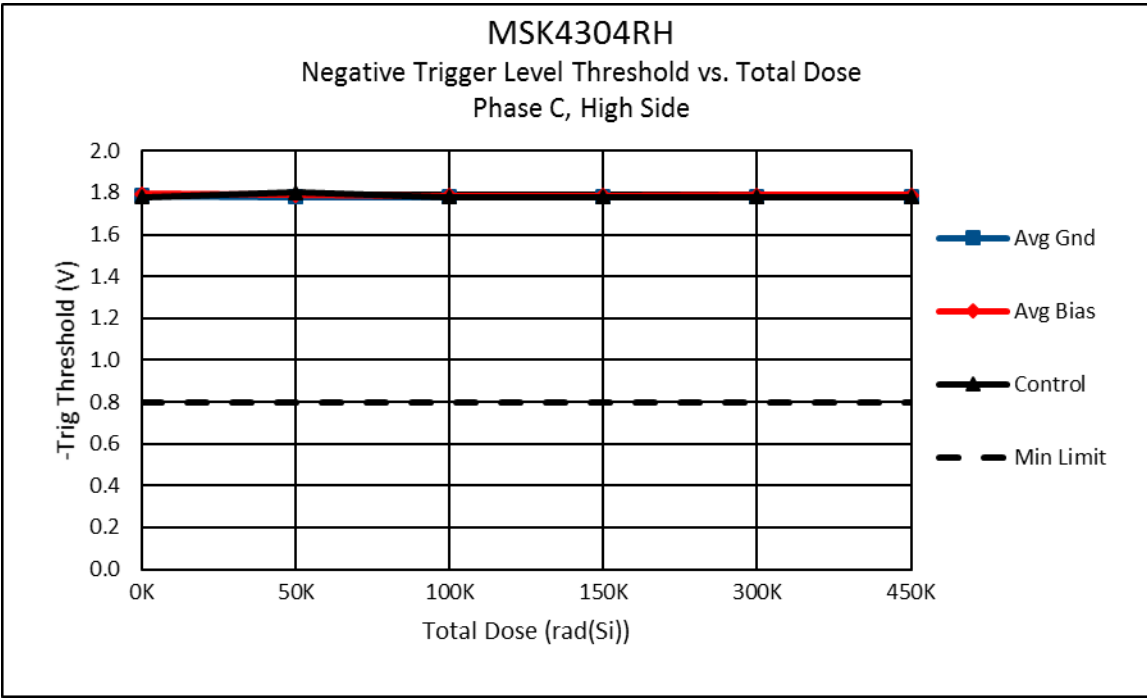


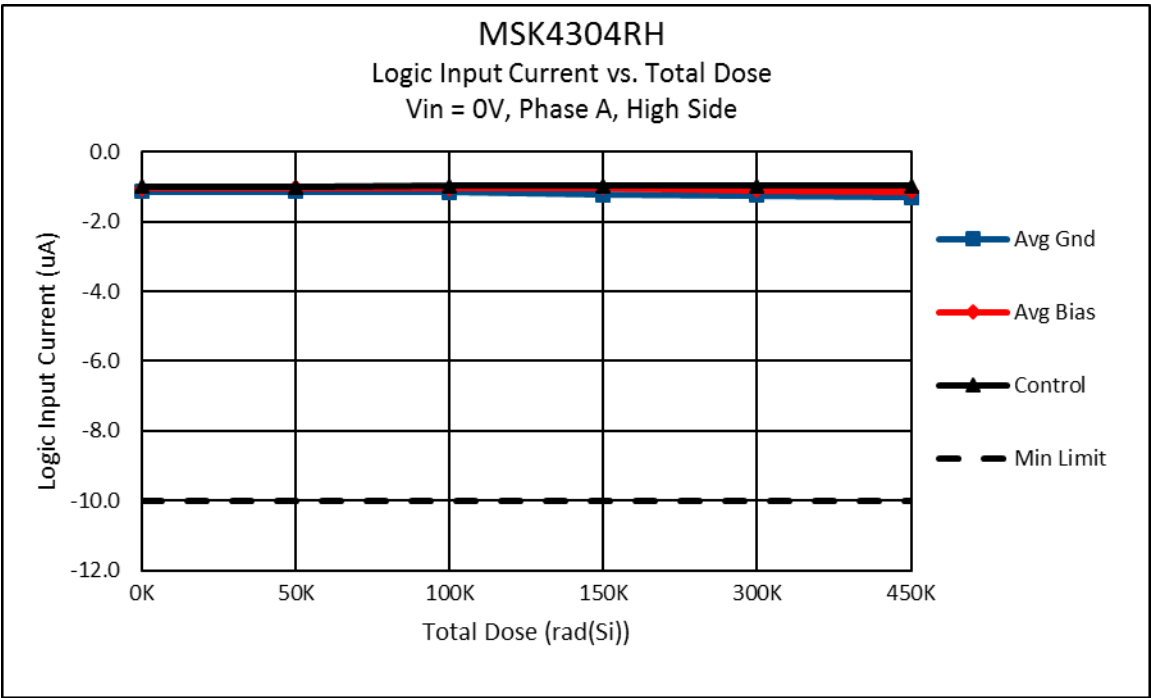
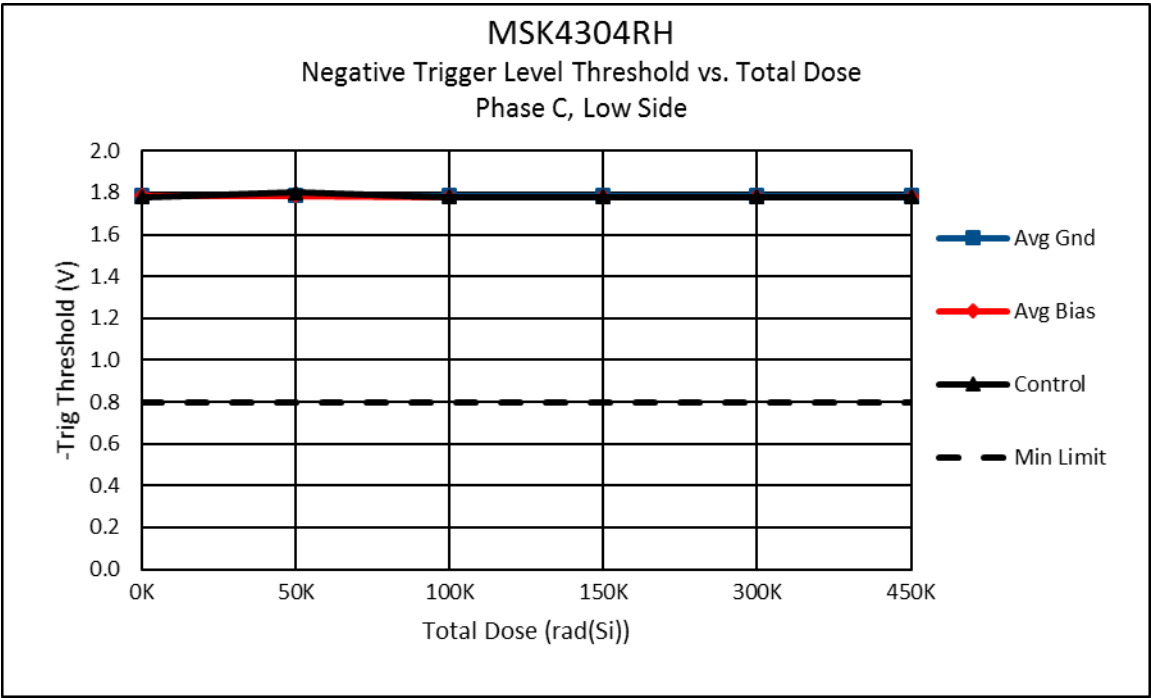


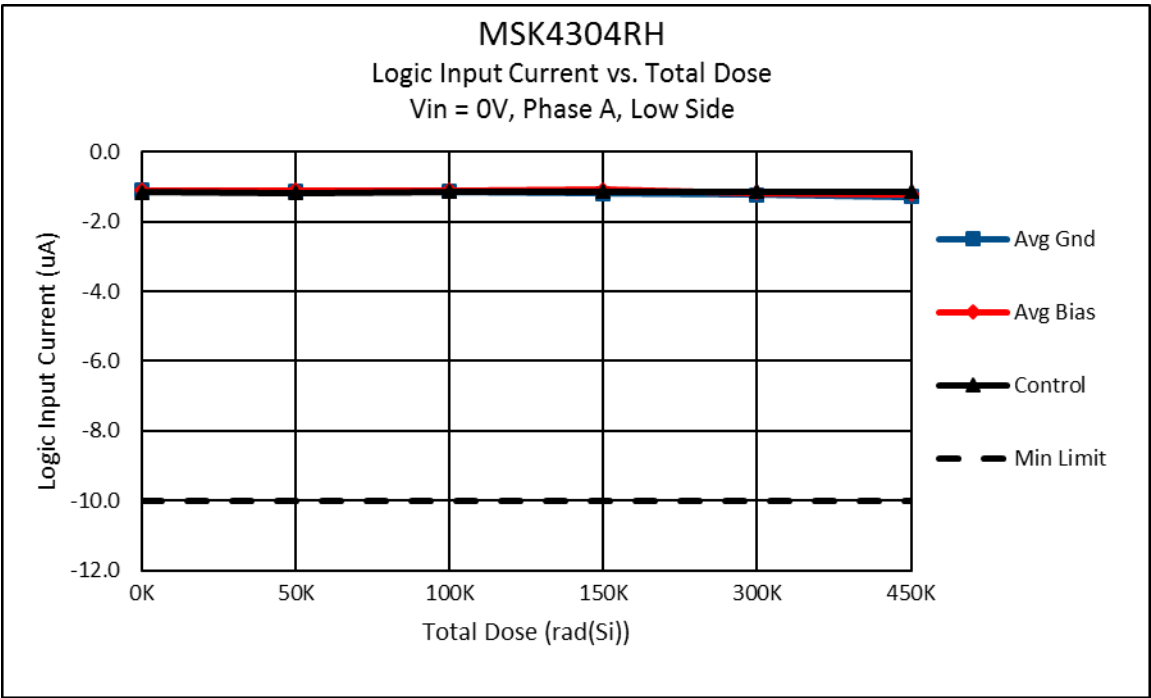
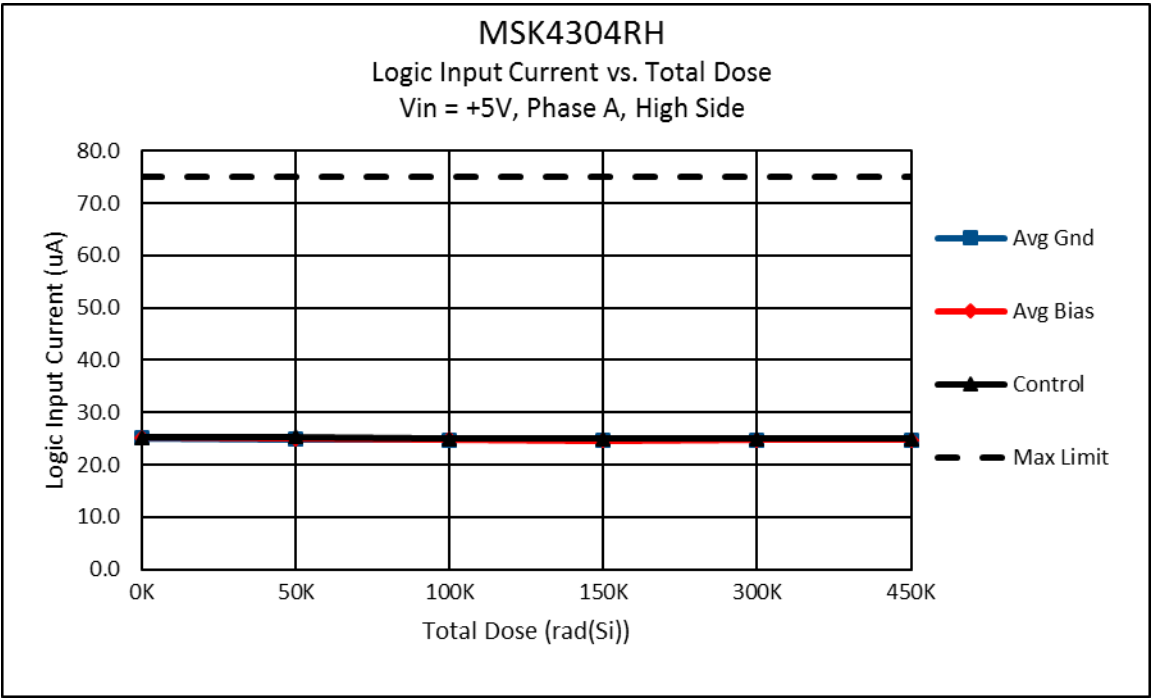


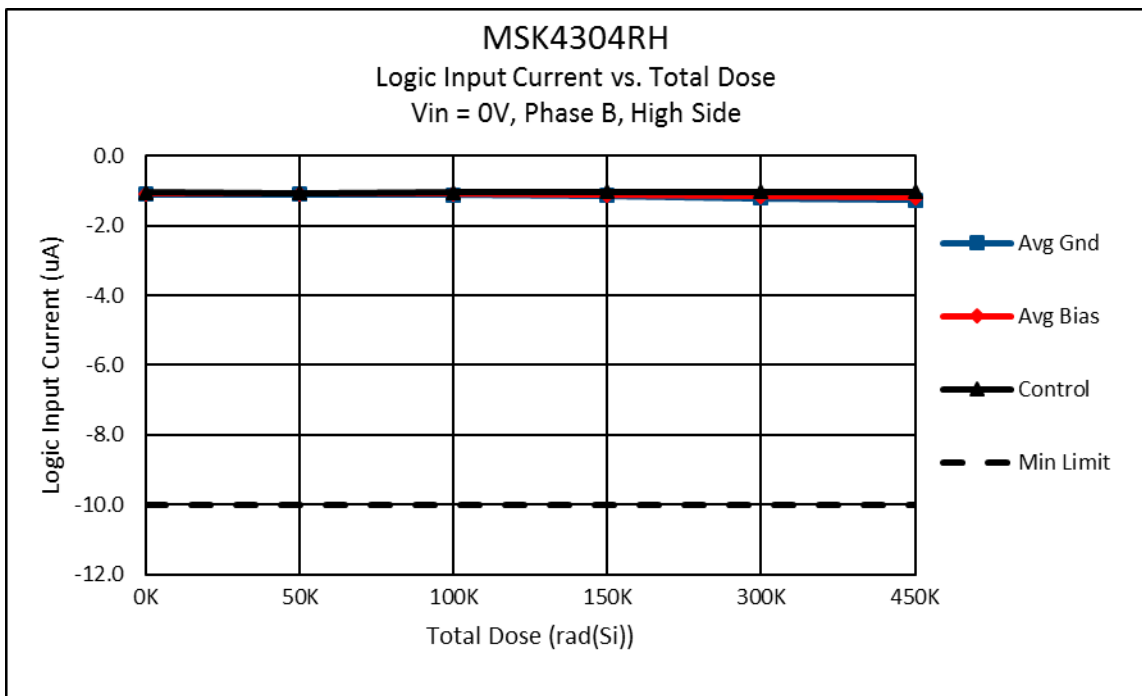
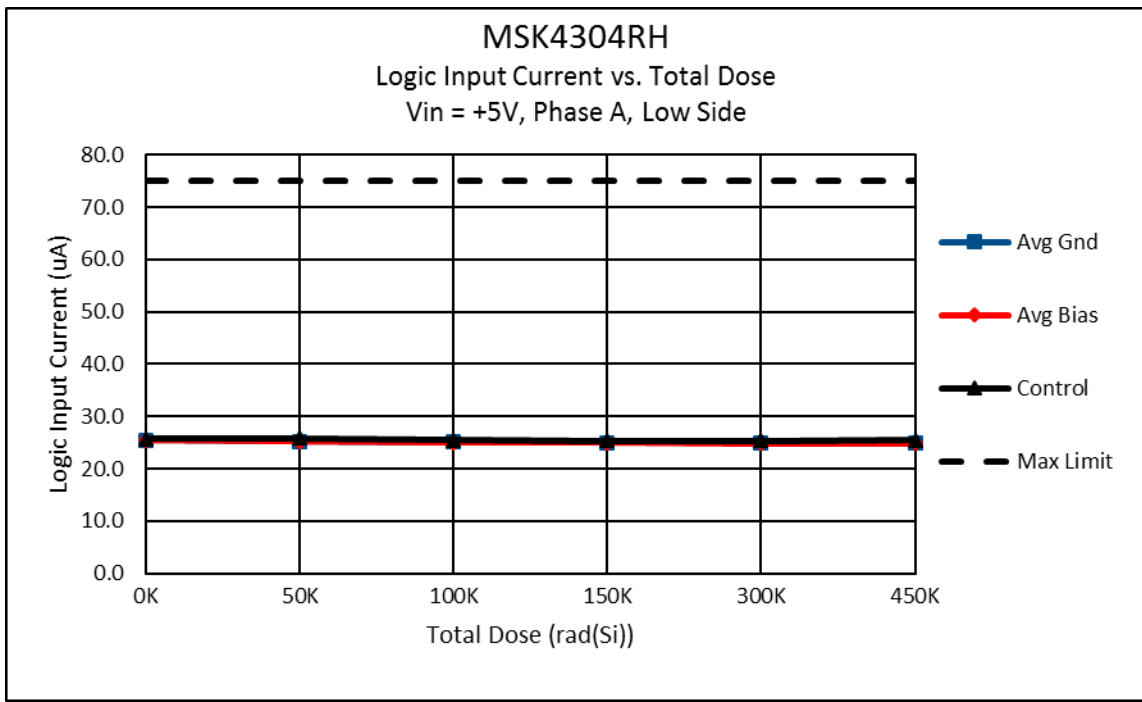


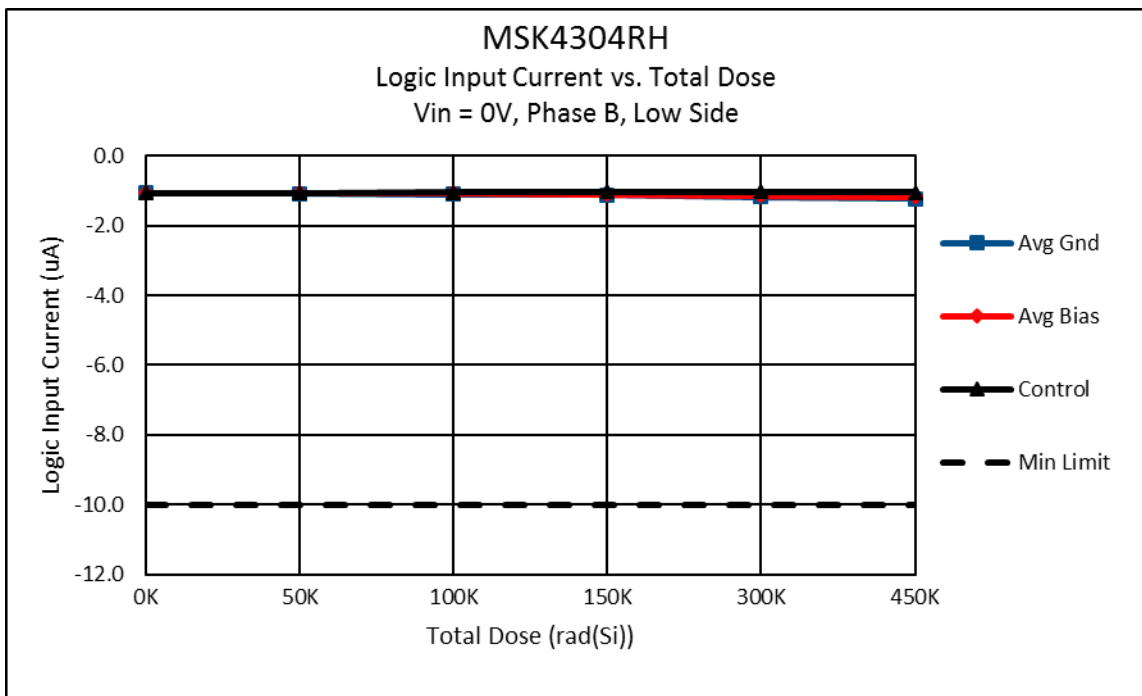
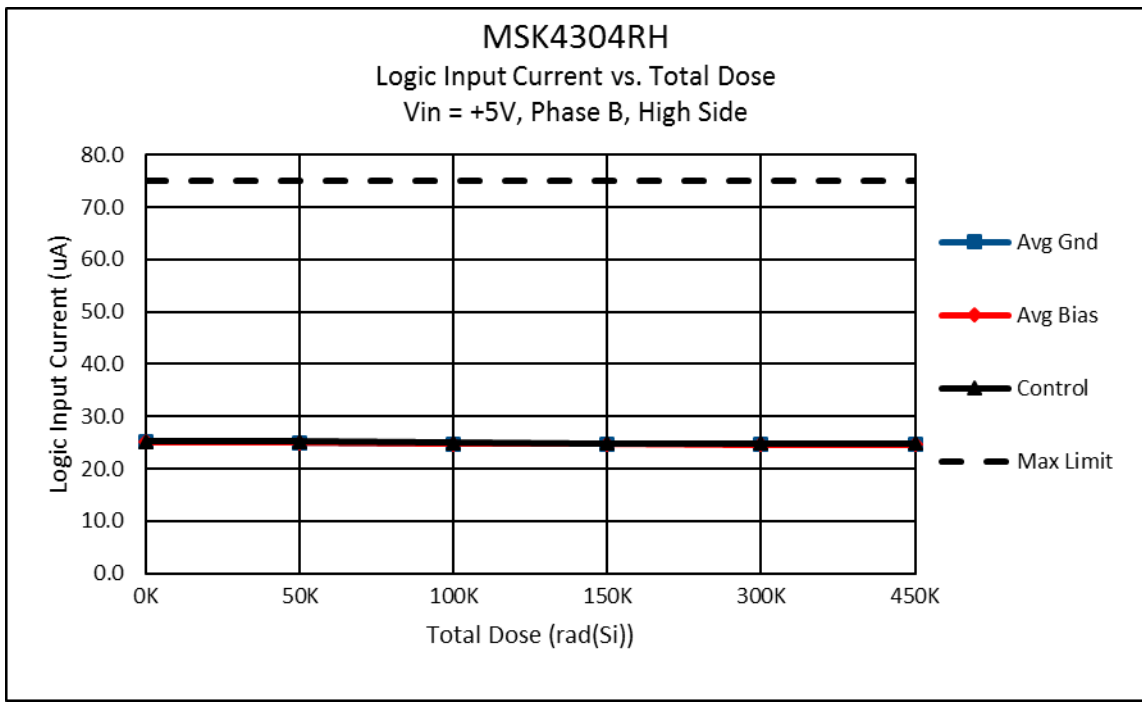


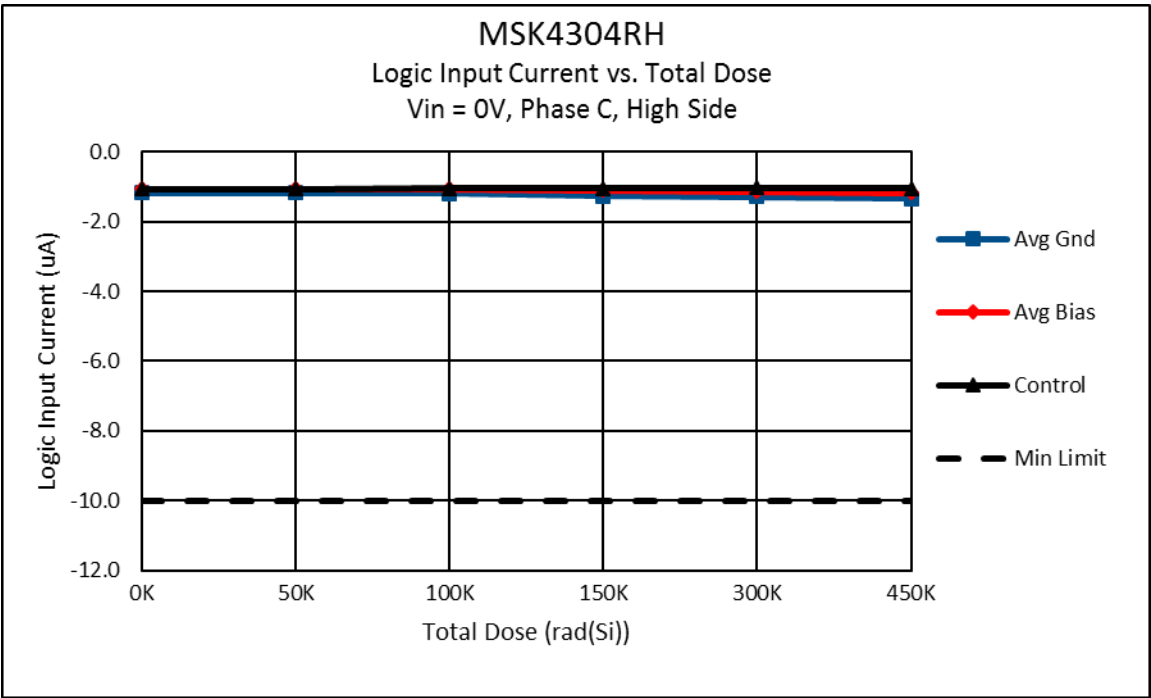
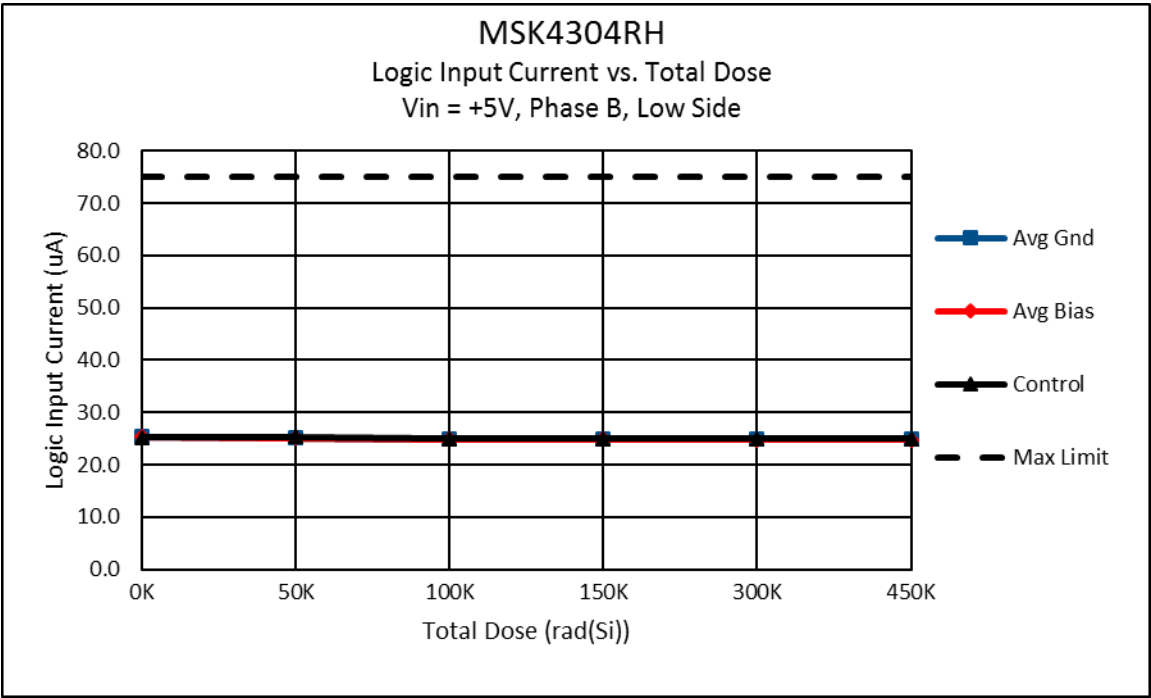


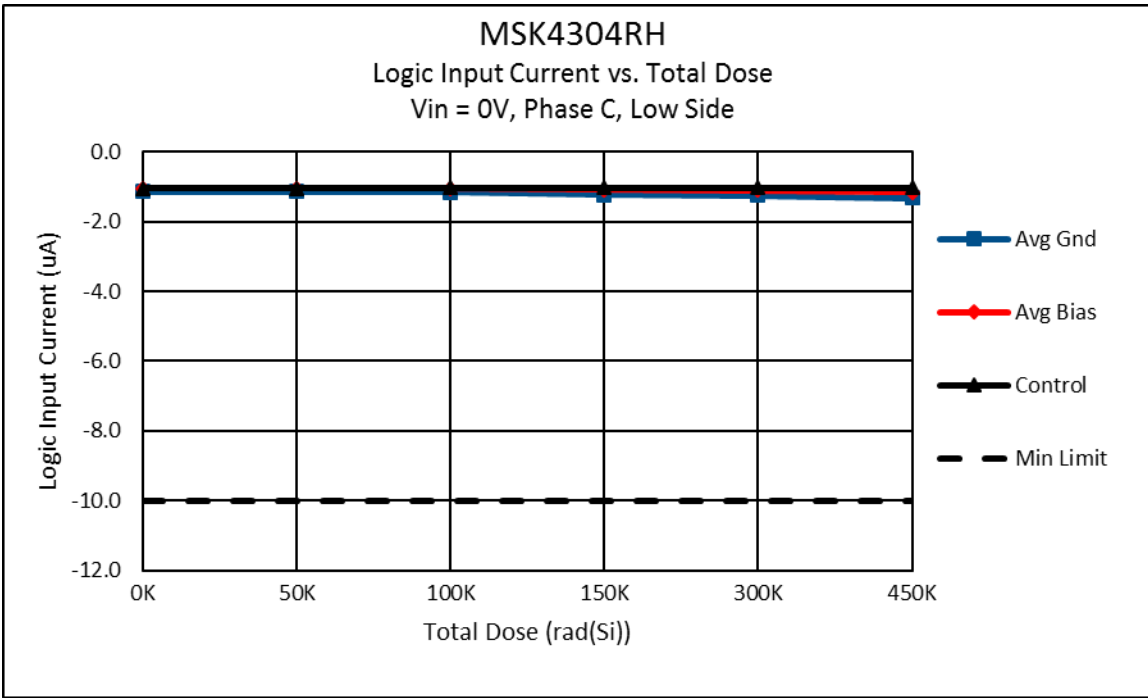
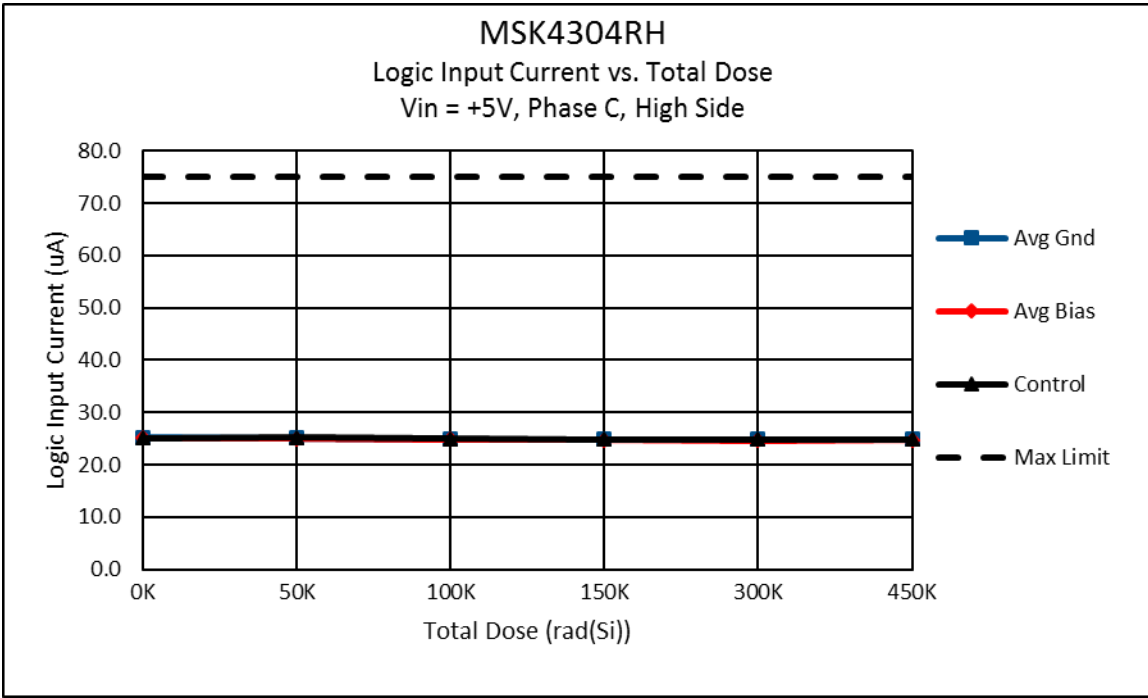


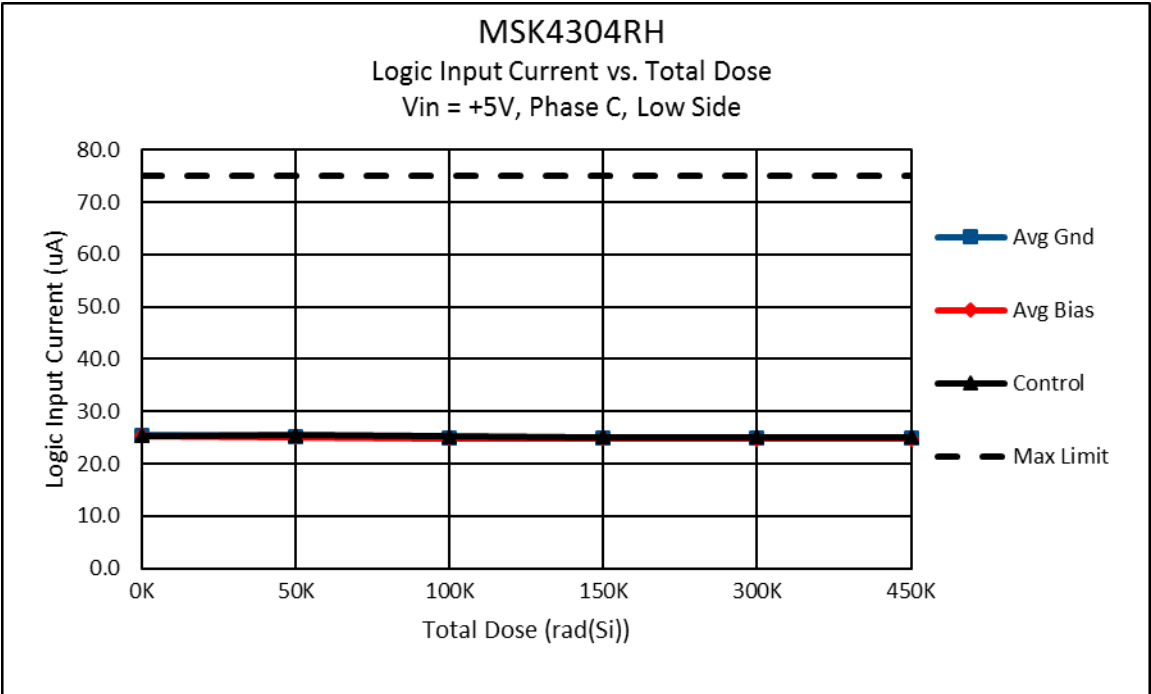












Total Dose Radiation Test Report

MSK 4304RH

3 - Phase Motor Drive Hybrid

June 14, 2007 (1st test)
November 6, 2013 (2nd test: IC Wafer Lot: DJA6ENC
Transistor Wafer Lot: BA1016MFA #16)

B. Horton
R. Wakeman

M.S. Kennedy Corporation
Liverpool, NY

I. Introduction:

The total dose radiation test plan for the MSK 4304 RH was developed to qualify the devices as RAD Hard to 300 KRAD (Si). The testing was performed beyond 300 KRAD (Si) to show trends in device performance as a function of total dose. The test does not classify maximum radiation tolerance of the device, but simply offers designers insight to the critical parameter-shifts up to the specified total dose level.

MIL-STD-883 Method 1019.7 and ASTM F1892-06 were used as guidelines in the development and implementation of the total dose test plan for the MSK 4304 RH.

II. Radiation Source:

Total dose was performed at the University of Massachusetts, Lowell, using a cobalt 60 radiation source. The dose rate was determined to be 88 Rads(Si)/sec. The total dose schedule can be found in Table I.

III. Test Setup:

All test samples were subjected to Group A Electrical Test at 25°C in accordance with the device data sheet. In addition, all devices received 320 hours of burn-in per MIL-STD-883 Method 1015. For test platform verification, one control device was tested at 25°C. Four devices (12 samples, See para. IV) were then tested at 25°C, prior to irradiation, and were found to be within acceptable test limits.

The devices were vertically aligned with the radiation source and enclosed in a lead/aluminum container during irradiation. Two devices (six samples) were biased during irradiation. Two devices (six samples) had all leads grounded during irradiation for the unbiased condition.

After each irradiation the device leads were shorted together and transported to the MSK automatic electrical test platform and tested IAW MSK device data sheet. Testing was performed on irradiated devices, as well as the control device, at each total dose level. Electrical tests were completed within one hour of irradiation. Devices were subjected to subsequent radiation doses within two hours of removal from the radiation field.

IV. Data:

Each device contains three identical and independently operating circuits. For each MSK4304RH tested the effective sample size is three. All performance curves are averaged from the test results of the biased and unbiased devices, respectively. If required, full test data can be obtained by contacting M.S. Kennedy Corporation.

V. Summary:

Based on the test data recorded during radiation testing, the MSK4304RH qualified as a 300 KRAD (Si) radiation hardened device. All performance curves stayed well within specification up to the maximum test dose, 450 KRAD (Si) TID

MSK 4304RH Biased/Unbiased Dose Rate Schedule
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Dosimetry Equipment
Bruker Biospin # 0162

Irradiation Date
11/6/13

Exposure Length (min:sec)	Incremental Dose rads(Si)	Cumulative Dose rads(Si)
0:19:31	103,048	103,048
0:09:46	51,568	154,616
0:29:16	154,528	309,144
0:29:16	154,528	463,672

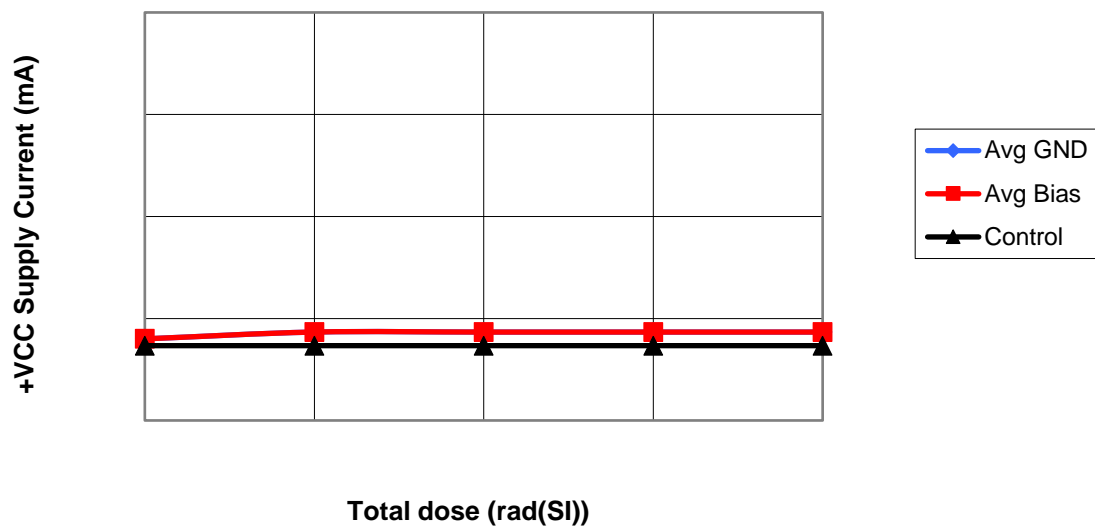
Biased S/N – 0154, 0155

Unbiased S/N – 0156, 0157

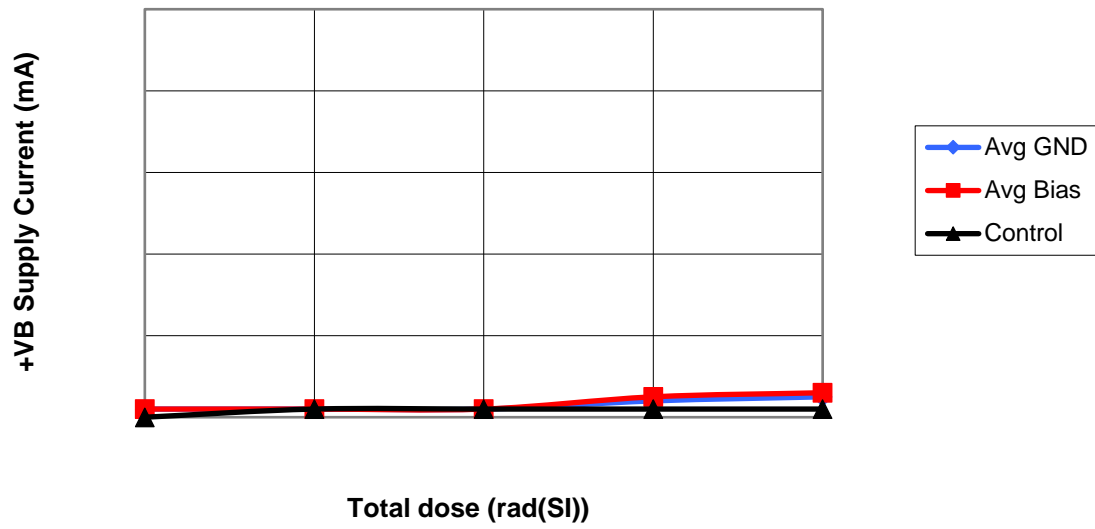
Table 1

Dose Time, Incremental Dose and Total Cumulative Dose

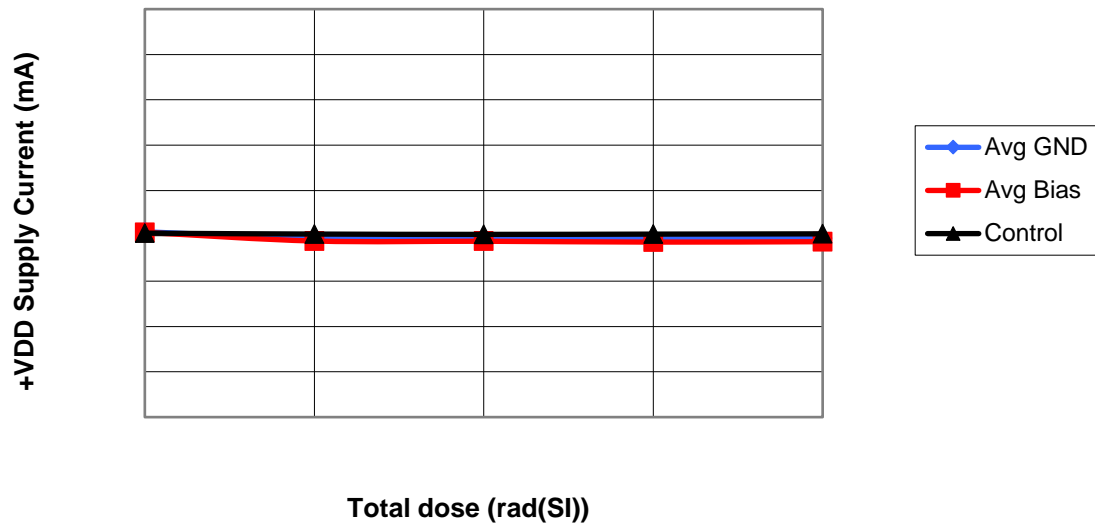
MSK4304RH
+VCC Supply Current vs. Total Dose



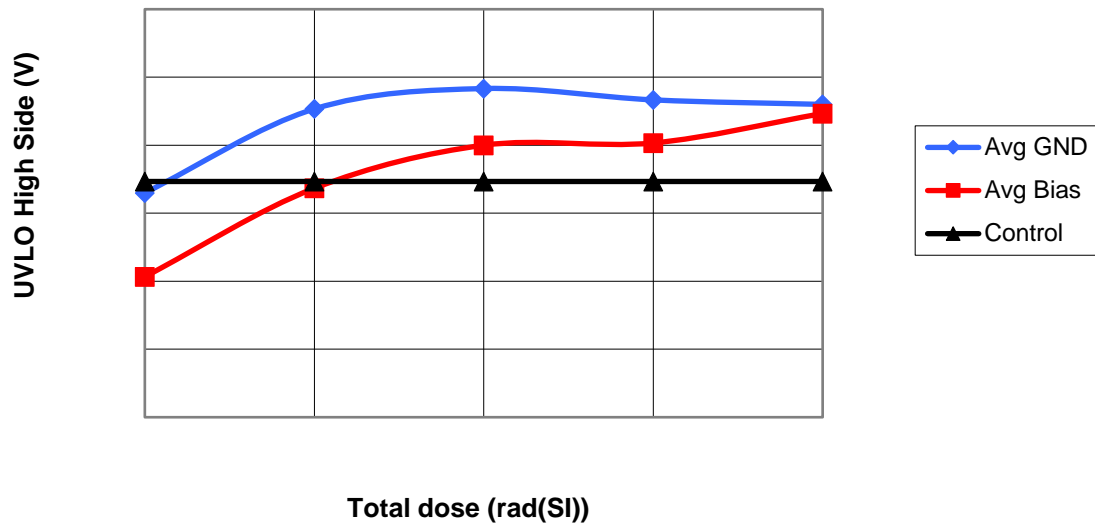
MSK4304RH
+VB Supply Current vs. Total Dose



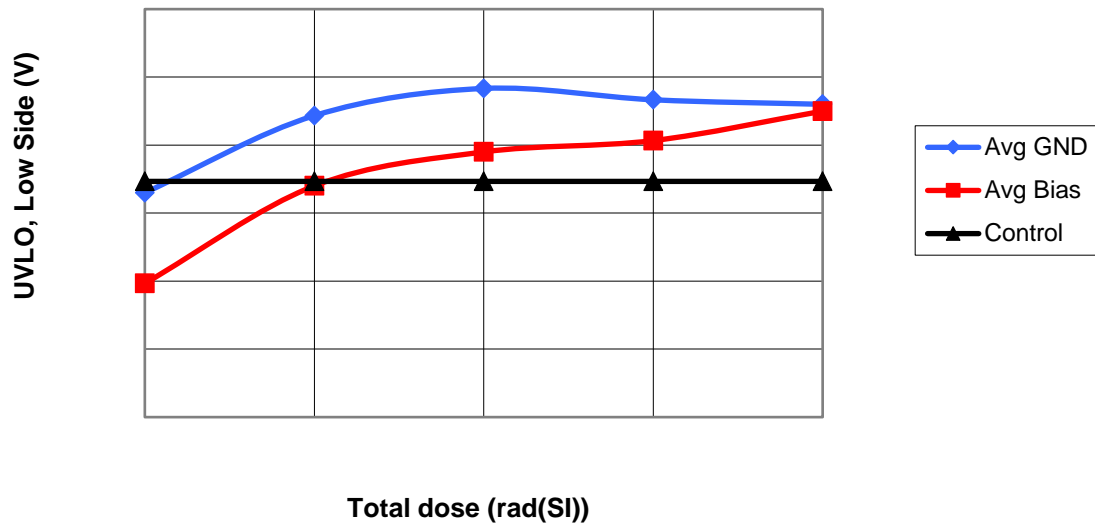
MSK4304RH
+VDD Supply Current vs. Total Dose



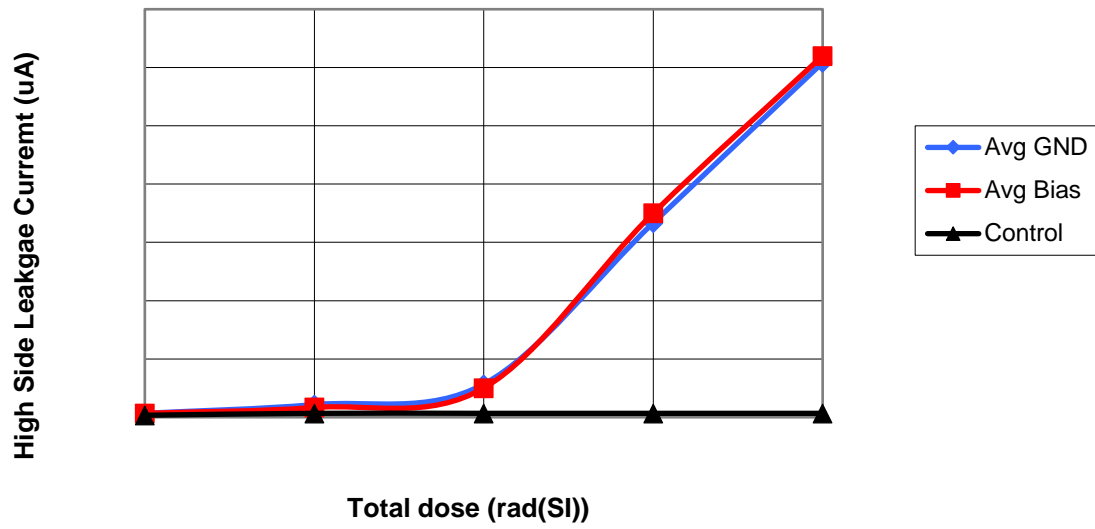
MSK4304RH
UVLO, High Side vs. Total Dose



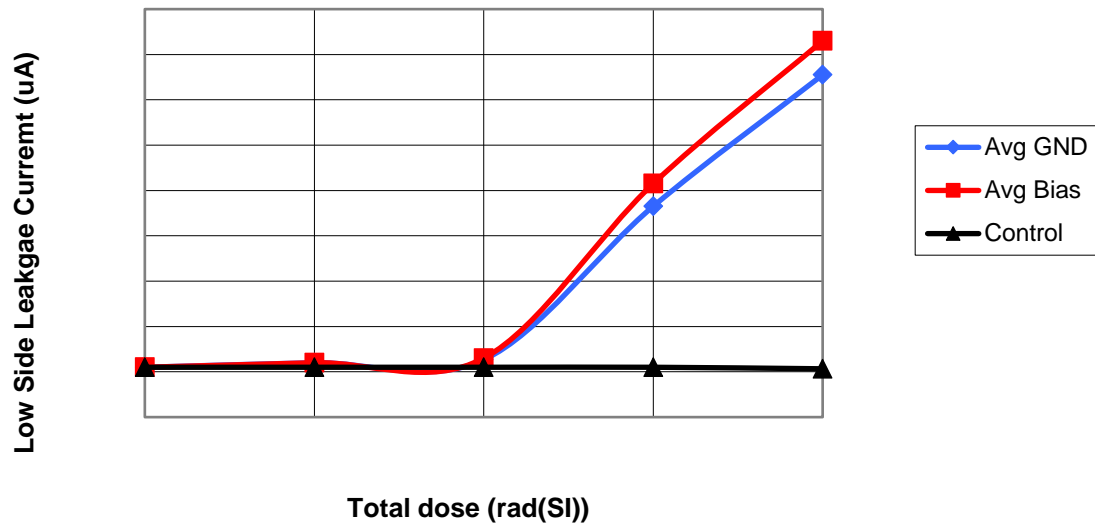
MSK4304RH
UVLO, Low Side vs. Total Dose



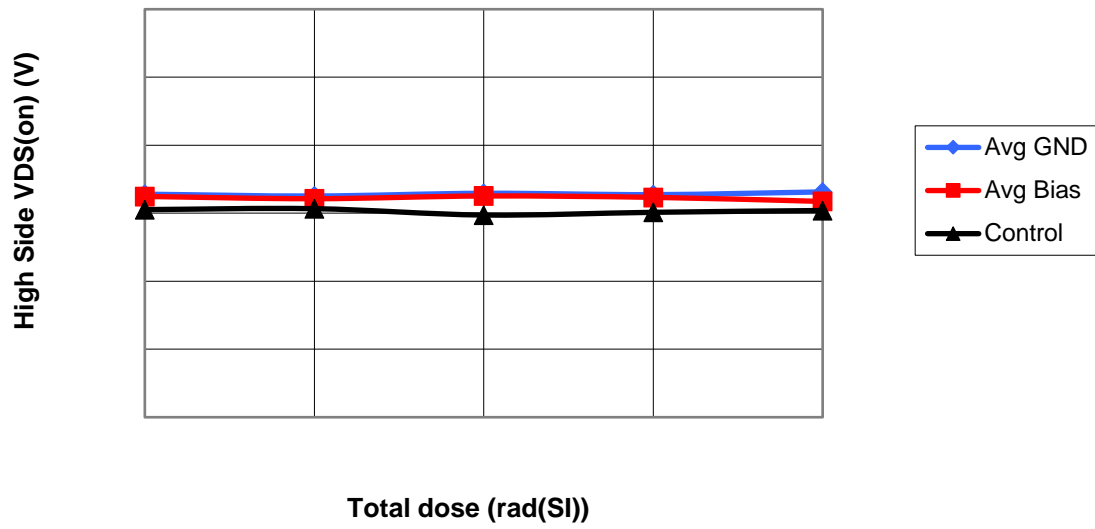
MSK4304RH
High Side Leakage Current vs. Total Dose



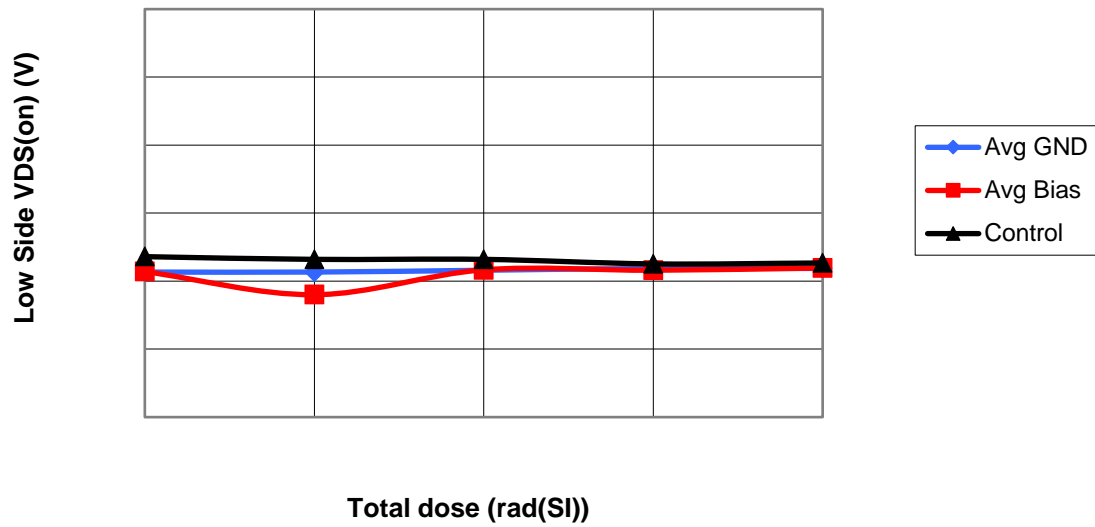
MSK4304RH
Low Side Leakage Current vs. Total Dose



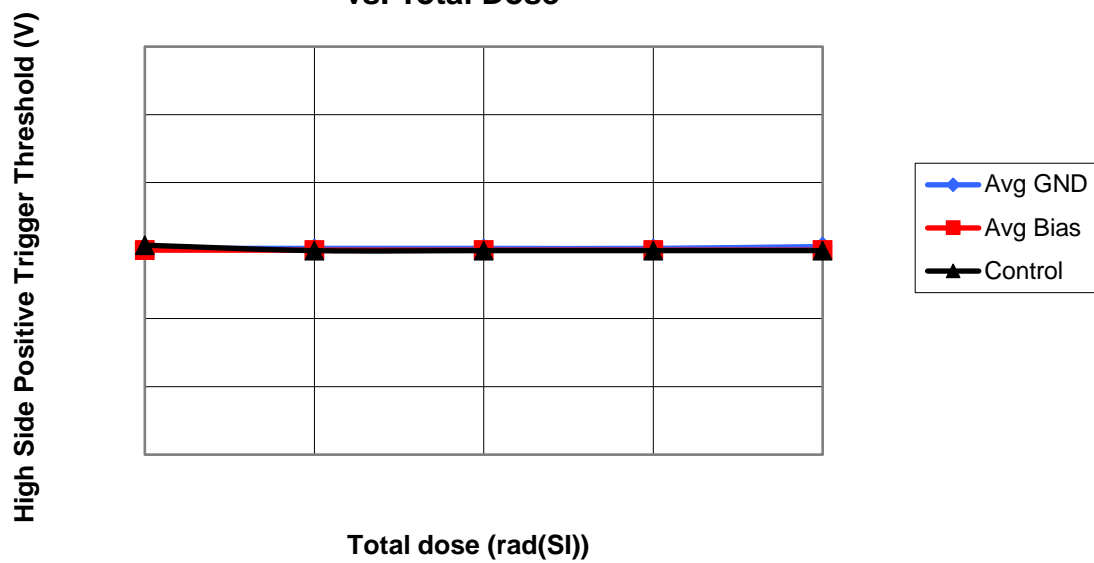
MSK4304RH
High Side VDS(on) vs. Total Dose



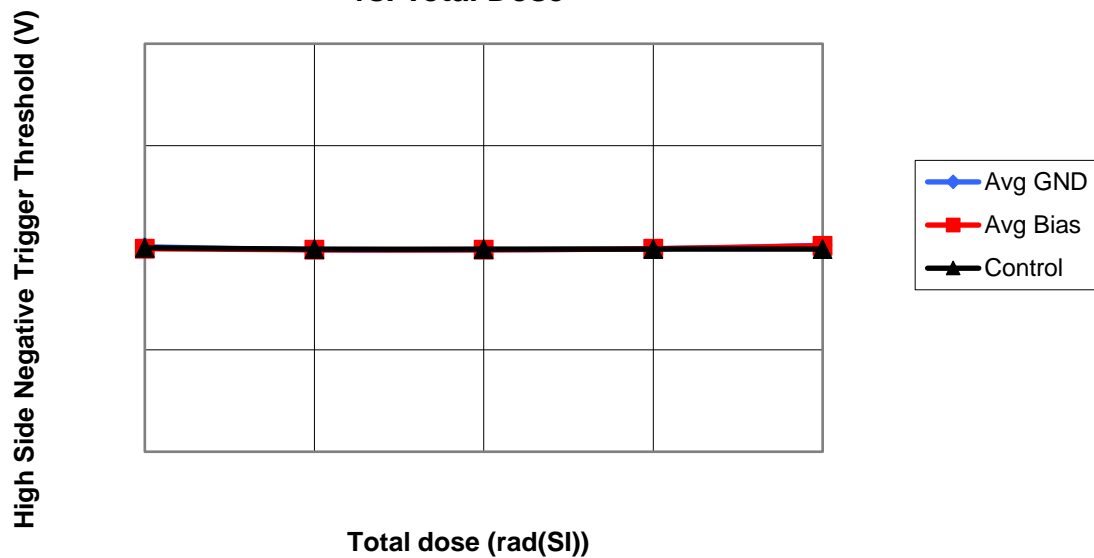
MSK4304RH
Low Side VDS(on) vs. Total Dose



MSK4304RH
High Side Positive Trigger Threshold Voltage
vs. Total Dose

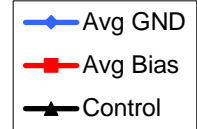
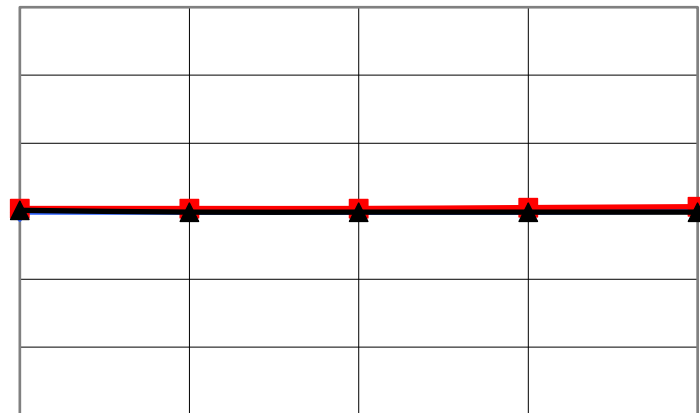


MSK4304RH
High Side Negative Trigger Threshold Voltage
vs. Total Dose



MSK4304RH
Low Side Positive Trigger Threshold Voltage
vs. Total Dose

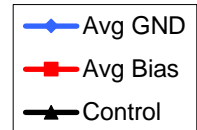
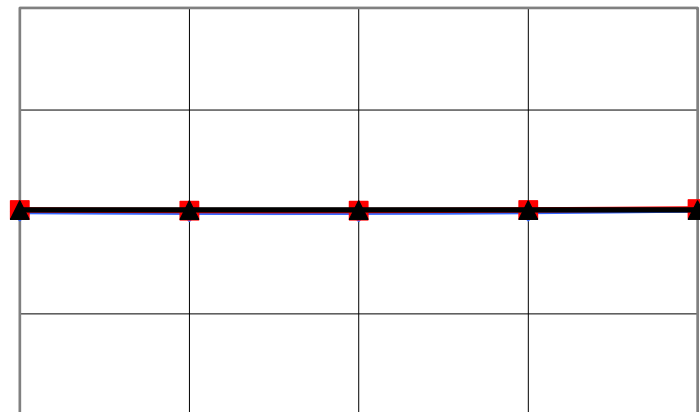
Low Side Positive Trigger Threshold (V)



Total dose (rad(SI))

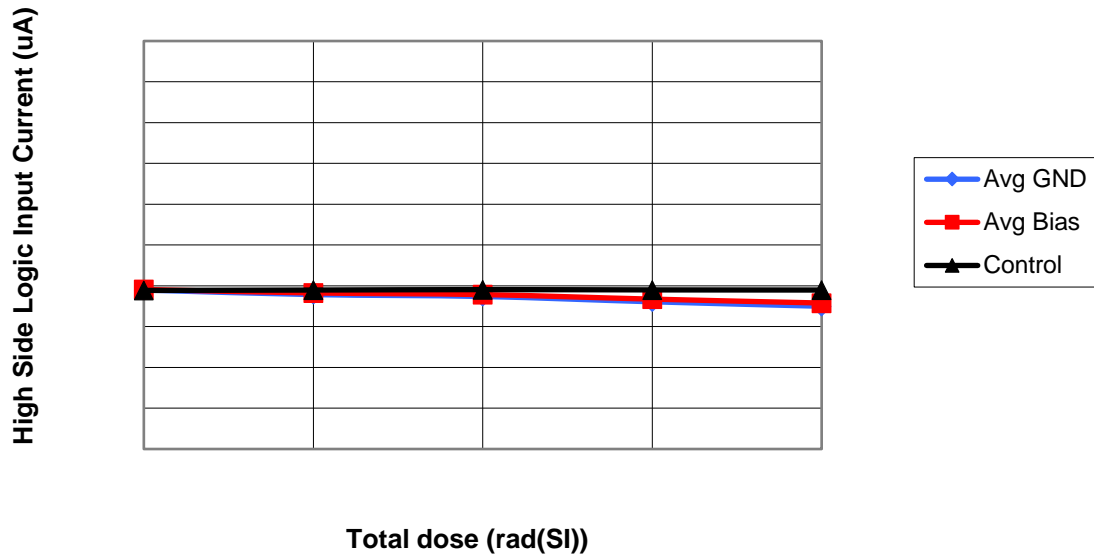
MSK4304RH
Low Side Negative Trigger Threshold Voltage
vs. Total Dose

Low Side Negative Trigger Threshold (V)

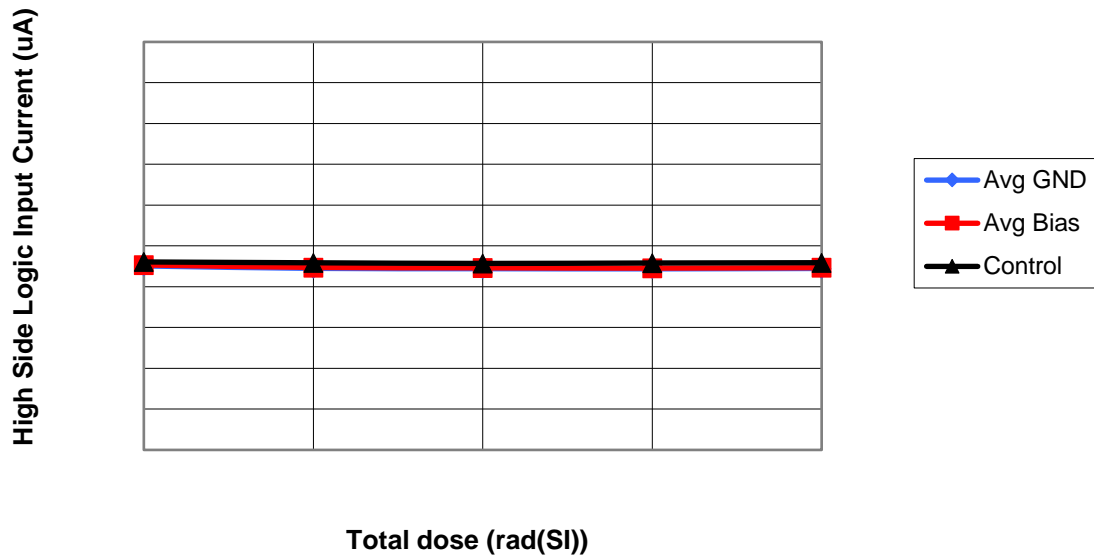


Total dose (rad(SI))

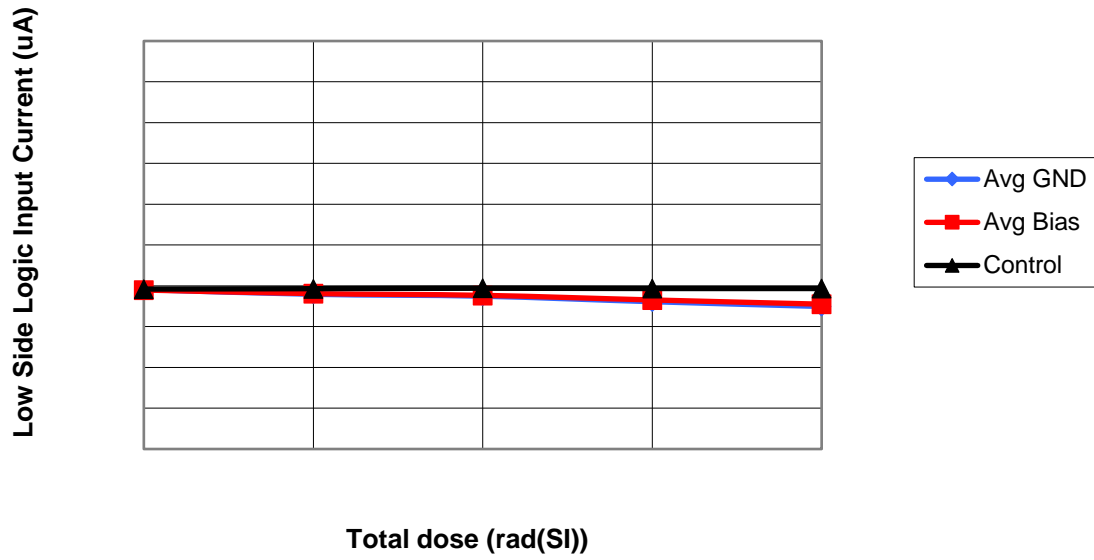
MSK4304RH
High Side Logic Input Current (0V) vs. Total Dose



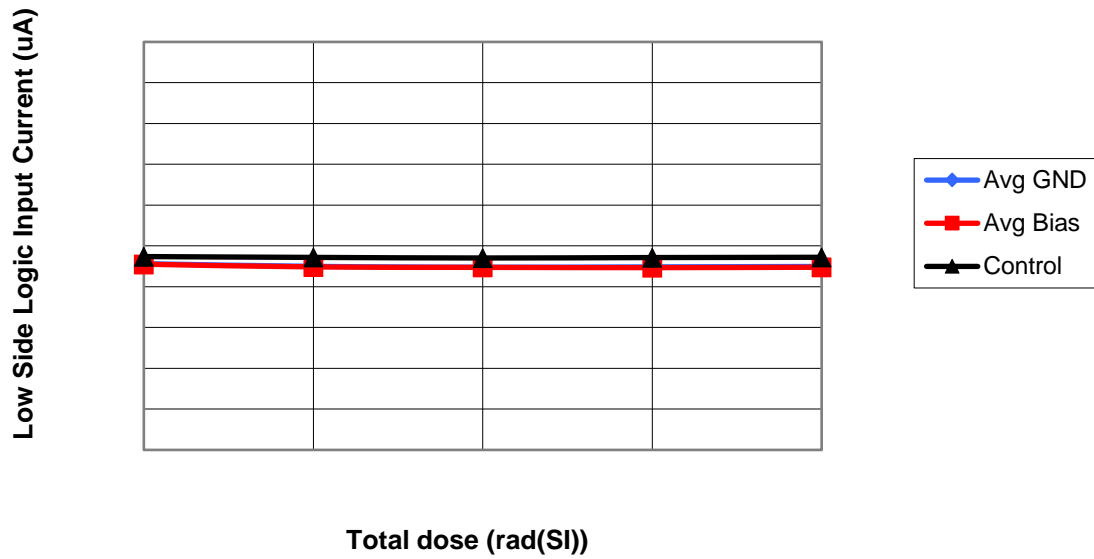
MSK4304RH
High Side Logic Input Current (5V) vs. Total Dose



MSK4304RH
Low Side Logic Input Current (0V) vs. Total Dose



MSK4304RH
Low Side Logic Input Current (5V) vs. Total Dose



Total Dose Radiation Test Report

MSK 4304RH

3 - Phase Motor Drive Hybrid

June 14, 2007

J. Douglas
B. Erwin

M.S. Kennedy Corporation
Liverpool, NY

I. Introduction:

The total dose radiation test plan for the MSK 4304RH was developed to qualify the device as a radiation tolerant device to 300 Krad(Si). The testing was performed up to 450 Krad to show trends in device performance as a function of total dose.

MIL-STD-883 Method 1019.7 and ASTM F1892-98 were used as guidelines in the development and implementation of the total dose test plan for the MSK 4304RH.

II. Radiation Source:

Total dose was performed at the University of Massachusetts, Lowell, using a cobalt 60 radiation source. Alanine dosimetry was performed and the dose rate was determined to be 118 Rads(Si)/sec. The total dose schedule can be found in Table I.

III. Test Setup:

All test samples were subjected to Group A Electrical Test in accordance with MSK4304RH Electrical Test Procedure 1702-12750 Rev -. In addition, all devices received 320 hours of burn-in per MIL-STD-883 Method 1015 and were electrically tested prior to irradiation. For test platform verification, one control device was tested at 25°C.

The devices were vertically aligned with the radiation source and enclosed in a lead/aluminum container during irradiation. Four devices were kept under bias during irradiation. Four devices had all leads grounded during irradiation for the unbiased condition.

After each irradiation the device leads were held electrically common and the devices were transported to the MSK electrical test platform and tested IAW the MSK 4304RH Electrical Test Procedure 1702-12750 Rev -. Testing was performed on irradiated devices, as well as the control device, at each total dose level. Electrical tests were completed within one hour of irradiation.

IV. Data:

All performance curves are averaged from the test results of the biased and unbiased devices respectively.

V. Summary:

Based on the test data recorded during radiation testing, the MSK 4304RH qualified as a 300 Krad(Si) radiation tolerant device. All test parameters stayed within specification up to and beyond 450 Krad(Si) TID.

Dosimetry Equipment:
Bruker Biospin #0141

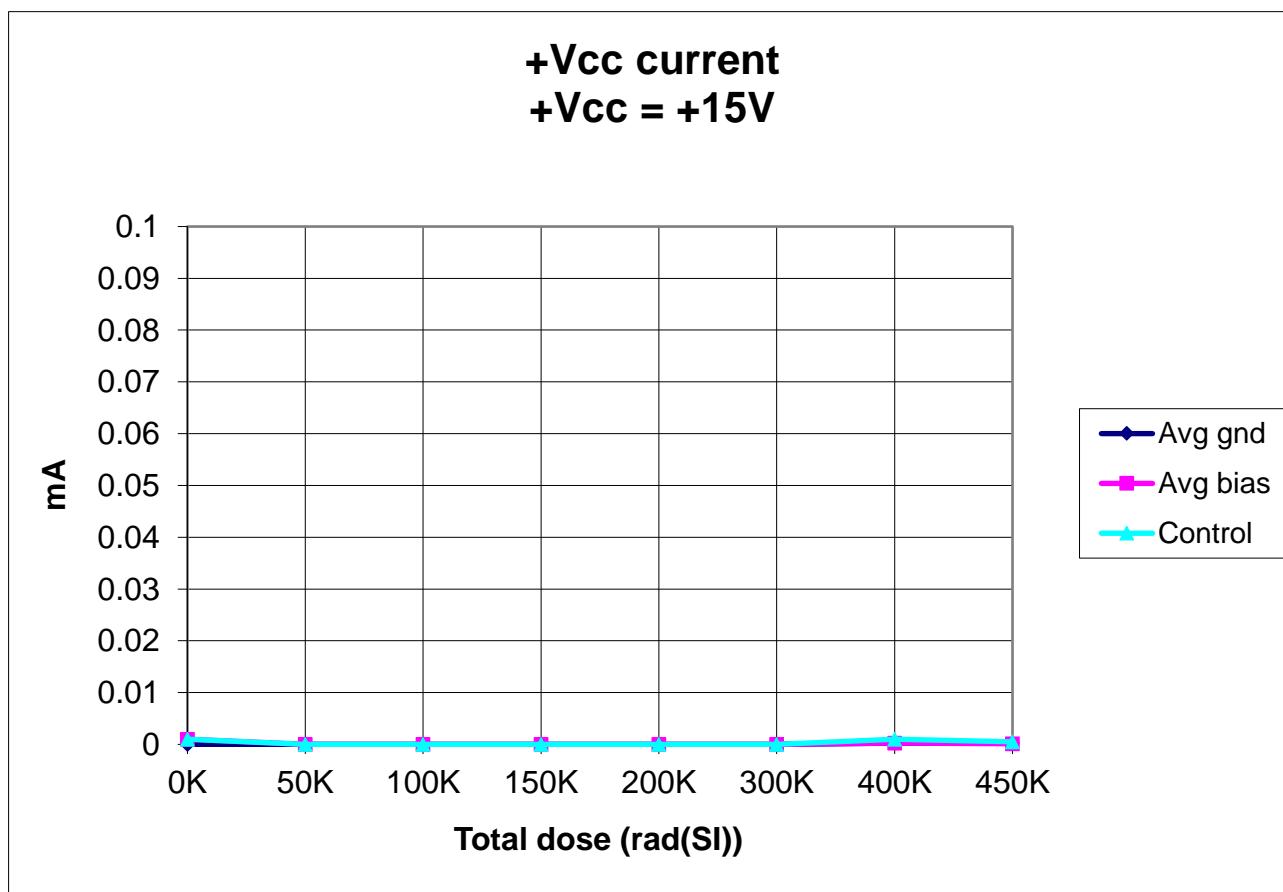
Dose Rate = 118 Rads(Si)/Sec
Device Date Code 0721

Testing Performed:
6/12/2007

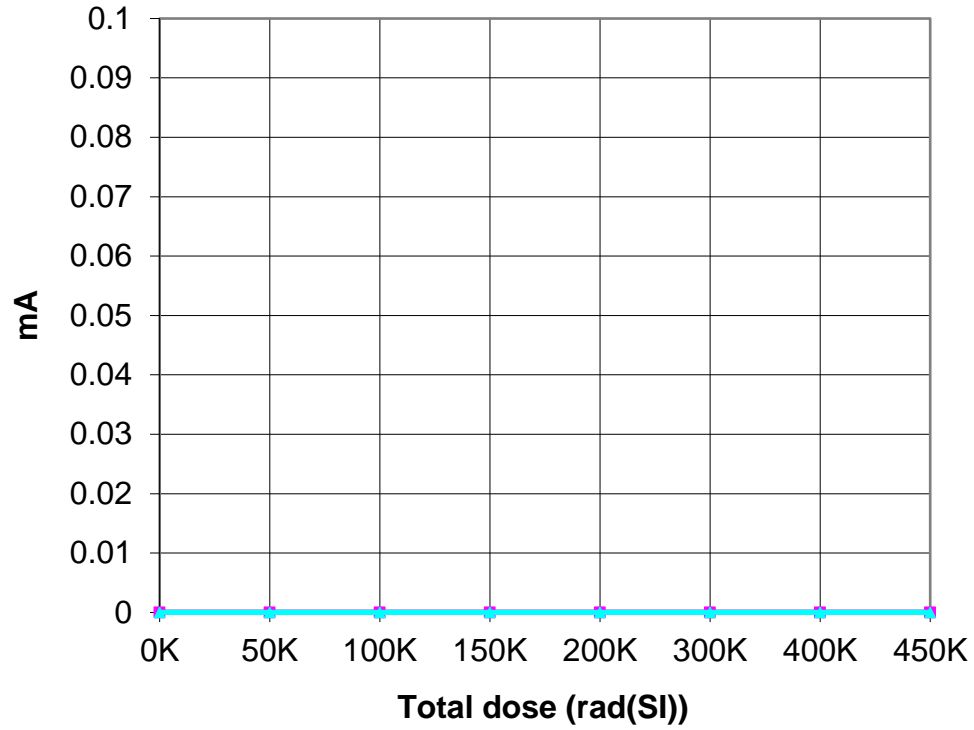
Board Identification MSK 4304 GND		
Exposure Length (min:sec)	Incremental Dose Rads(Si)	Cumulative Dose Rads(Si)
07:17	51,556	51,556
07:17	51,556	103,132
07:17	51,556	154,698
07:17	51,556	206,264
14:33	103,014	309,278
14:33	103,014	412,292
14:33	103,014	515,306
Serial #'s: 3645, 3646, 3647 & 3648		

Board Identification 4304 BIAS		
Exposure Length (min:sec)	Incremental Dose Rads(Si)	Cumulative Dose Rads(Si)
07:17	51,556	51,556
07:17	51,556	103,132
07:17	51,556	154,698
07:17	51,556	206,264
14:33	103,014	309,278
14:33	103,014	412,292
14:33	103,014	515,306
Serial #'s: 3650, 3651, 3652 & 3654		

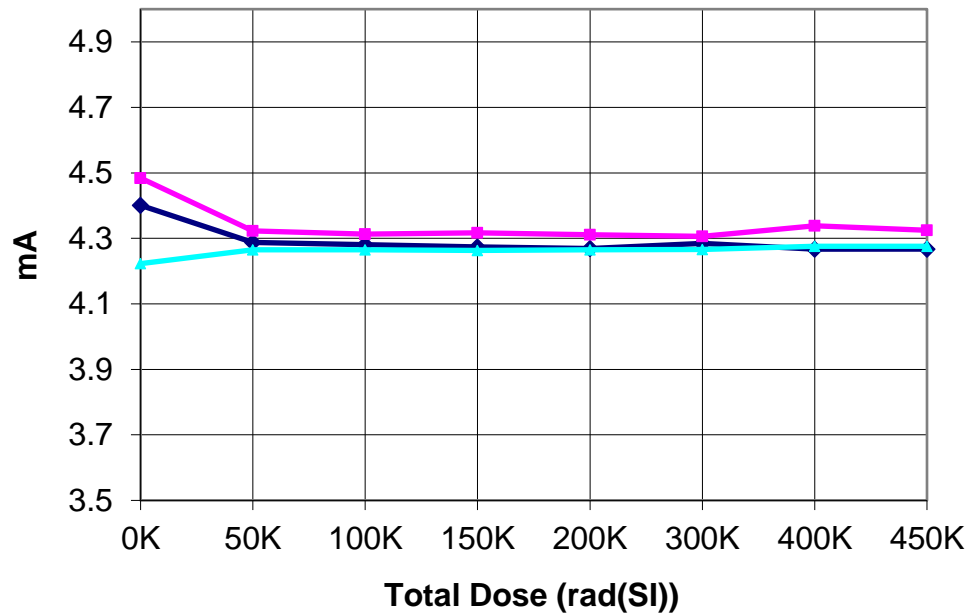
Table I
Dose Time, Incremental Dose and Total Cumulative Dos



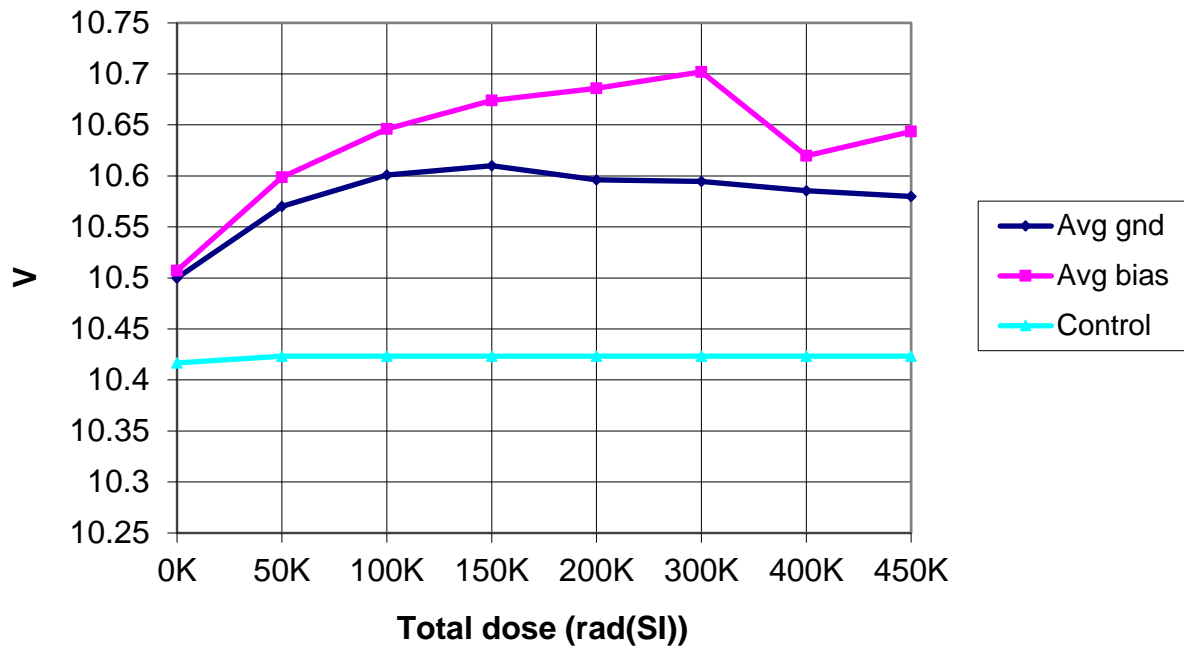
+VB Current
+VB = +15V



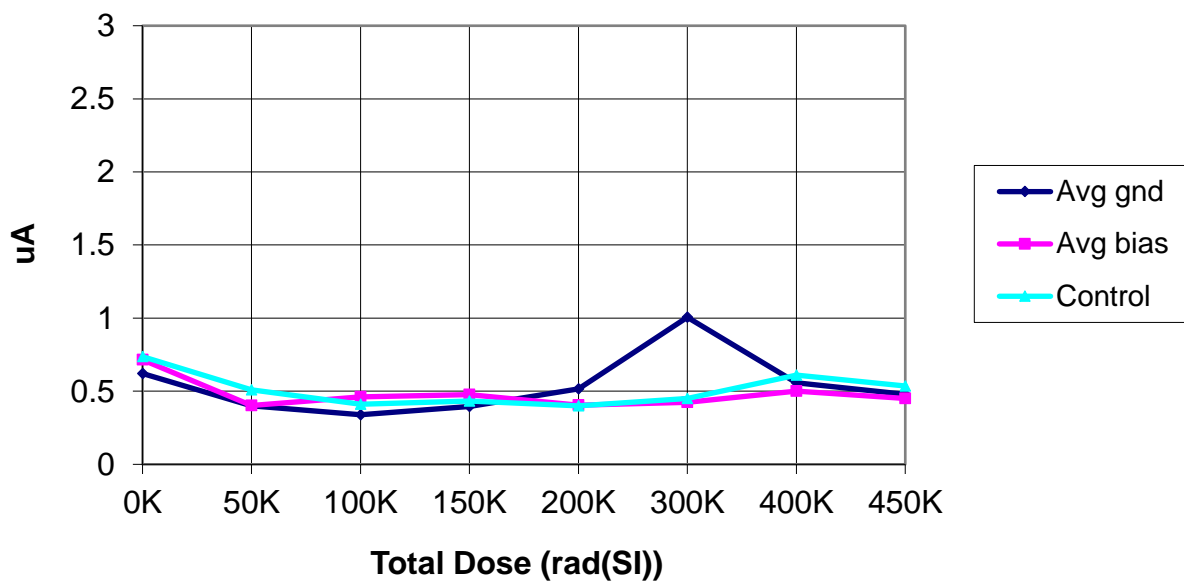
+VDD Current
+VDD = +15V



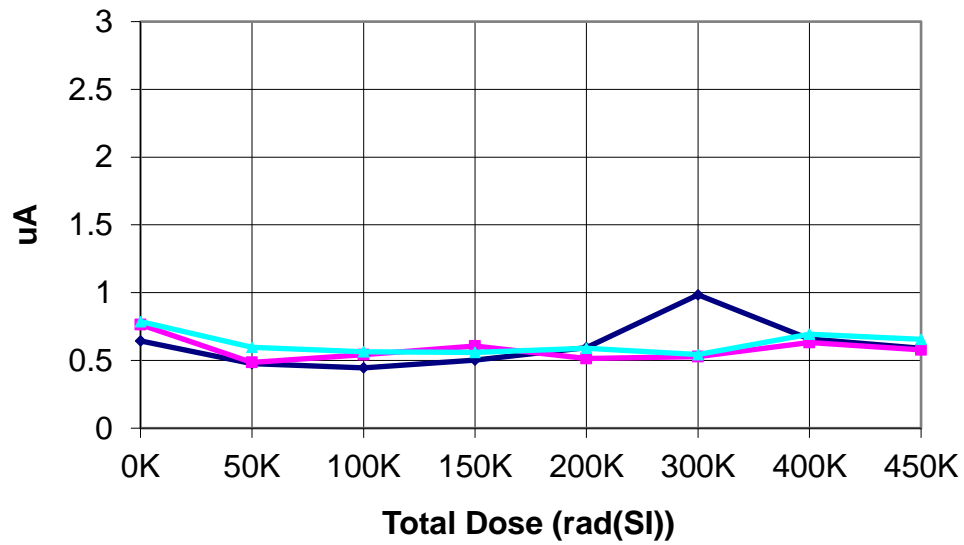
UVLO



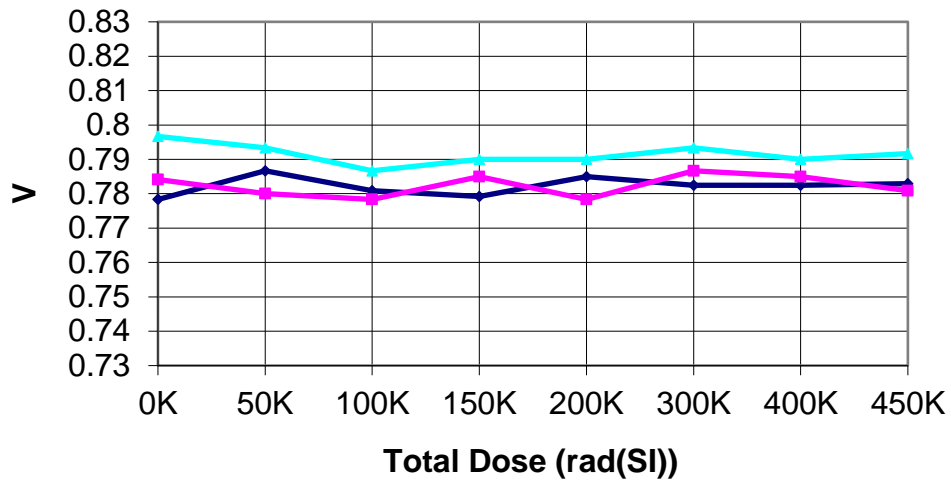
High Side Leakage $V_D = 80V$



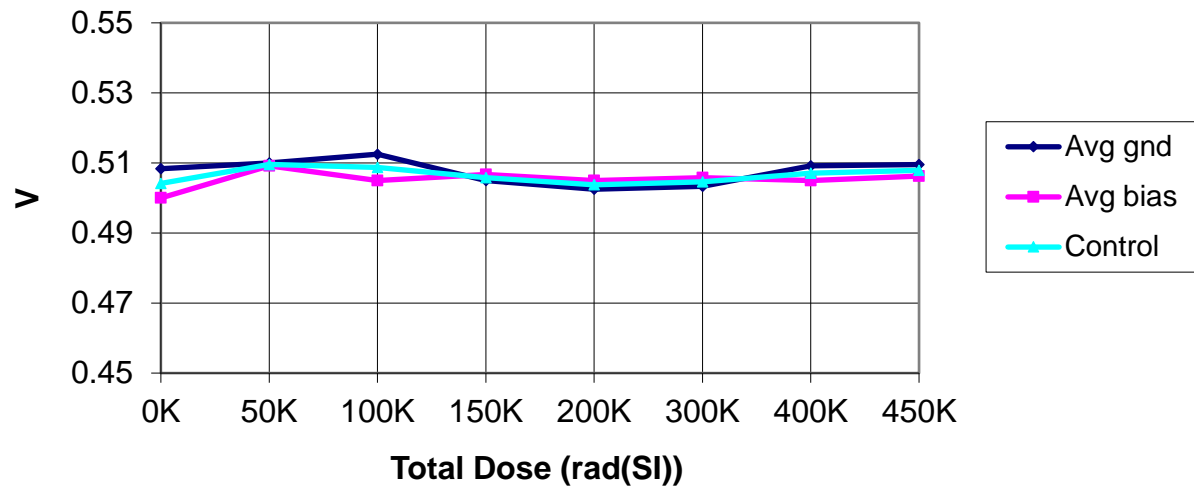
Low Side Leakage $V_D = 80V$



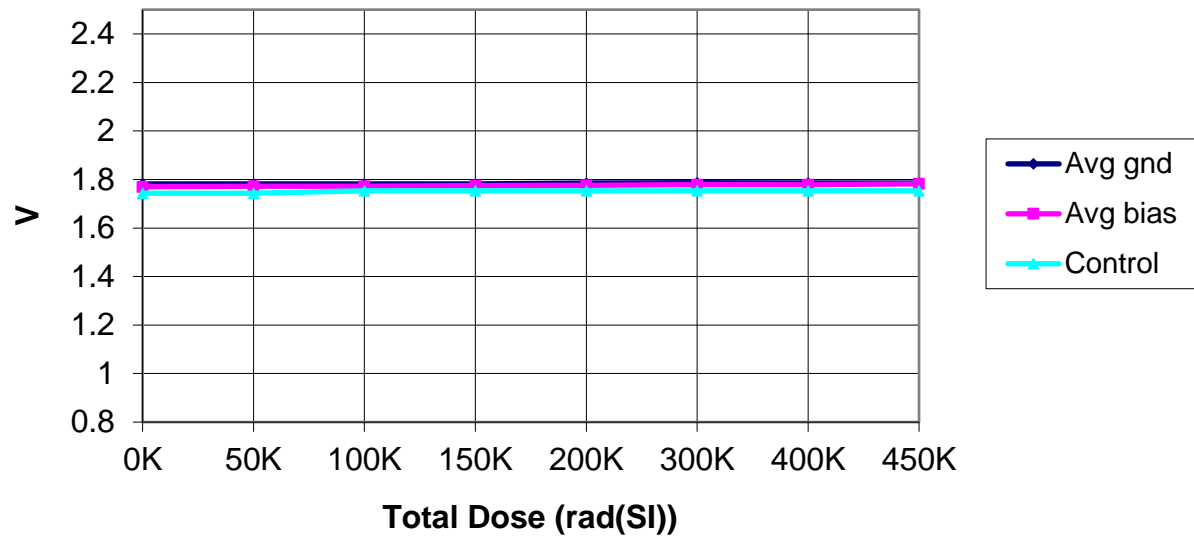
High Side VDS(on) $I_{DS} = 10A$



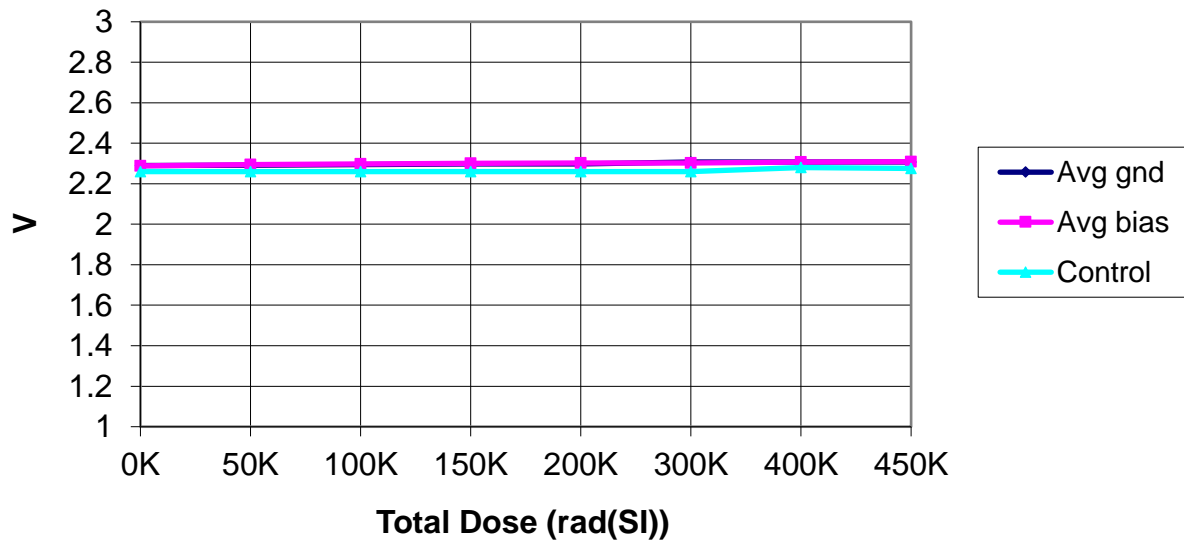
Low Side VDS(on) IDS = 10A



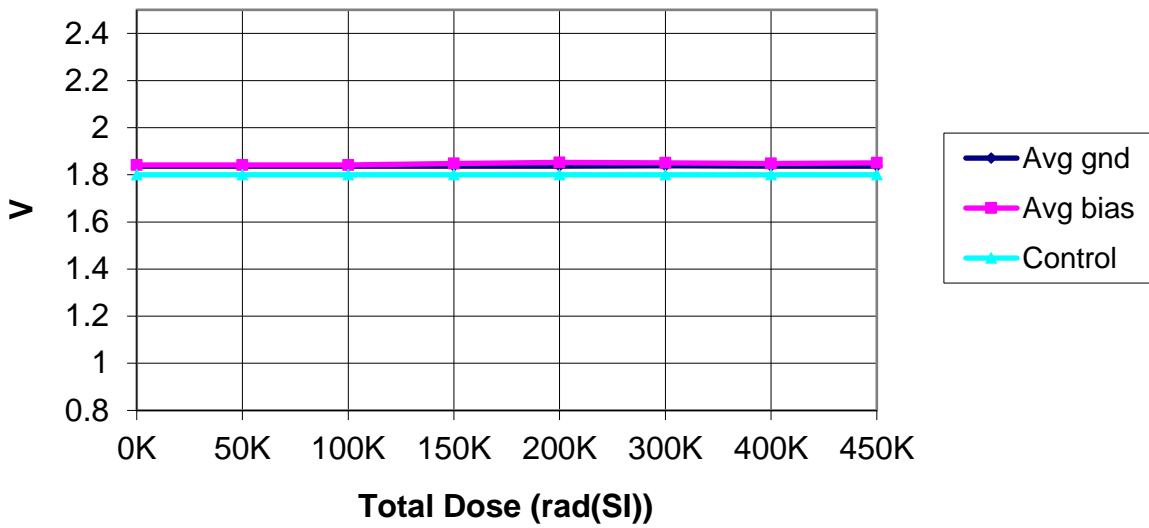
High Side Logic Low Input Threshold



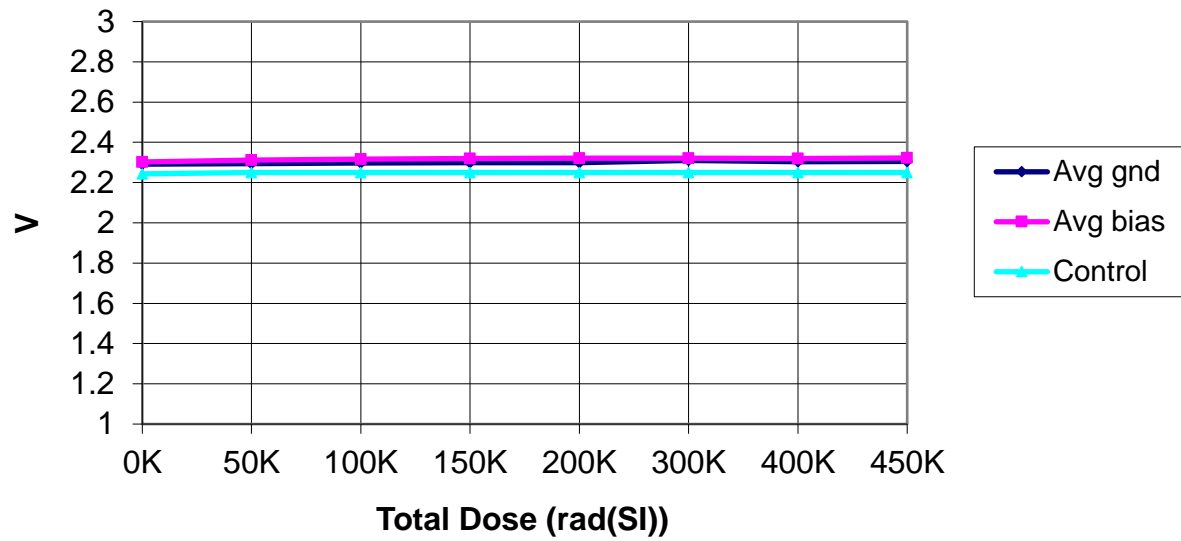
High Side Logic High Input Threshold



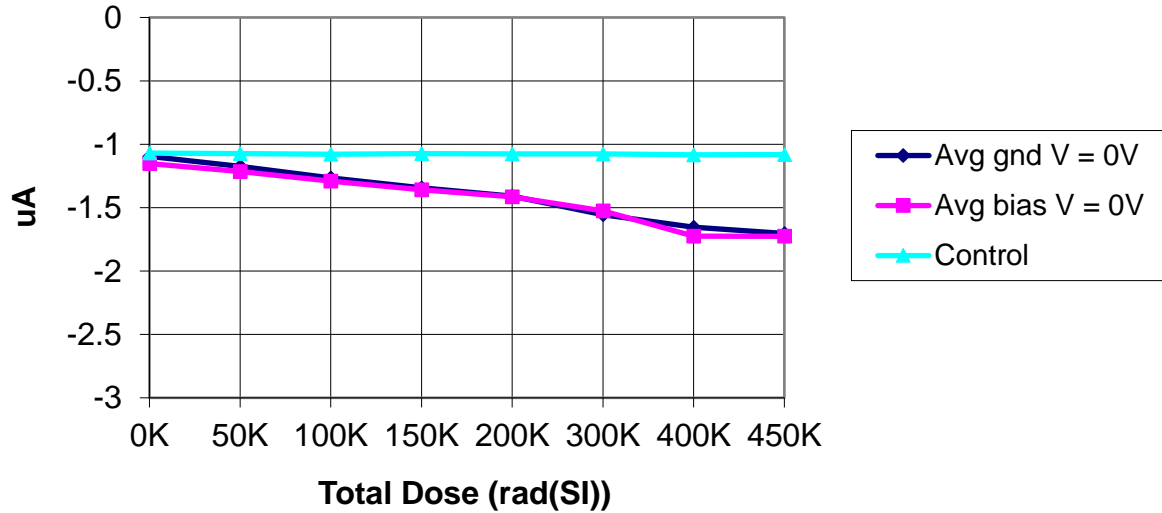
Low Side Logic Low Input Threshold



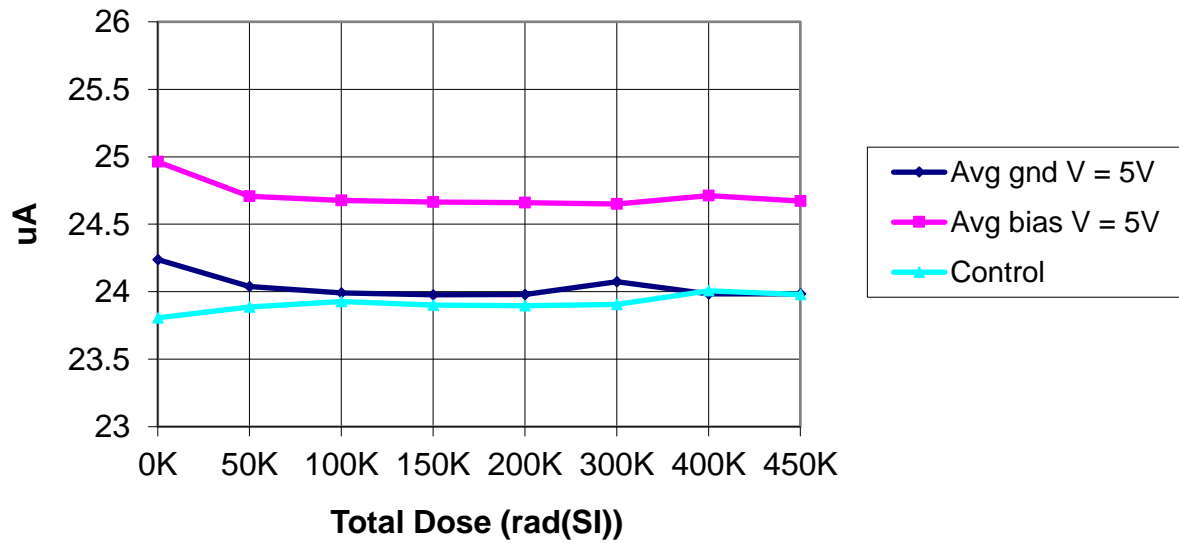
Low Side Logic High Input Threshold



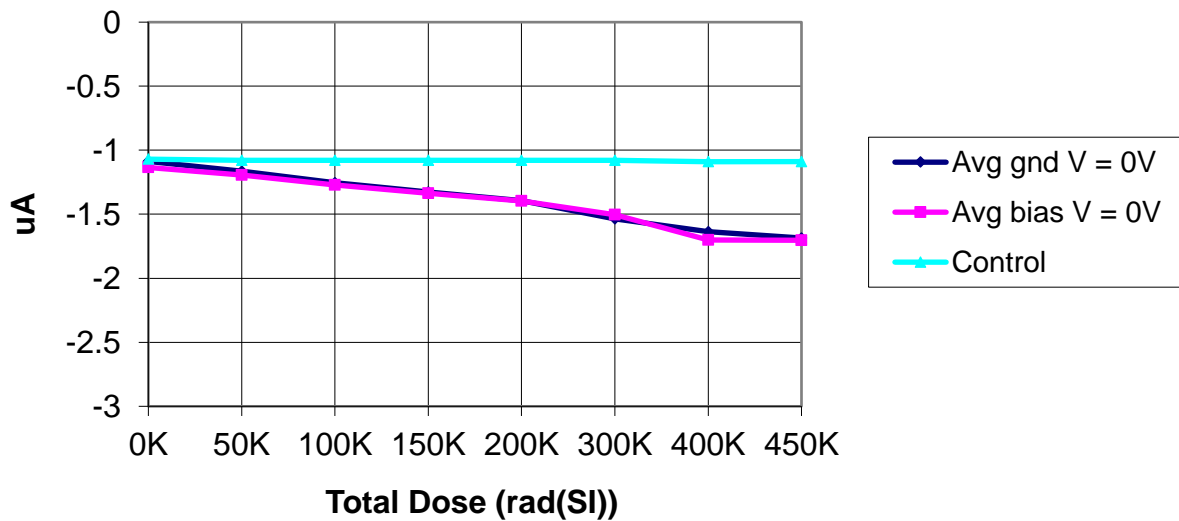
High Side Logic Input Current $V_{in} = 0V$



High Side Logic Input Current $V_{in} = 5V$



Low Side Logic Input Current $V_{in} = 0V$



Low Side Logic Input Current $V_{in} = 5V$

