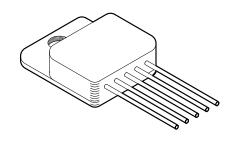


# DUAL POSITIVE/NEGATIVE, 3 AMP, LOW DROPOUT <u>FIXED VOLTAGE REGUL</u>ATORS

# 5000 SERIES

## FEATURES:

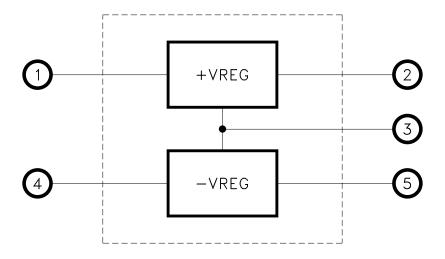
- · Dual Low Dropout Voltage
- Internal Short Circuit Current Limit
- Output Voltages Are Internally Set To ±1% Max
- · Electrically Isolated Case
- Internal Thermal Overload Protection
- · Many Output Voltage Combinations
- · Alternate Package and Lead Form Configurations Available
- Equivalent Rad Hard Device MSK5930RH Series



## **DESCRIPTION:**

The MSK5000 Series offers low dropout voltages on both the positive and negative regulators. This, combined with the low  $\theta$ JC, allows increased output current while providing exceptional device efficiency. Because of the increased efficiency, a small hermetic 5 pin package can be used providing maximum performance while occupying minimal board space. Output voltages are internally trimmed to  $\pm 1\%$  maximum resulting in consistent and accurate operation. Additionally, both regulators offer internal short circuit current and thermal limiting, which allows circuit protection and eliminates the need for external components and excessive derating.

# **EQUIVALENT SCHEMATIC**



# TYPICAL APPLICATIONS

- · High Efficiency Linear Regulators
- · Constant Voltage/Current Regulators
- · System Power Supplies
- Switching Power Supply Post Regulators

# PIN-OUT INFORMATION

- 1 +VIN
- 2 +VOUT
- 3 GND
- 4 -VIN
- 5 -VOUT

CASE = ISOLATED

# ABSOLUTE MAXIMUM RATINGS

9

+VIN	Input Voltage (WRT VOUT)	±30V
PD	Power Dissipation	Internally Limited
lout	Output Current	±3A
TJ	Junction Temperature	+150°C

Tst	Storage Temperature Range10	65°C to +150°C
TLD	Lead Temperature Range	
	(10 Seconds Soldering)	300°C
Tc	Case Operating Temperature Range	
	MSK5000-5009	40°C to +85°C
	MSK5000B-5009B	55°C to +125°C
	ESD Rating	Class 2

# **ELECTRICAL SPECIFICATIONS**

Parameter	Test Conditions (3)	Group A MSK5000B SERIES			MSK5000 SERIES			Units	
raiametei	Test conditions (3)	Subgroup	Min.	Тур.	Max.	Min.	Тур.	Max.	Ullits
POSITIVE OUTPUT REGULATORS:									
Output Voltage Tolerance	IOUT = 0A; VIN = VOUT +3V	1	-	0.1	1.0	-	0.1	2.0	%
Cutput voltage folcrafied		2, 3	-	0.1	2.0	-	-	-	%
Dropout Voltage 2	$0A \le IOUT \le 3A$ ; $\triangle VOUT = 50mV$	1	-	1.3	1.5	-	1.3	1.6	V
Load Regulation	100mA ≤ Iouт ≤ 3A VIN = VOUT +3V	1	-	0.2	1	-	0.2	2	%
Load Negulation		2, 3	-	0.3	2	-	-	-	%
Line Regulation	IOUT = 0A (VOUT +3V) ≤ VIN ≤ (VOUT +15V)	1	-	0.1	0.5	-	0.1	0.6	%
Line Regulation		2, 3	-	0.2	.75	-	-	-	%
Quiescent Current	VIN = VOUT +3V; IOUT = 0A	1, 2, 3	-	10	15	-	10	15	mA
Short Circuit Current 2	VIN = VOUT+5V	-	3.2	4	-	3.0	4	-	Α
Ripple Rejection 2	Iouт=3A; Couт=25µF; f=120Hz	-	60	75	-	60	75	-	dB
Thermal Resistance 2	JUNCTION TO CASE @ Tc = 125°C	-	-	4.3	4.5	-	4.3	4.6	°C/W
NEGATIVE OUTPUT REGULATORS: (8)									
Output Voltage Tolerance	IOUT = 0A; VIN = VOUT +3V	1	-	0.1	1.0	-	0.1	2.0	%
Output voltage Tolerance		2, 3	-	0.1	2.0	-	-	-	%
Dropout Voltage 2	$0A \le IOUT \le 3A$ ; $\triangle VOUT = 50mV$	1	-	8.0	1.2	-	8.0	1.3	V
Load Pagulation	VIN = VOUT +3V 100mA ≤ Iouт ≤ 3A	1	-	0.2	1	-	0.2	2	%
Load Regulation		2, 3	-	0.3	2	-	-	-	%
Line Degulation	I <sub>OUT</sub> = 0A (VOUT +3V) ≤ VIN ≤ (VOUT +15V)	1	-	.1	.5	-	.1	.6	%
Line Regulation		2, 3	-	.2	.75	-	-	-	%
Quiescent Current	VIN = VOUT +3V; Iout = 0A	1, 2, 3	-	4.5	10	-	4.5	10	mA
Short Circuit Current (2)	VIN = VOUT +5V	-	3.0	3.5	-	3.0	3.5	-	Α
Ripple Rejection 2	louт = 3A; Couт = 25µF; f = 120Hz	-	60	75	-	60	75	-	dB
Thermal Resistance 2	JUNCTION TO CASE @ Tc = 125°C	-	-	4.5	4.7	-	4.5	4.8	°C/W

# NOTES:

- Outputs are decoupled to ground using 33µF minimum low ESR capacitors unless otherwise specified.
- (2) Guaranteed by design but not tested. Typical parameters are representative of actual device performance but are for reference only.
- (3) All output parameters are tested using a low duty cycle pulse to maintain T<sub>J</sub> = Tc
- (4) Industrial devices shall be tested to subgroup 1 unless otherwise specified.
- (5) Military grade devices ("B" suffix) shall be 100% tested to subgroups 1, 2 and 3.
- (6) Subgroup 1  $TA = Tc = +25^{\circ}C$ 
  - 2 TA = Tc = +125°C
  - 3 TA =  $Tc = -55^{\circ}C$
- Please consult the factory if alternate output voltages are required.
- (8) Input voltage (VIN = VOUT + a specified voltage) is implied to be more negative than VOUT.
- (9) Continuous operation at or above absolute maximum ratings may adversely effect the device performance and/or life cycle.
- (10) Internal solder reflow temperature is 180°C, do not exceed.

PART NUMBER	OUTPUT VOLTAGES		
7	POSITIVE	NEGATIVE	
MSK5000	+3.3V	-5.2V	
MSK5001	+5.0V	-5.0V	
MSK5002	+5.0V	-5.2V	
MSK5003	+12.0V	-5.0V	
MSK5004	+12.0V	-12.0V	
MSK5005	+15.0V	-15.0V	
MSK5006	+15.0V	-5.0V	
MSK5007	+5.0V	-12.0V	
MSK5008	+5.0V	-15.0V	
MSK5009	+10.0V	-10.0V	

# **APPLICATION NOTES**

## **BYPASS CAPACITORS**

For most applications a 33uF minimum, low ESR (0.5-2 ohm) tantalum capacitor should be attached as close to the regulator's output as possible. This will effectively lower the regulator's output impedance, increase transient response and eliminate any oscillations that are normally associated with low dropout regulators. Additional bypass capacitors can be used at the remote load locations to further improve regulation. These can be either of the tantalum or the electrolytic variety. Unless the regulator is located very close to the power supply filter capacitor(s), a 4.7uF minimum low ESR (0.5-2 ohm) tantalum capacitor should also be added to the regulator's input. An electrolytic may also be substituted if desired. When substituting electrolytic in place of tantalum capacitors, a good rule of thumb to follow is to increase the size of the electrolytic by a factor of 10 over the tantalum value.

#### LOAD REGULATION

For best results the ground pin should be connected directly to the load as shown below, this effectively reduces the ground loop effect and eliminates excessive voltage drop in the sense leg. It is also important to keep the output connection between the regulator and the load as short as possible since this directly affects the load regulation. For example, if 20 gauge wire were used which has a resistance of about .008 ohms per foot, this would result in a drop of 8mV/ft at 1Amp of load current. It is also important to follow the capacitor selection guidelines to achieve best performance. Refer to Figure 2 for connection diagram.

#### **MSK5002 TYPICAL APPLICATION:**

Low Dropout Positive and Negative Power Supply

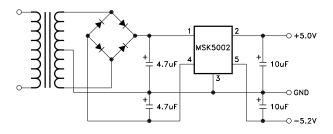
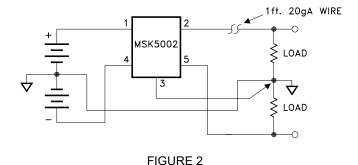


FIGURE 1

#### Avoiding Ground Loops



#### OVERLOAD SHUTDOWN

The regulators feature both power and thermal overload protection. When the maximum power dissipation is not exceeded, the regulators will current limit slightly above their 3 amp rating. As the VIN-VOUT voltage increases, however, shutdown occurs in relation to the maximum power dissipation curve. If the device heats enough to exceed its rated die junction temperature due to excessive ambient temperature, improper heat sinking etc., the regulators also shutdown until an appropriate junction temperature is maintained. It should also be noted that in the case of an extreme overload, such as a sustained direct short, the device may not be able to recover. In these instances, the device must be shut off and power reapplied to eliminate the shutdown condition.

#### **HEAT SINKING**

To determine if a heat sink is required for your application and if so, what type, refer to the thermal model and governing equation below.

Governing Equation: Tj = Pd x (R $\theta$ jc + R $\theta$ cs + R $\theta$ sa) + Ta

#### WHERE

Tj = Junction Temperature

Pd = Total Power Dissipation

 $R\theta$ jc = Junction to Case Thermal Resistance

 $R\theta cs$  = Case to Heat Sink Thermal Resistance

Rθsa = Heat Sink to Ambient Thermal Resistance

Tc = Case Temperature

Ta = Ambient Temperature

Ts = Heat Sink Temperature

## **EXAMPLE**:

This example demonstrates an analysis where each regulator is at one-half of its maximum rated power dissipation, which occurs when the output currents are at 1.5 amps each.

Conditions for MSK5002:

 $VIN = \pm 7.0V$ ; lout =  $\pm 1.5A$ 

- 1.) Assume 45° heat spreading model.
- 2.) Find positive regulator power dissipation:

- 3.) For conservative design, set Tj = +125°C Max.
- 4.) For this example, worst case Ta = +90°C.
- 5.)  $R\theta jc = 4.5$ °C/W from the Electrical Specification Table.
- 6.) R $\theta$ cs = 0.15°C/W for most thermal greases.
- 7.) Rearrange governing equation to solve for  $R\theta$ sa:

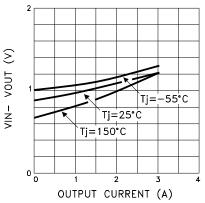
```
Rθsa = ((Tj - Ta)/Pd) - (Rθjc) - (Rθcs)
= (125°C - 90°C)/3.0W - 4.5°C/W - 0.15°C/W
= 7.0°C/W
```

The same exercise must be performed for the negative regulator. In this case the result is 7.0°C/W. Therefore, a heat sink with a thermal resistance of no more than 7.0°C/W must be used in this application to maintain both regulator circuit junction temperatures under 125°C.

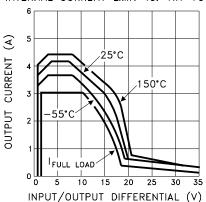
# TYPICAL PERFORMANCE CURVES

## POSITIVE REGULATORS

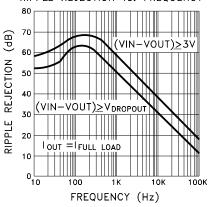
#### DROPOUT VOLTAGE vs. OUTPUT CURRENT



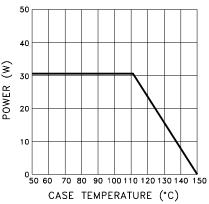
## INTERNAL CURRENT LIMIT vs. VIN-VOUT



RIPPLE REJECTION vs. FREQUENCY

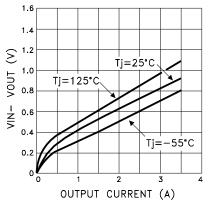


POWER DISSIPATION vs. TEMPERATURE

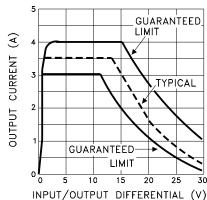


# **NEGATIVE REGULATORS**

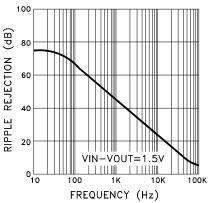
#### DROPOUT VOLTAGE vs. OUTPUT CURRENT



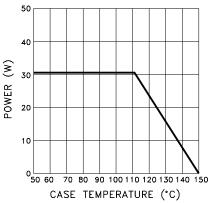
INTERNAL CURRENT LIMIT vs. VIN-VOUT

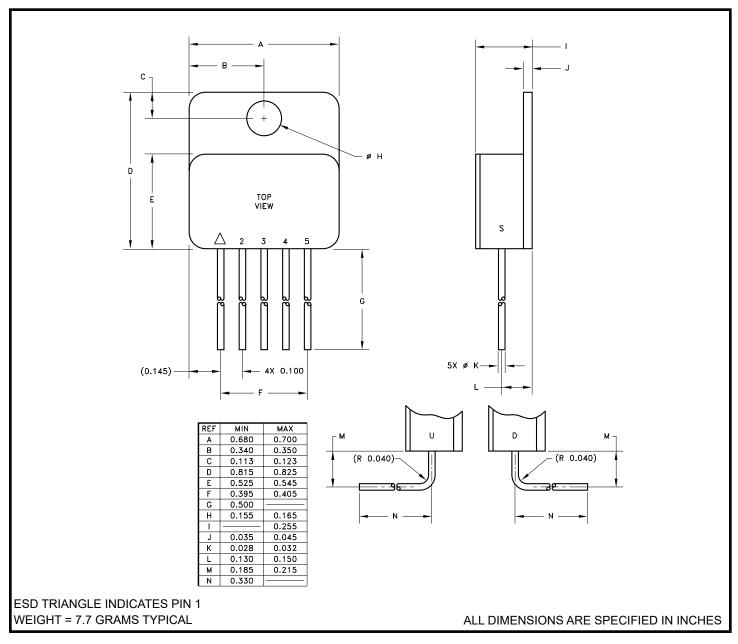


RIPPLE REJECTION vs. FREQUENCY

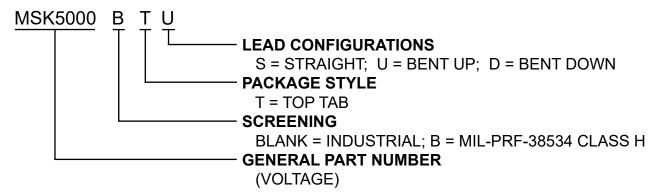


POWER DISSIPATION vs. TEMPERATURE



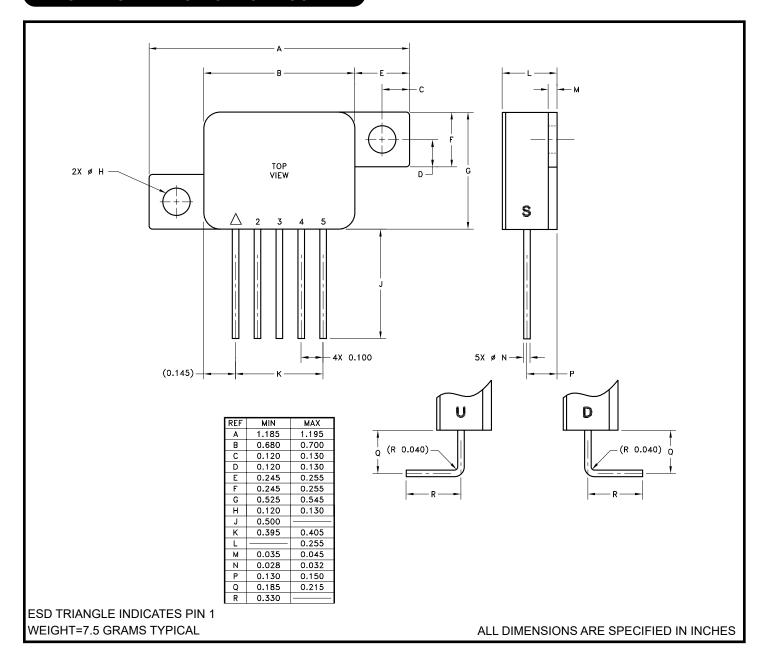


# ORDERING INFORMATION

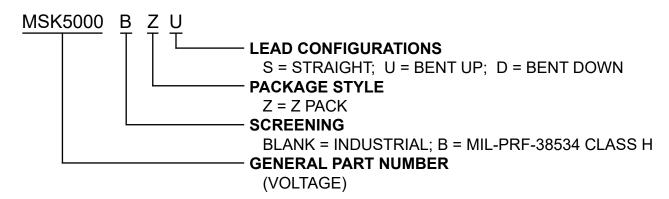


THE ABOVE EXAMPLE IS A +3.3V, -5.2V MILITARY REGULATOR USING THE TOP TAB PACKAGE WITH LEADS BENT UP.

# MECHANICAL SPECIFICATIONS CONT'D



# ORDERING INFORMATION



# **REVISION HISTORY**

REV	STATUS	DATE	DESCRIPTION
K	Released	02/15	Add internal note and clarify mechanical specifications
L	Released	1/23	Remove MIL-PRF-38535, update company name and website

# TTM Technologies

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