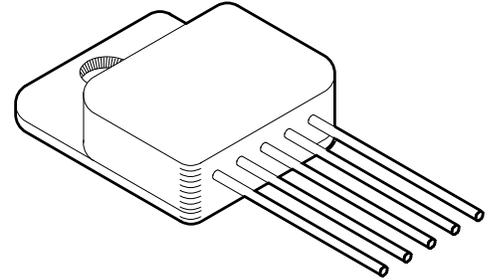


# HIGH CURRENT, SUPER LOW DROPOUT FIXED VOLTAGE REGULATORS

# 5010 SERIES

## FEATURES:

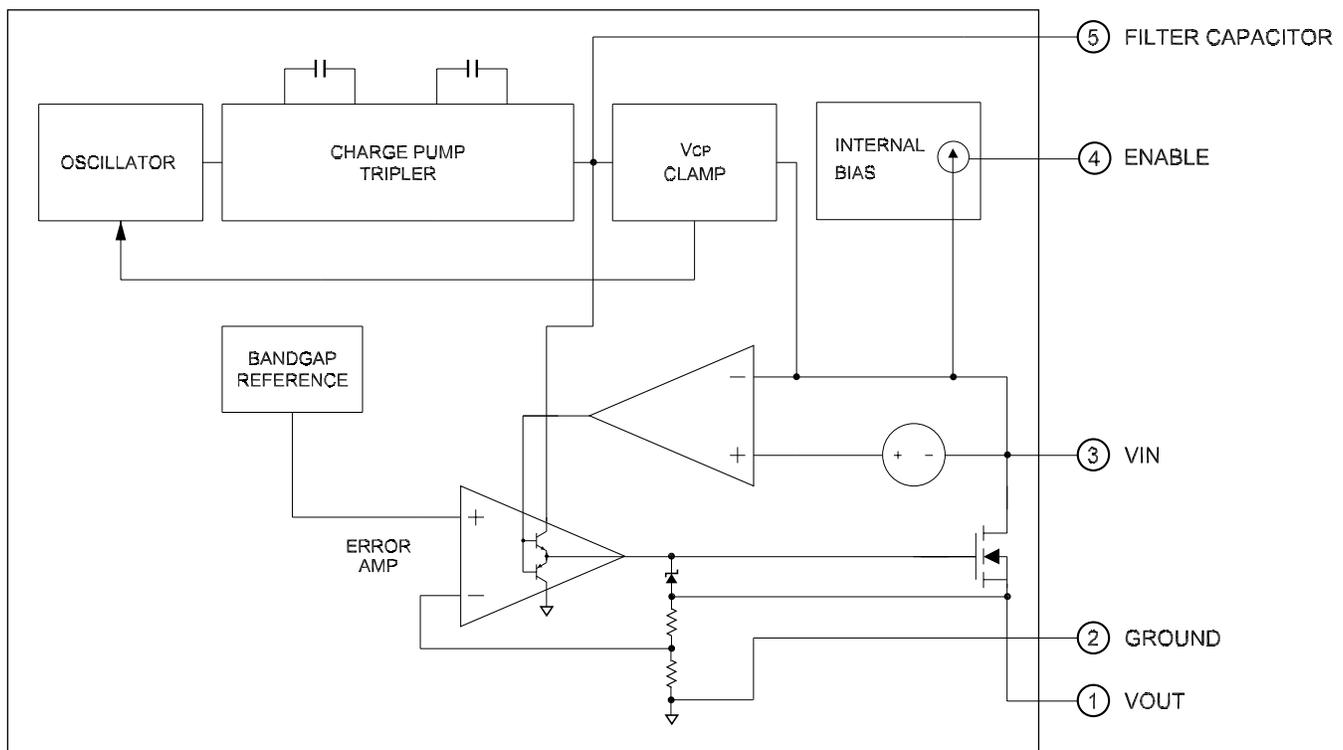
- Extremely Low Dropout Voltage 0.45V @ 10 Amps
- Available in 3.3V, 5.0V and 12.0V versions
- Output Voltages are internally set to  $\pm 1.5\%$  maximum
- TTL Level Enable Pin
- Electrically Isolated Case
- Low Quiescent Current
- Output Current to 10 Amps
- Available in Two Package Styles
- Available with Three Lead Form Options
- Contact TTM Technologies for MIL-PRF-38534 Qualification Status



## DESCRIPTION:

The MSK5010 series voltage regulators are available in a +3.3V, +5.0V or +12.0V fixed output configuration. All three boast ultra low dropout specifications due to the utilization of a MOSFET output pass transistor with extremely low  $R_{ds(on)}$ . Dropout voltages of 0.45V at 10 amps are typical, in this configuration, which drives efficiency up and power dissipation down. Accuracy is guaranteed with a 1.5% maximum initial output voltage tolerance that varies only  $\pm 2\%$  with temperature. The MSK 5010 series is packaged in a space efficient 5 pin SIP that is electrically isolated from the internal circuitry allowing direct heat sinking for maximum heat dissipation.

## EQUIVALENT SCHEMATIC



## TYPICAL APPLICATIONS

- High Efficiency, High Current Linear Regulators
- Constant Voltage/Current Regulators
- System Power Supplies
- Switching Power Supply Post Regulators

## ABSOLUTE MAXIMUM RATINGS

⑨

V <sub>IN</sub>	Input Voltage	36V
V <sub>EN</sub>	Enable Voltage	-0.3V to 36V
I <sub>OUT</sub>	Output Current	10A within SOA
T <sub>J</sub>	Junction Temperature	+175°C

T <sub>ST</sub>	Storage Temperature Range	⑩ -65°C to +150°C
T <sub>LD</sub>	Lead Temperature Range (10 Seconds Soldering)	300°C
T <sub>C</sub>	Case Operating Temperature Range	
	MSK5010 Series	-40°C to +85°C
	MSK5010B Series	-55°C to +125°C

## ELECTRICAL SPECIFICATIONS

Parameter	Test Conditions ③	Group A Subgroup	MSK5010B SERIES			MSK5010 SERIES			Units
			Min.	Typ.	Max.	Min.	Typ.	Max.	
Output Voltage Tolerance	I <sub>OUT</sub> = 100mA; V <sub>IN</sub> = V <sub>OUT</sub> +3V	1	-	±0.3	±1.5	-	±0.5	±1.5	%
		2, 3	-	±1.0	±2.0	-	-	-	%
Dropout Voltage ②	I <sub>OUT</sub> = 0A	1	-	0.002	0.010	-	0.002	0.015	V
	I <sub>OUT</sub> = 10A	1	-	0.45	0.75	-	0.5	1.0	V
Load Regulation	V <sub>IN</sub> = V <sub>OUT</sub> +3V 100mA ≤ I <sub>OUT</sub> ≤ 9A	1	-	±0.5	±1.0	-	±0.5	±1.5	%
		2, 3	-	±0.5	±2.0	-	-	-	%
Line Regulation	(V <sub>OUT</sub> + 1.5) ≤ V <sub>IN</sub> ≤ (V <sub>OUT</sub> + 15), I <sub>OUT</sub> = 100mA	1	-	±0.3	±1.0	-	±0.5	±1.5	%
		2, 3	-	±0.5	±1.0	-	-	-	%
Quiescent Current	V <sub>EN</sub> = V <sub>IN</sub> ; V <sub>IN</sub> = V <sub>OUT</sub> + 3V; I <sub>OUT</sub> = 0A	1, 2, 3	-	4.5	10	-	4.5	10	mA
	② V <sub>EN</sub> = 0V; V <sub>IN</sub> = V <sub>OUT</sub> + 3V; I <sub>OUT</sub> = 0A	1, 2, 3	-	0.15	5.0	-	0.15	5.0	µA
Ripple Rejection ②	f = 120Hz	-	-	45	-	-	45	-	dB
Thermal Resistance ②	Junction to Case	-	-	0.7	0.8	-	0.7	1.0	°C/W
Enable Input Voltage ②	HIGH	1	2.4	1.2	-	2.4	1.2	-	V
	LOW	1	-	1.2	0.8	-	1.2	0.8	V
Enable Input Current ②	HIGH	1	-	12.5	50	-	20	50	µA
	LOW	1	-	0.8	25	-	10	25	µA
Output Settling Time ② ⑧	To within 10mV	-	-	5	-	-	5	-	µS
Input Voltage Range ②		-	3	-	36	3	-	36	V

### NOTES:

- ① Output decoupled to ground using 47µF minimum capacitor unless otherwise specified.
- ② Guaranteed by design but not tested. Typical parameters are representative of actual device performance but are for reference only.
- ③ All output parameters are tested using a low duty cycle pulse to maintain T<sub>J</sub> = T<sub>C</sub>.
- ④ Industrial grade devices shall be tested to subgroup 1 unless otherwise specified.
- ⑤ Military grade devices ('B' suffix) shall be 100% tested to subgroups 1, 2 and 3.
- ⑥ Subgroup 1 T<sub>A</sub> = T<sub>C</sub> = +25°C  
Subgroup 2 T<sub>A</sub> = T<sub>C</sub> = +125°C  
Subgroup 3 T<sub>A</sub> = T<sub>C</sub> = -55°C
- ⑦ Please consult the factory if alternate output voltages are required.
- ⑧ After application of enable pulse.
- ⑨ Continuous operation at or above absolute maximum ratings may adversely effect the device performance and/or life cycle.
- ⑩ Internal solder reflow temperature is 180°C, do not exceed.

PART NUMBER ⑦	OUTPUT VOLTAGE
MSK5010-3.3	+3.3V
MSK5010-5.0	+5.0V
MSK5010-12	+12.0V

## APPLICATION NOTES

### MINIMIZING OUTPUT RIPPLE:

The output voltage ripple of the MSK 5010 series voltage regulators can be minimized by placing a filter capacitor from the output to ground. The optimum value for this capacitor may vary from one application to the next and is best determined by experimentation. Transient load response can also be improved by placing a (10uF to 20uF) capacitor directly across the load.

### CASE CONNECTIONS:

The case of the MSK 5010 is electrically isolated from the internal circuitry so that a direct connection can be made to the heat sink for most efficient heat dissipation. However, it may be necessary in some applications to connect the case to ground. Grounding the case will help eliminate oscillations and produce a clean, noise free output.

### LOAD REGULATION:

For best results, the ground pin should be connected directly to the load (see next note). This effectively reduces the ground loop effect and eliminates excessive voltage drop in the sense leg. It is also important to keep the output connection between the regulator and the load as short as possible since this directly affects the load regulation. For example, if 20 gauge wire were used which has a resistance of about .008 ohms per foot, this would result in a drop of 8mV/ft at a load current of 1 am.

### LOAD CONNECTIONS:

In voltage regulator applications where very large load currents are present, the load connection is very important. The path connecting the output of the regulator to the load must be extremely low impedance to avoid affecting the load regulation specifications. Any impedance in this path will form a voltage divider with the load. The same holds true for the connection from the low end of the load to ground. For best load regulation, the low end of the load must be connected directly to pin 2 of the MSK 5010 and not to a ground plane inches away from the hybrid.

### ENABLE/SHUTDOWN PIN:

The MSK 5010 series of voltage regulators are equipped with a TTL compatible ENABLE pin. A TTL high level on this pin activates the internal bias circuit and powers up the device. A TTL low level on this pin places the controller in shutdown mode and the device draws a maximum of 5µA of quiescent current.

### CHARGE CAPACITOR:

For all applications, the user must connect a 1.0uF capacitor from pin 5 directly to ground. This capacitor is part of the circuit which drives the gate of the internal MOSFET. Approximately three times the voltage seen on the input will appear across this capacitor. Careful attention must be paid to capacitor voltage rating since voltages larger than the power supply are present.

### HEAT SINK SELECTION:

To select a heat sink for the MSK5010, the following formula for convective heat flow must be used.

Governing Equation:

$$T_j = P_d \times (R_{\theta jc} + R_{\theta cs} + R_{\theta sa}) + T_a$$

WHERE:

$T_j$  = Junction Temperature  
 $P_d$  = Total Power Dissipation  
 $R_{\theta jc}$  = Junction to Case Thermal Resistance  
 $R_{\theta cs}$  = Case to Heat Sink Thermal Resistance  
 $R_{\theta sa}$  = Heat Sink to Ambient Thermal Resistance  
 $T_a$  = Ambient Temperature

First, the power dissipation must be calculated as follows:

$$\text{Power Dissipation} = (V_{in} - V_{out}) \times I_{out}$$

Next, the user must select a maximum junction temperature. The absolute maximum allowable junction temperature is 175°C. The equation may now be rearranged to solve for the required heat sink to ambient thermal resistance ( $R_{\theta sa}$ ).

EXAMPLE:

An MSK5010-3.3 is configured for  $V_{in} = +7V$  and  $V_{out} = +3.3V$ .  $I_{out}$  is a continuous 10A DC level. The ambient temperature is +25°C. The maximum desired junction temperature is 150°C.  $R_{\theta jc} = 0.8^\circ C/W$  and  $R_{\theta cs} = 0.15^\circ C/W$  typically.

$$\begin{aligned} \text{Power Dissipation} &= (7V - 3.3V) \times (10A) \\ &= 37 \text{ Watts} \end{aligned}$$

Solve for  $R_{\theta sa}$ :

$$R_{\theta sa} = \left[ \frac{150^\circ C - 25^\circ C}{37W} \right] - 0.8^\circ C/W - 0.15^\circ C/W$$

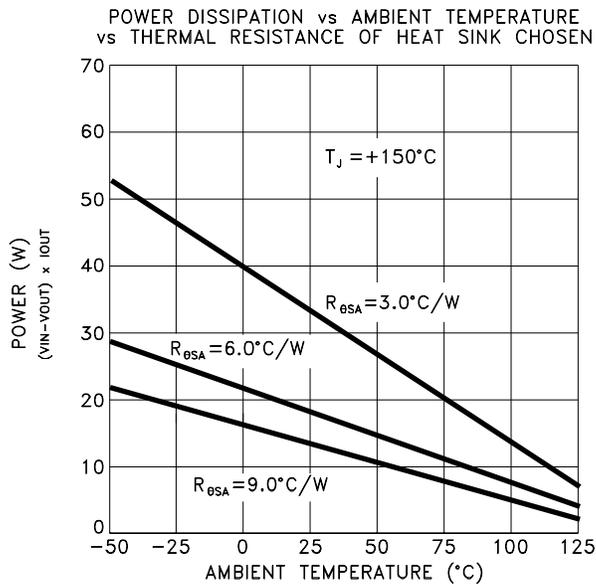
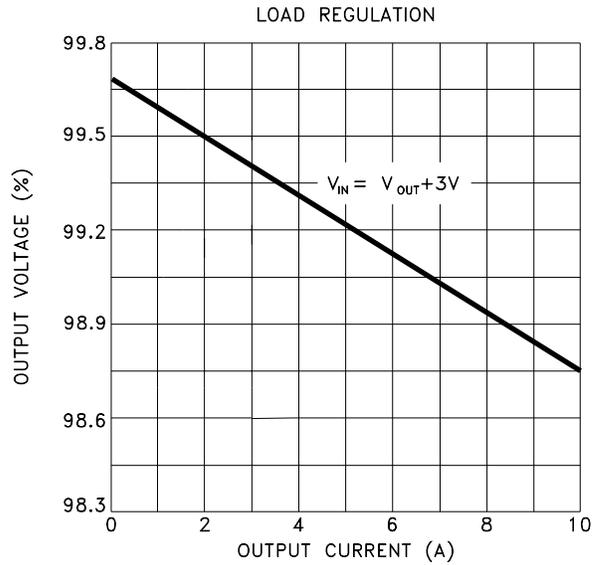
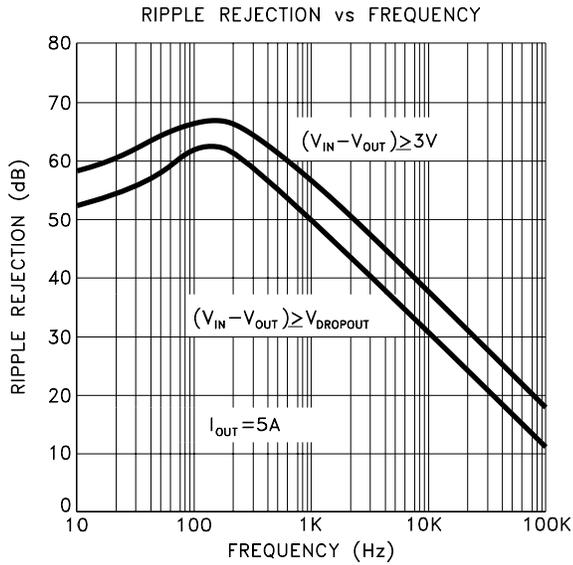
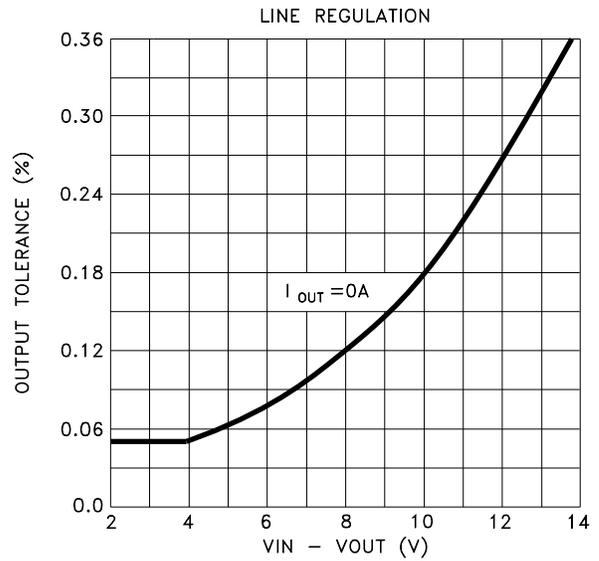
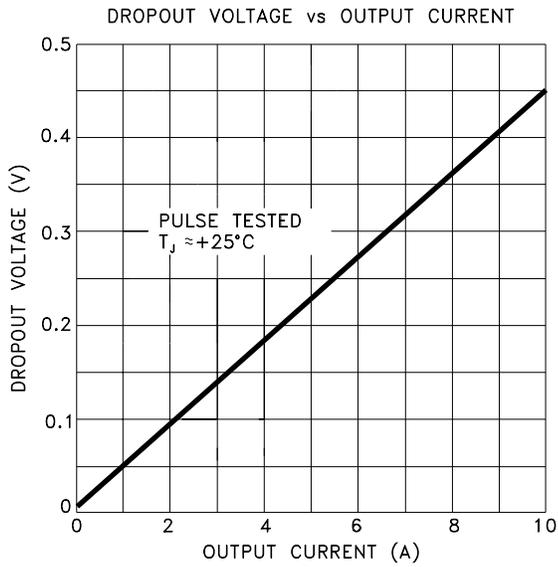
In this example, a heat sink with a thermal resistance of no more than 2.43°C/W must be used to maintain a junction temperature of no more than 150°C. The Thermalloy Corporation makes a heat sink with a thermal resistance of 2.2°C/W that would work well for this application.

(See Thermalloy part number 7023)

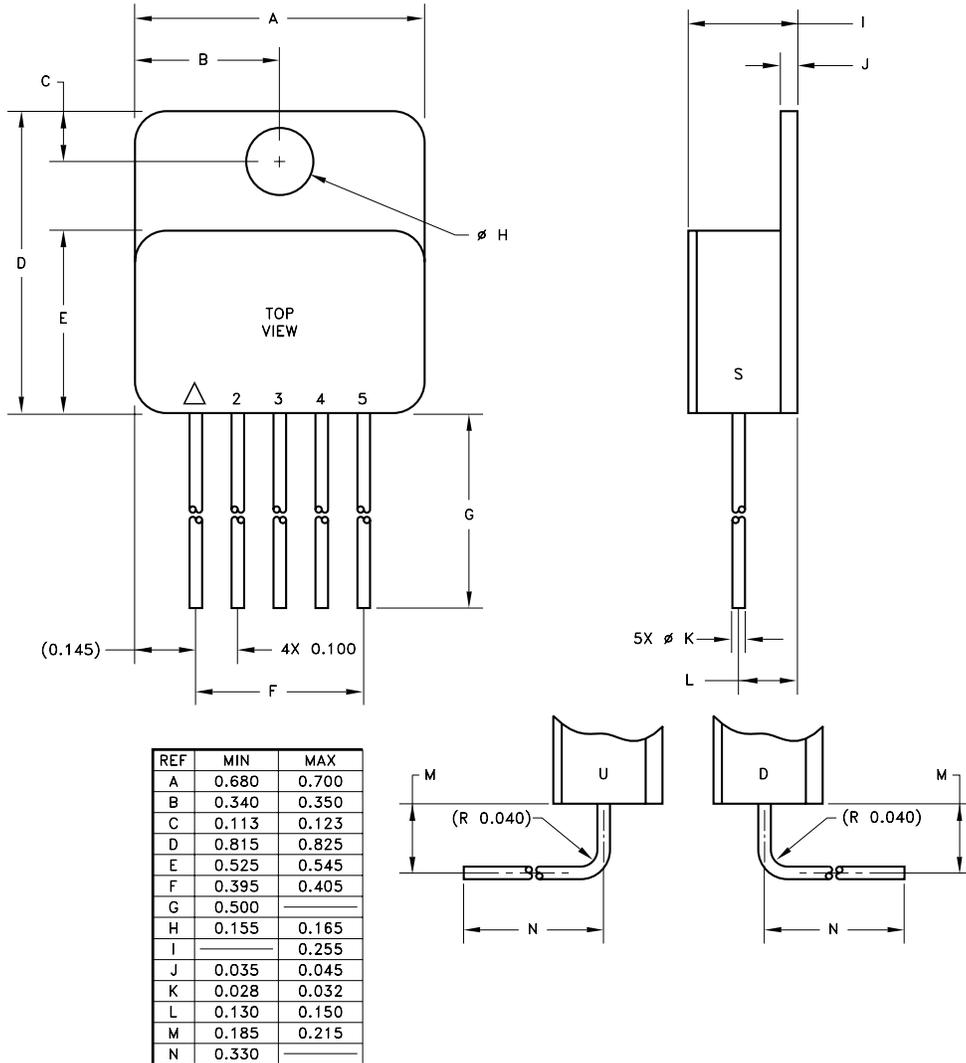
### POWER DISSIPATION:

The output pass transistor in the MSK5010 is rated to dissipate nearly 100 watts. The limiting factor of this device is effective dissipation of heat generated under such conditions. Careful consideration must be paid to heat dissipation and junction temperature when applying this device.

# TYPICAL PERFORMANCE CURVES



# MECHANICAL SPECIFICATIONS



ESD TRIANGLE INDICATES PIN 1  
WEIGHT = 7.7 GRAMS TYPICAL

ALL DIMENSIONS ARE SPECIFIED IN INCHES

## ORDERING INFORMATION

MSK5010-3.3 B T U

### LEAD CONFIGURATIONS

S = STRAIGHT; U = BENT UP; D = BENT DOWN

### PACKAGE STYLE

T = TOP TAB

### SCREENING

BLANK = INDUSTRIAL; B = MIL-PRF-38534 CLASS H

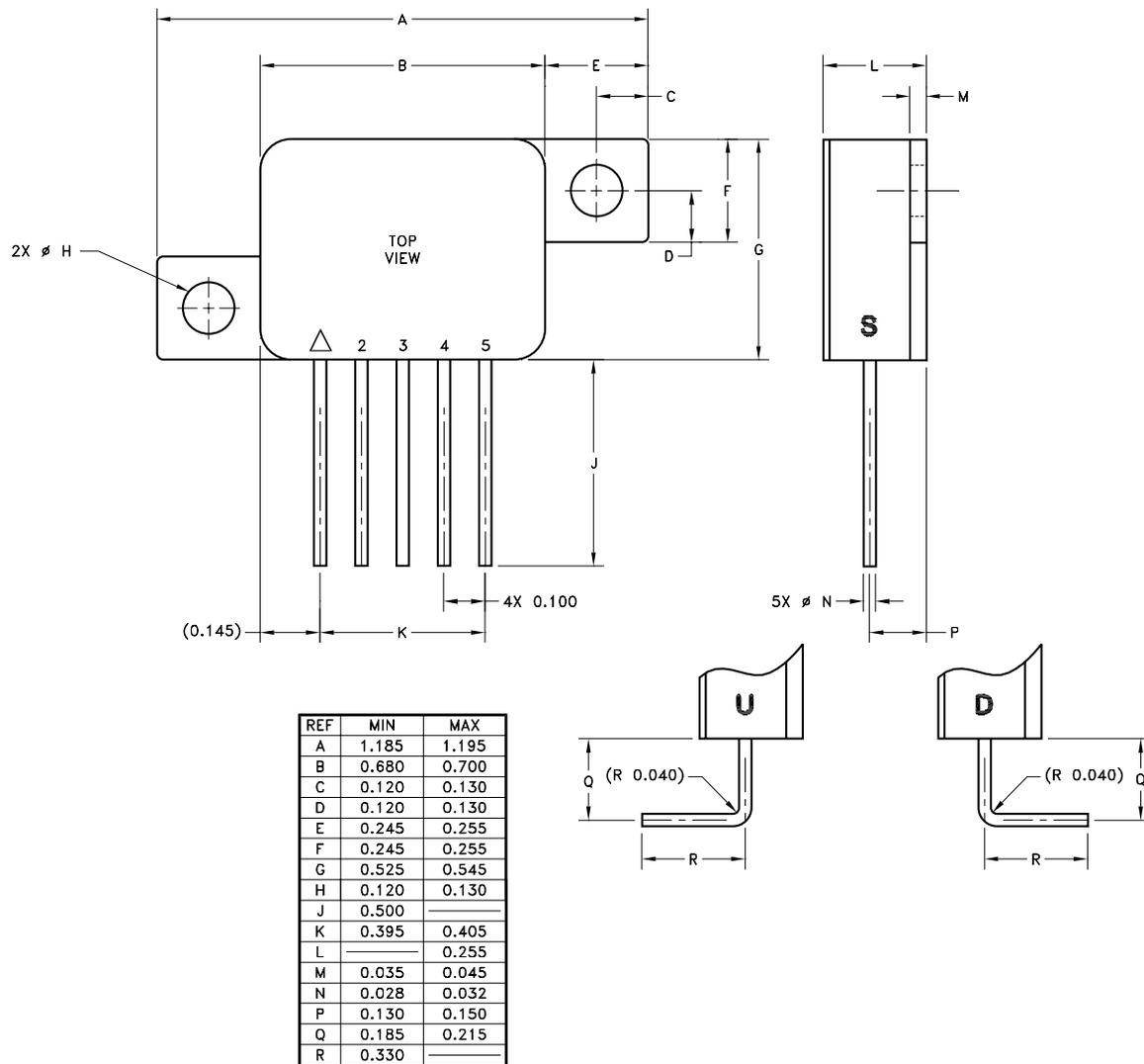
### OUTPUT VOLTAGE

3.3 = +3.3V; 5 = +5.0V; 12 = +12.0V

### GENERAL PART NUMBER

THE ABOVE EXAMPLE IS A +3.3V, MILITARY REGULATOR USING THE TOP TAB PACKAGE WITH LEADS BENT UP.

# MECHANICAL SPECIFICATIONS CONT'D

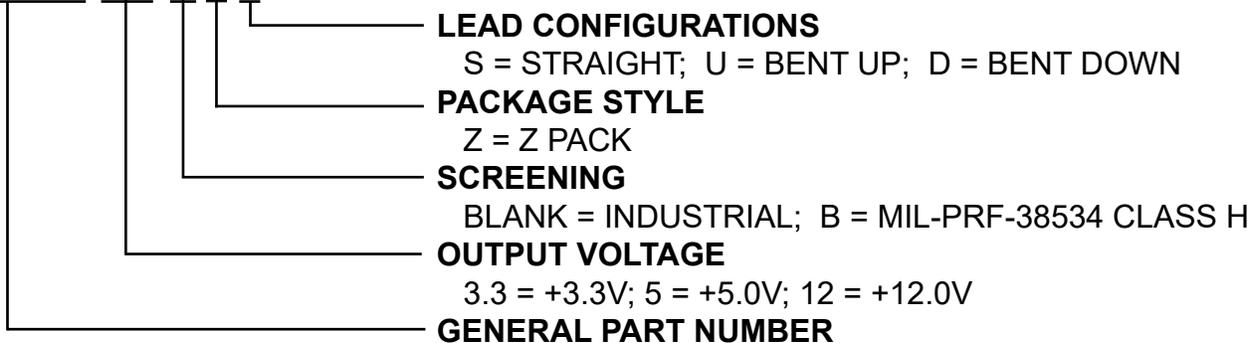


ESD TRIANGLE INDICATES PIN 1  
 WEIGHT = 8 GRAMS TYPICAL

ALL DIMENSIONS ARE SPECIFIED IN INCHES

## ORDERING INFORMATION

MSK5010-3.3 B Z U



## REVISION HISTORY

REV	STATUS	DATE	DESCRIPTION
J	Released	04/15	Add internal note and clarify mechanical specifications.
K	Released	01/23	Remove MIL-PRF-38535, update company name and website

TTM Technologies  
[www.ttm.com](http://www.ttm.com)

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