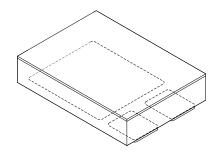


HIGH CURRENT, LOW DROPOUT SURFACE MOUNT VOLTAGE REGULATORS

5251 SERIES

FEATURES:

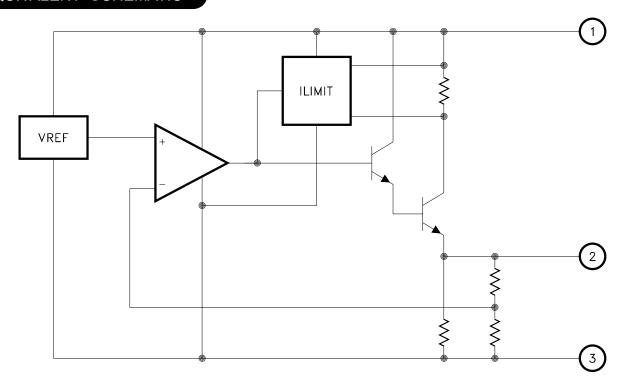
- · Ultra-Fast Transient Response
- Very Low Output Voltage
- Available in 0.8V, 0.9V, 1.0V, 1.2V, 1.3V, 1.4V and 1.5V
- · On Board Thermal Shut Down
- · Reverse Battery and Load Dump Protection
- 1% Maximum Guaranteed Accuracy
- · Output Current to 5 Amps
- · Alternate Output Voltages Available
- Ultra-Low Package Height 0.110" Max.
- · Contact TTM Technologies for MIL-PRF-38534 Qualification Status



DESCRIPTION:

The MSK5251 series voltage regulators offer high current and low output voltage capability ideal for use with low voltage microprocessors. Low output impedance, very fast transient response and minimal output capacitance requirements make the MSK5251 an excellent choice for ASIC and FPGA core voltage supplies. The device is available in +0.8V, +0.9V, +1.0V, +1.2V, +1.3V, +1.4V and +1.5V output configurations with output accuracy guaranteed to 1% maximum. The MSK5251 series is packaged in a low profile 3 pin hermetically sealed power surface mount ceramic package.

EQUIVALENT SCHEMATIC



TYPICAL APPLICATIONS

- PLD/FPGA Core Power Supply
- · ASIC Core Voltage Regulator
- System Power Supplies
- · Switching Power Supply Post Regulators
- · Battery Powered Equipment

PIN-OUT INFORMATION

- 1 VIN
- 2 VOUT
- 3 GROUND

ABSOLUTE MAXIMUM RATINGS

Input Voltage+6.5V	TST Storage Temperature Range65°C to +150°C
Power DissipationInternally Limited	TLD Lead Temperature
	(10 Seconds Soldering)300°C
	Tc Case Operating Temperature Range
	Power DissipationInternally Limited

MSK5251 Series....-40°C to +85°C MSK5251H Series-55°C to +125°C

ELECTRICAL SPECIFICATIONS

Parameter	Test Conditions ①③	Group A	MSK5251H SERIES			MSK5251 SERIES			Units
Parameter		Subgroup	Min.	Тур.	Max.	Min.	Тур.	Max.	Units
Input Voltage Range —	VOUT ≤ 1V	-	+3.0	-	±6.5	+3.0	-	±6.5	%
Input voltage Kange —	VOUT ≥ 1V	-	VOUT +2.1	-	±6.5	VOUT +2.1	-	±6.5	%
Outros and Outros and Outros	IL = 0mA	1	-	48	60	-	48	70	mA
Quiescent Current (2) —	IL = 5.0A	1	-	70	120	-	70	130	mA
Output Voltage Telerance	VIN = 5V; IOUT = 10mA	1	-	±0.5	±1.0	-	±0.5	±1.2	%
Output Voltage Tolerance		2B, 3	-	±1.0	±2.5	-	-	-	%
Dropout Voltage (2)	ΔVOUT = -1%; Ιουτ = 2.5mA	1	-	1.5	2.0	-	1.5	2.0	V
	Δ VOUT = -1%; IouT = 5mA	1	-	1.7	2.1	-	1.7	2.1	V
Lord Dogulation	10mA ≤ Iouт ≤ 5.0A	1	-	±0.2	±1.0	-	±0.2	±1.2	%
Load Regulation (8)		2, 3	-	±0.3	±2.0	-	±0.3	-	%
Line Regulation	VIN Min to +6V IOUT = 10mA	1	-	±0.05	±0.5	-	±0.05	±0.6	%
		2, 3	-	±0.5	±1.0	-	±0.5	-	%
Output Current Limit 28	VOUT = 0V	-	-	7.5	9.5	-	7.5	9.5	Α
Thermal Resistance 2	Junction to Case @ 125°C	-	-	1.6	2.0	-	1.6	2.2	°C/W
Thermal Shutdown	TJ	-	-	140	-	-	140	-	°C
Turn On Time 2	Соит = 10µF		-	10	45	-	10	55	μS

OUTPUT VOLTAGE
+0.8V
+0.9V
+1.0V
+1.2V
+1.3V
+1.4V
+1.5V

NOTES:

- Output decoupled to ground using 10μF ceramic capacitor unless otherwise specified.
- (2) This parameter is guaranteed by design but need not be tested. Typical parameters are representative of actual device performance but are for reference only.
- (3) All output parameters are tested using a low duty cycle pulse to maintain T_J = Tc.
- (4) Industrial grade devices shall be tested to subgroup 1 unless otherwise specified.
- (5) Military grade devices ("H" suffix) shall be 100% tested to subgroups 1, 2 and 3.
- (6) Subgroup 1 Tc = +25°C
 - Subgroup 2 $T_J = +125$ °C
 - Subgroup 2B Tc = +135°C
 - Subgroup 3 $T_A = -55^{\circ}C$
- (7) Please consult the factory if alternate output voltages are required.
- Due to internal thermal shutdown, maximum output current may not be available at all values of VIN VOUT and temperatures. See typical performance curves for clarification.
- Continuous operation at or above absolute maximum ratings may adversely effect the device performance and/or life cycle.

APPLICATION NOTES

REGULATOR PROTECTION

The MSK5251 series are high performance linear regulators for high current, low voltage applications requiring fast transient response. The devices are fully protected from damage due to fault conditions, offering constant current limiting and thermal shutdown. The thermal shutdown junction temperature is typically 140°C and is 100% tested to verify thermal shutdown occurs above 135°C.

INPUT SUPPLY VOLTAGE

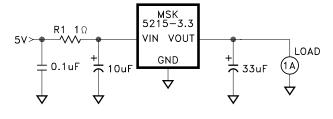
The input voltage must be maintained at a minimum of 3.0 volts for proper operation for devices with output voltage below 1.0 volt. With an output voltage of 1.0 volt or higher, the input voltage must be a minimum of 2.1 volts above the output.

LOAD CONNECTIONS

In voltage regulator applications where very large load currents are present, the load connection is very important. The path connecting the output of the regulator to the load must be extremely low impedance to avoid affecting the load regulation specifications. Any impedance in this path will form a voltage divider with the load.

MINIMIZING POWER DISSIPATION

To maximize the performance and reduce power dissipation of the MSK5251 series devices, Vin should be maintained as close to dropout as possible. See Input Supply Voltage requirements. A series resistor can be used to lower Vin close to the dropout specification, lowering the input to output voltage differential. In turn, this will decrease the power that the device is required to dissipate. Knowing peak current requirements and worst case voltages, a resistor can be selected that will drop a portion of the excess voltage and help to distribute the heating. The circuit below illustrates this method.



The maximum resistor value can be calculated from the following:

Where: VIN min = Minimum input voltage

VOUT max = Maximum output voltage across the full temperature range

VDROP = Worst case dropout voltage (Typically 2.1 Volts)

IOUT peak = Maximum load current

Quiescent Current = Max. quiescent current at lout peak

INPUT CAPACITOR

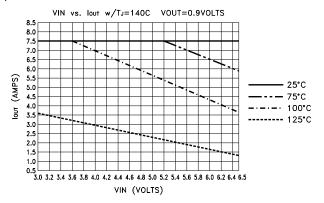
If the device is to be located more than 4 inches from the bulk supply capacitance, a minimum 1uF capacitor should be placed as close to the input pin as possible for proper bypassing. A smaller value capacitor such as 0.01uF should be placed in parallel with the larger value capacitor. Larger input capacitor values will help to improve ripple rejection.

OUTPUT CAPACITOR

The MSK5251 series devices require a minimum of external components to maintain stability. A minimum of output capacitance is necessary for stable operation. Due to the wide bandwidth design, the device will operate with a wide range of capacitance and ESR values. For most applications, a 10uF ceramic capacitor will suffice. Ideally, this should be an X7R ceramic capacitor or a tantalum capacitor due to their thermal performance. There is no upper limit to the amount of output capacitance that may be used.

THERMAL SHUTDOWN

The MSK5251 series of devices is equipped with a thermal shutdown circuit that will turn off the device when the junction temperature reaches approximately 140°C. It is important for the user to be aware that high temperature operation will limit the current capability of the device due to this thermal shutdown protection. In cases of maximum input voltage and high case temperature, the output current available may be less than 3 Amps. See curve below for clarification.



HEAT SINK SELECTION

To select a heat sink for the MSK5251, the following formula for convective heat flow may be used.

First, the power dissipation must be calculated as follows:

Power Dissipation = (VIN - VOUT) x lout x Quiescent Current

Next, the user must select a maximum junction temperature. The equation may now be arranged to solve for the required heat sink to ambient thermal resistance (R0sA).

Example:

An MSK5251-1.3 is configured for VIN=+3.4V and VOUT=+1.3V. Iout is a continuous 5Amp DC level. Under these conditions the maximum quiescent current would be 120mA. The ambient temperature is +25°C and the maximum junction temperature is 125°C.

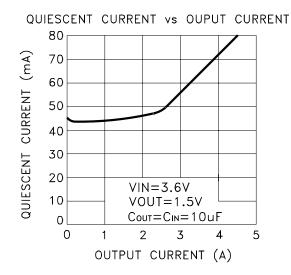
RθJC =
$$2.0$$
°C/W and RθCs = 0.5 °C/W typically.
Power Dissipation = $(3.4V - 1.3V) \times (5A) + (3.4 \times 120mA)$
= 10.9 Watts

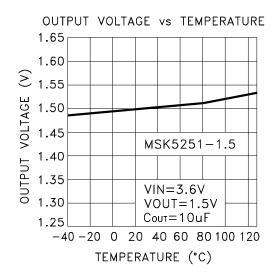
Solve for R₀SA:

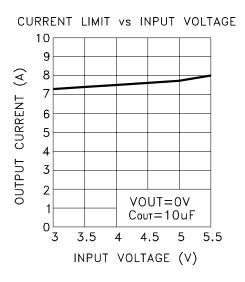
R
$$\theta$$
SA = $\left[\frac{125^{\circ}\text{C} - 25^{\circ}\text{C}}{10.9\text{W}}\right]$ -2.0°C/W - 0.5°C/W
= 6.67°C/W

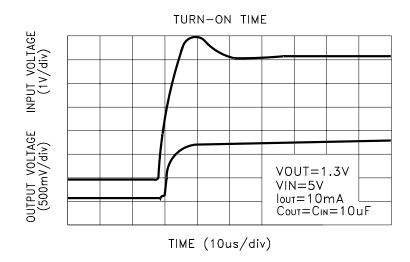
In this example, a heat sink with a thermal resistance of no more than 6.6°C/W must be used to maintain a junction temperature of no more than 125°C.

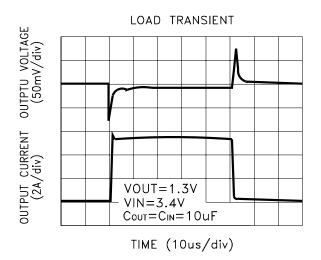
TYPICAL PERFORMANCE CURVES

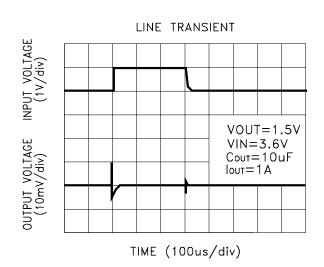




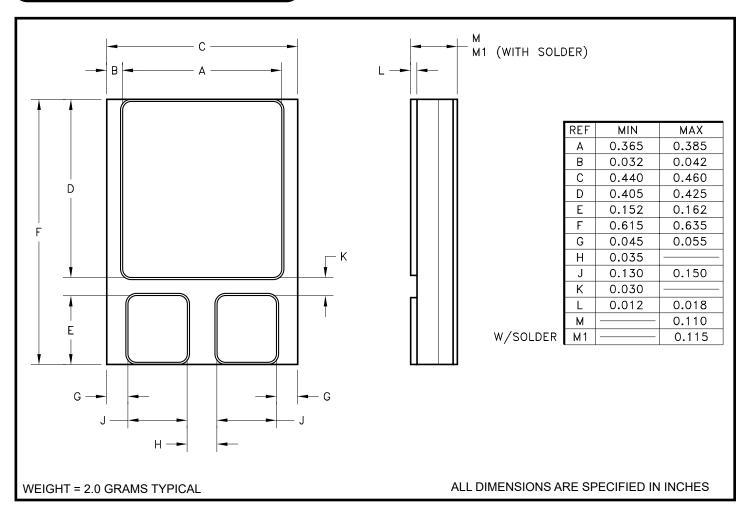




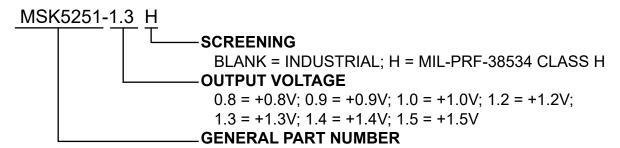




MECHANICAL SPECIFICATIONS



ORDERING INFORMATION



The above example is a +1.3V, Military regulator.

REVISION HISTORY

REV	STATUS	DATE	DESCRIPTION
I	Released	04/14	Add form #, clarify mechanical specifications and correct 1.2V part number
J	Released	04/23	Remove MIL-PRF-38535 and update company name and website

TTM Technologies

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