

# Xinger. III

Hybrid Coupler 3 dB, 90°



#### **Description:**

The X3C07F1-03S is a low profile, high performance 3dB hybrid coupler in a new easy to use, manufacturing friendly surface mount package. It is designed for AMPS, GSM, WCDMA and LTE band applications. The X3C07F1-03S is designed particularly for balanced power and low noise amplifiers, plus signal distribution and other applications where low insertion loss and tight amplitude and phase balance is required. It can be used in high power applications up to 25 watts.

Parts have been subjected to rigorous qualification testing and they are manufactured using materials with coefficients of thermal expansion (CTE) compatible with common substrates such as FR4, G-10, RF-35, RO4003 and polyimide. Produced with 6 of 6 RoHS compliant tin immersion finish.

#### **Detailed Electrical Specifications:**

Side View

Frequency	Isolation	Insertion Loss	VSWR	Amplitude Balance
MHz	dB Min	dB Max	Max : 1	dB Max
700-1000	23	0.25	1.15	± 0.60
600-900	23	0.17	1.15	± 0.70
695-805	26	0.15	1.12	± 0.35
731-881	26	0.17	1.12	± 0.50
758-960	23	0.25	1.15	± 0.45
Group Delay	Phase	Power	ΘJC	Operating Temp.
ns	Degrees	Avg. CW Watts at 95° C	°C/Watt	°C
0.24 ± 0.04	$90 \pm 4.0$	25	45.6	-55 to +140
0.24 ± 0.04	$90 \pm 3.0$	25	45.6	-55 to +140
$0.24 \pm 0.04$	$90 \pm 4.0$	25	45.6	-55 to +140
$0.24 \pm 0.04$	$90 \pm 4.0$	25	45.6	-55 to +140
$0.24 \pm 0.04$	90 ± 4.0	25	45.6	-55 to +140

\*Specification based on performance of unit properly installed on TTM Test Board with small signal applied.
\*\*Specifications are subject to change without notice. Refer to parameter definitions for details

#### Features:

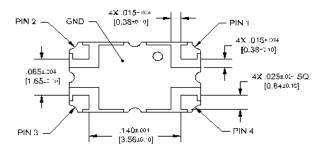
- 600-1000 MHz
- AMPS, GSM, WCDMA & LTE
- High Power
- Very Low Loss
- Tight Amplitude Balance
- High Isolation
- Production Friendly
- Tape and Reel
- Lead-Free

#### **Outline Drawing:**

.065...007 [1.6640.17] .200+::m PIN 1 -PIN 2 [3.08-073] X3C 125\_.00 ORIENTATION 07F1-03S [3.1840.25]MARY DENOTES PIN 1 RR CC PIN 3 DENOTES ARRAY PIN 4 ROW (RR) & COLUMN (CC) GND

Top View (Near-Side)

Dimensions are in Inches [Millimeters] X3C07F1-03S Mechanical Outline Battom View (Far-Side)



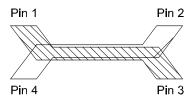
Tolerances are Non-Cumulative





## **Hybrid Coupler Pin Configuration**

The X3C07F1-03S has an orientation marker to denote Pin 1. Once port one has been identified the other ports are known automatically. Please see the chart below for clarification:

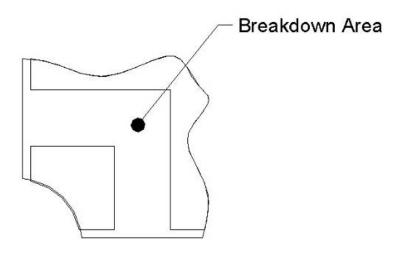


Configuration	Pin 1	Pin 2	Pin 3	Pin 4
Splitter	Input	Isolated	-3dB < Θ -90	3dB < Θ
Splitter	Isolated	Input	-3dB < Θ	-3dB < ⊖ -90
Splitter	-3dB < Θ -90	3dB < Θ	Input	Isolated
Splitter	3dB < Θ	-3dB < Θ -90	Isolated	Input
*Combiner	Α< Θ -90	Α< Θ	Isolated	Output
*Combiner	Α< Θ	A< Θ -90	Output	Isolated
*Combiner	Isolated	Output	Α< Θ -90	Α< Θ
*Combiner	Output	Isolated	Α< Θ	A< Θ -90

\*Note: "A" is the amplitude of the applied signals. When two quadrature signals with equal amplitudes are applied to the coupler as described in the table, they will combine at the output port. If the amplitudes are not equal, some of the applied energy will be directed to the isolated port.

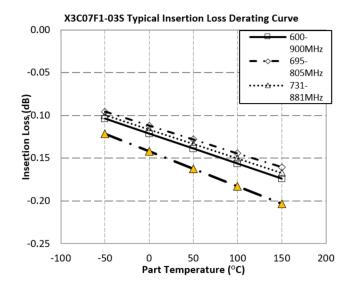
### **Peak Power Handling**

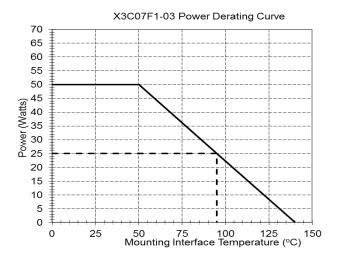
High-Pot testing of these couplers during the qualification procedure resulted in a minimum breakdown voltage of 1.37Kv (minimum recorded value). This voltage level corresponds to a breakdown resistance capable of handling at least 12dB peaks over average power levels, for very short durations. The breakdown location consistently occurred across the air interface at the coupler contact pads (see illustration below). The breakdown levels at these points will be affected by any contamination in the gap area around these pads. These areas must be kept clean for optimum performance. It is recommended that the user test for voltage breakdown under the maximum operating conditions and over worst case modulation induced power peaking. This evaluation should also include extreme environmental conditions (such as high humidity).





#### **Insertion Loss and Power Derating Curves**





#### **Insertion Loss Derating:**

The insertion loss, at a given frequency, of a group of couplers is measured at 25°C and then averaged. The measurements are performed under small signal conditions (i.e. using a Vector Network Analyzer). The process is repeated at 85°C and 150°C. A best-fit line for the measured data is computed and then plotted from -55°C to 150°C.

## **Power Derating:**

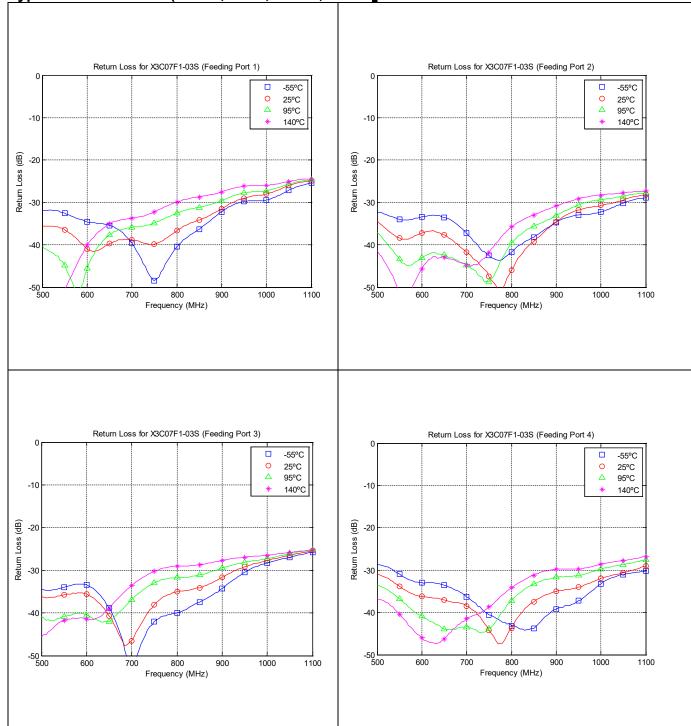
The power handling and corresponding power derating plots are a function of the thermal resistance, mounting surface temperature (base plate temperature), maximum continuous operating temperature of the coupler, and the thermal insertion loss. The thermal insertion loss is defined in the Power Handling section of the data sheet.

As the mounting interface temperature approaches the maximum continuous operating temperature, the power handling decreases to zero.

If mounting temperature is greater than 95°C, Xinger coupler will perform reliably as long as the input power is derated to the curve above.

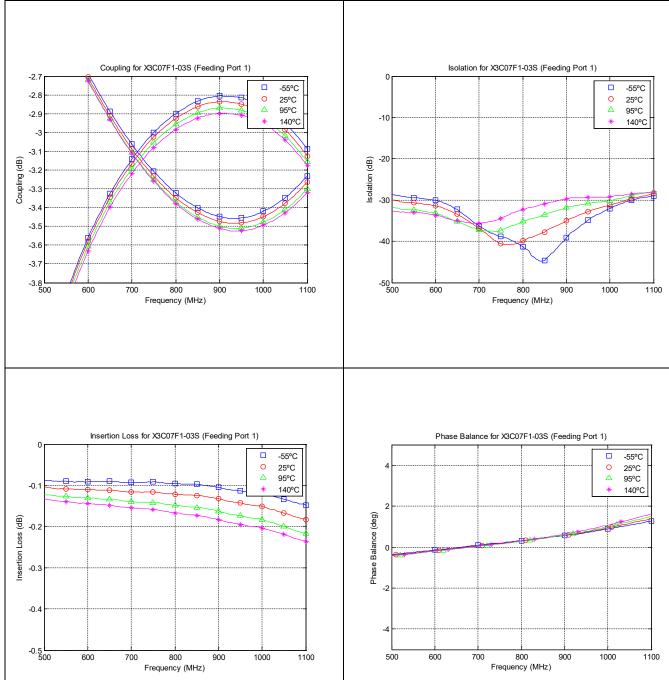


Typical Performance (-55°C, 25°C, 105°C, 140°C)





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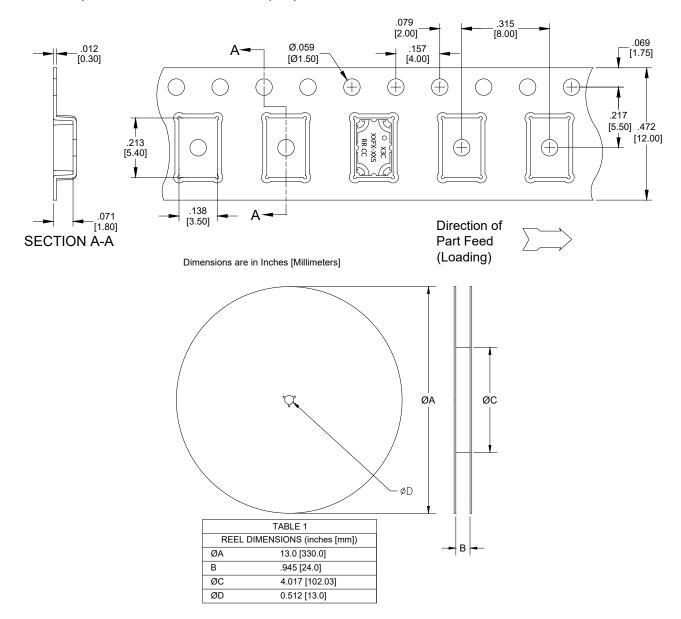
# **Definition of Measured Specifications**

Parameter	Definition	Mathematical Representation
VSWR (Voltage Standing Wave Ratio)	The impedance match of the coupler to a $50\Omega$ system. A VSWR of 1:1 is optimal.	$VSWR = rac{V_{max}}{V_{min}}$ Vmax = voltage maxima of a standing wave Vmin = voltage minima of a standing wave
Return Loss	The impedance match of the coupler to a $50\Omega$ system. Return Loss is an alternate means to express VSWR.	$Return \ Loss(dB) = 20log \ \frac{VSWR + 1}{VSWR - 1}$
Insertion Loss	The input power divided by the sum of the power at the two output ports.	Insertion Loss(dB) = $10log \frac{P_{in}}{P_{cpl} + P_{direct}}$
Isolation	The input power divided by the power at the isolated port.	$Isolation(dB) = 10log \frac{P_{in}}{P_{iso}}$
Phase Balance	The difference in phase angle between the two output ports.	Phase at coupled port – Phase at direct port
Amplitude Balance	The power at each output divided by the average power of the two outputs.	$10log \ \frac{P_{cpl}}{(P_{cpl} + P_{direct})/2} \ \text{and} \ 10log \ \frac{P_{direct}}{(P_{cpl} + P_{direct})/2}$



## **Packaging and Ordering Information:**

Parts are available in reels. Packaging follows EIA 481-D for reels. Parts are oriented in tape and reel as shown below. Tape and reel is available in 4000 pcs per reel.



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