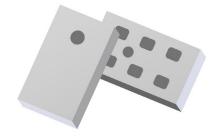




Ultra Small Low Profile 0603 Balun 50 Ω to 100 Ω Balanced



Description:

The X4BD70L1-50100G is an ultra-small (0603), low profile, broadband balanced to unbalanced transformer designed for differential inputs and output for 5G (n46,47) & 6G applications. X4BD70L1-50100G is ideal for high volume manufacturing. X4BD70L1-50100G is available on tape and reel for pick and place high volume manufacturing.

All of the Xinger components are constructed from ceramic filled PTFE composites, which possess excellent electrical and mechanical stability. All parts have been subjected to rigorous Xinger qualification testing and units are 100% RF tested. Produced in an ENIG final finish.

Electrical Specifications:

| 5000-9200 MHz 50 Ohm to 2 x 50 Ohm | Frequency | Port Impedance | Insertion Loss | Return Loss | Amplitude Imbalance |
|---------------------------------------|-----------|---------------------|-------------------|----------------------------|------------------------|
| 5G (n46,47) & 6G Applications | MHz | Unbalanced:Balanced | dB Max | dB Min | dB Max |
| Very Low Loss | 5800-8400 | 50:100 | 0.8 | 15 | ± 1 |
| Tight Amplitude Balance | 5000-9200 | 50:100 | 0.9 | 13 | ± 1 |
| Non-conductive Surface | | Phase | CMRR | Power | Operating |
| Production Friendly | | Imbalance | | | Temp. |
| RoHS Compliant Halogen Free | | Degrees | dB Min | Avg. CW Watts @105°C | °C |
| Tape and Reel | | 180 ± 10 | 20 | 1 | -55 to +140 |
| | | 180 ± 15 | 15 | 1 | -55 to +140 |

**Specification based on performance of unit properly installed on a TTM test board with small signal applied.

*Specifications subject to change without notice. Refer to parameter definitions for details.

TOP VIEW (NEAR SIDE) SIDE VIEW BOTTOM VIEW (FAR SIDE) 0.54±0.05 1.69±0.10 4x .34 1 2x .21 ł 1 0.52 0 94±0.10 6x .20 6 5 4 ORIENTATION MARK Dimensions are in Millimeters Denotes Pin1 Location 4x .50 6x .25 Tolerance are Non-Cumulative Designation Pin Unbalanced 1 2 DC Bias / GND Balanced Port 3 4 Balanced Port 5 GND 6 NC

Mechanical Outline:

Features:

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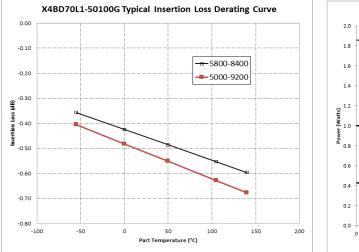
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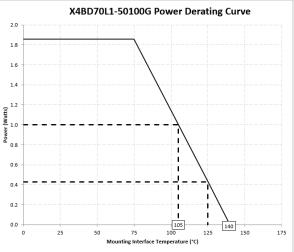
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Insertion Loss and Power Derating Curves:





Insertion Loss Derating:

The insertion loss, at a given frequency, of a group of Balun is measured at 25° C and then averaged. The measurements are performed under small signal conditions (i.e. using a Vector Network Analyzer). The process is repeated at -55°C and 140°C. A best-fit line for the measured data is computed and then plotted from -55°C to 140°C.

Power Derating:

The power handling and corresponding power derating plots are a function of the thermal resistance, mounting surface temperature (base plate temperature), maximum continuous operating temperature of the Balun, and the thermal insertion loss. The thermal insertion loss is defined in the Power Handling section of the data sheet.

As the mounting interface temperature approaches the maximum continuous operating temperature, the power handling decreases to zero.

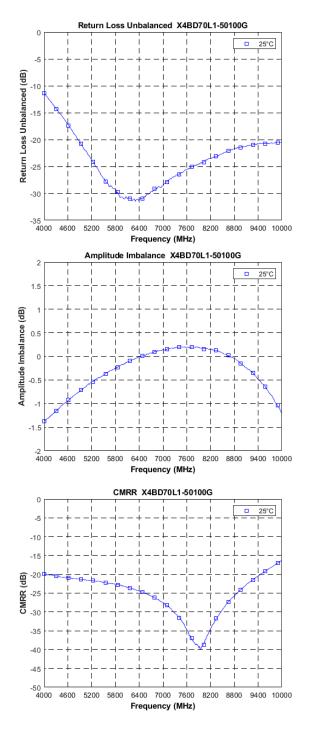
If mounting temperature is greater than 105°C, Xinger Balun will perform reliably as long as the input power is derated to the curve above.

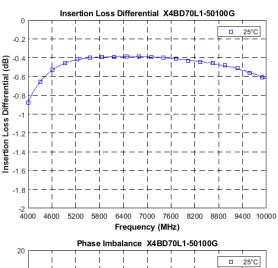
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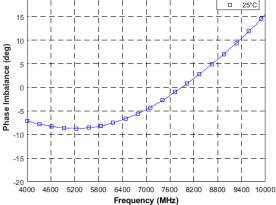
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Typical Performance: 4000 MHz to 10000 MHz







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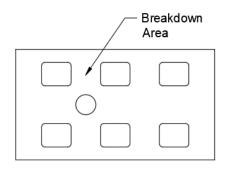
Definition of Measured Specifications:

| Parameter | Definition | Mathematical Representation |
|----------------------------------|---|---|
| Return Loss | The impedance match at the single ended port. | $RL = 20Log_{10}(S_{11})$ |
| Differential Port Return Loss | The impedance match at the differential port. | $RLD = 20Log_{10} 0.5 * (S_{22} - S_{23} - S_{32} + S_{33}) $ |
| Insertion Loss | Power loss from common mode to differential mode. | $ILD = 20Log_{10}(0.707 * (S_{21} - S_{31}))$ |
| Phase Imbalance | The difference in phase angle between the two differential ports, offset by 180 deg. | $PB = (Phase(S_{21}) - Phase(S_{31})) - 180^{\circ}$ |
| Amplitude Imbalance | The ratio of the power at differential ports. | $AB = 20Log_{10} \frac{S_{21}}{S_{31}} $ |
| Common Mode Rejection Ratio | The ratio of powers of the differential gain to the common-mode gain. | $CMRR = \pm 20Log_{10}(S_{21}+S_{31})/(S_{21}-S_{31})$ |

*100% RF test is performed per spec definition

Peak Power Handling:

High-Pot testing of these components during the qualification procedure resulted in a minimum breakdown voltage of 1Kv (minimum recorded value). This voltage level corresponds to a breakdown resistance capable of handling at least 12dB peaks over average power levels, for very short durations. The breakdown location consistently occurred across the pads and the ground bar (see illustration below). The breakdown levels at these points will be affected by any contamination in the gap area around these pads. These areas must be kept clean for optimum performance. It is recommended that the user test for voltage breakdown under the maximum operating conditions and over worst case modulation induced power peaking. This evaluation should also include extreme environmental conditions (such as high humidity).



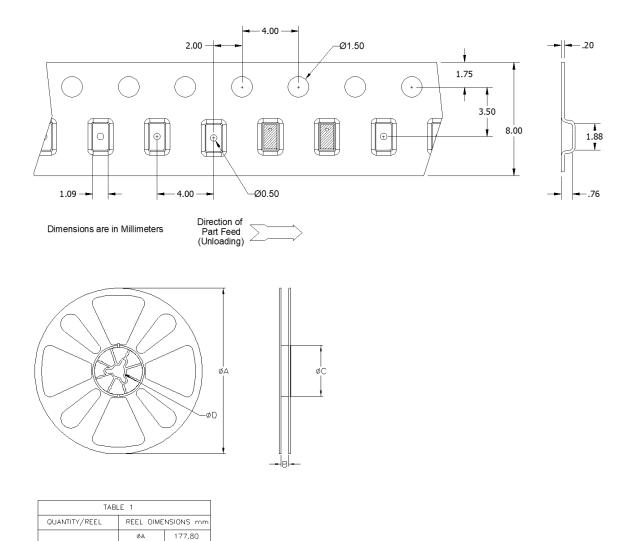
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Packaging and Ordering Information:

Parts are available in reel and are packaged per EIA 481. Parts are oriented in tape and reel as shown below. Minimum order quantities are 4000 per reel.



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8.00

50.80

13.00

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